SmartSwitch™



General Description

The AAT4618 SmartSwitch™ is a member of AnalogicTech's Application Specific Power MOS-FET™ (ASPM™) product family. It is a Current Limited P-channel MOSFET power switch designed for high-side load-switching applications operating with inputs ranging from 2.4V to 5.5V which makes it ideal for 2.5V, 3V and 5V systems. An integrated current-limiting circuit protects the input supply from large currents which potentially cause the supply to fall out of regulation. In addition, the AAT4618 thermal overload protection limits power dissipation and junction temperatures associated with prolonged periods of operation in current limit. Due to its low R_{DS(ON)}, the AAT4618 can be used to control loads that require up to factory preset 400mA, 500mA, or 1A. The ultra fast current-limit response to a sudden short circuit is a mere 400ns which reduces the requirements of local supply bypassing. An open drain FAULT flag signals an over current or over temperature condition after a 4ms timeout. Quiescent current is a low 10µA and in shutdown mode, the supply current decreases to less than 1µA.

The AAT4618 is available in a 5 pin SOT-23, and an 8 pin SC70JW specified over a -40 to 85°C temperature range.

The AAT4600 Series is a family of adjustable and fixed SmartSwitch™ products with a range of current handling capabilities. Available are single versions with adjustable current limit (AAT4610, AAT4601) as well as fixed current limit (AAT4625), and dual versions with fixed current limit (AAT4626, AAT4670).

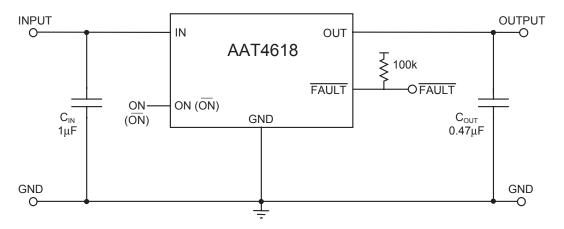
Features

- 2.4V to 5.5V Input voltage range
- Current limit trip threshold options:
 - 400mA min
 - 500mA min
 - 1A min
- Fast transient response:
 - 400ns response to short circuit
- Low quiescent current
 - 10µA typical
 - < 1µA with Switch off
- 125mΩ typical R_{DS(ON)}
- FAULT flag with 4ms blanking
- Undervoltage Lockout
- Thermal shutdown
- Temp range -40 to 85°C
- 4kV ESD Rating
- 5 pin SOT23 package
- 8 pin SC70JW package

Applications

- Peripheral ports
- **USB Ports**
- Notebook computers
- Personal communication devices
- Hot swap supplies

Typical Application



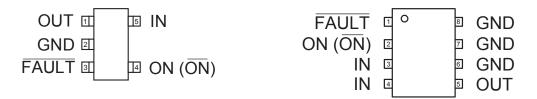


Pin Descriptions

| Pin # | | | |
|---------|----------|---------|---|
| SOT23-5 | SC70JW-8 | Symbol | Function |
| 1 | 5 | OUT | P-channel MOSFET drain |
| 2 | 6, 7, 8 | GND | Ground connection |
| 3 | 1 | FAULT | Open-drain FAULT output |
| 4 | 2 | ON (ON) | Enable Input: 4618 active low, 4618-1 active high |
| 5 | 3, 4 | IN | P-channel MOSFET source |

Pin Configuration

SOT23-5 SC70JW-8





Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

| Symbol | Description | Value | Units |
|------------------------|--|------------------------------|-------|
| V _{IN, FAULT} | IN, FAULT to GND | -0.3 to 6 | V |
| V_{ON} | ON (ON) to GND | -0.3 to V _{IN} +1 | V |
| V _{OUT} | OUT to GND | -0.3 to V _{IN} +0.3 | V |
| I _{OUT} | Output Current | Internally Limited | Α |
| T _J | Operating Junction Temperature Range | -40 to 150 | °C |
| V _{ESD} | ESD Rating ¹ —HBM | 4000 | V |
| T _{LEAD} | Maximum Soldering Temperature (at Leads) | 300 | °C |

Note: Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one Absolute Maximum rating should be applied at any one time.

Note 1: Human body model is a 100pF capacitor discharged through a 1.5k Ω resistor into each pin.

Thermal Characteristics

| Symbol | Description | Value | Units |
|---------------|---|-------|-------|
| Θ_{JA} | Maximum Thermal Resistance ² (SOT23-5, SC70JW-8) | 150 | °C/W |
| P_{D} | Maximum Power Dissipation ² (SOT23-5, SC70JW-8) | 667 | mW |

Note 2: Mounted on a demo board.

Electrical Characteristics ($V_{IN} = 5V$, $T_A = -40$ to 85° C unless otherwise noted. Typical values are at $T_A = 25^{\circ}$ C; **bold** values denote full temperature range)

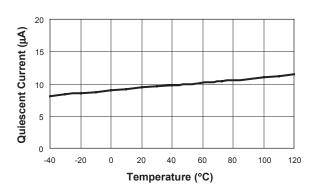
| Symbol | Description Conditions | | | Min | Тур | Max | Units |
|------------------------|---|---|--|------|------|------|--------|
| V _{IN} | Operation Voltage | | | 2.4 | | 5.5 | V |
| I_{Q} | Quiescent Current | V_{IN} = 5V, ON (ON) = | | | 10 | 25 | μA |
| I _{Q(OFF)} | Off Supply Current | ON (ON) = inactive, \ | V _{IN} = 5.5V | | | 1 | μA |
| I _{SD(OFF)} | Off Switch Current | $ON(\overline{ON}) = inactive, V$ | \overline{ON} (\overline{ON}) = inactive, V_{IN} = 5.5V, V_{OUT} = 0 | | 0.1 | 1 | μA |
| V _{UVLO} | Undervoltage Lockout | V _{IN} falling | | | 1.9 | | V |
| V _{UVLO(hys)} | Undervoltage Lockout Hysteresis | | | | 0.3 | | V |
| | On-Resistance | $V_{IN} = 5.0V, T_A = 25^{\circ}C$ | | | 125 | 175 | mΩ |
| R _{DS(ON)} | On-Resistance | V _{IN} = 3.0V, T _A = 25°C | | | 155 | 225 | |
| TC _{RDS} | ON-Resistance Temp-co | | | | 2800 | | ppm/°C |
| | | | AAT4618-0.4 | 400 | 500 | 600 | mA |
| I _{LIM} | Current Limit | V _{OUT} = V _{IN} - 0.5V | AAT4618-0.5 | 500 | 750 | 1000 | |
| | | | AAT4618-1.0 | 1000 | 1500 | 2000 | |
| V _{ON(L)} | ON (ON) Input Low Voltage | V _{IN} = 2.7V to 5.5V ³ | | | | 0.8 | V |
| 1 | ON (ON) Input High Voltage | V _{IN} = 2.7V to ≤ 4.2V ³ | | 2.0 | | | V |
| V _{ON(H)} | ON (ON) Input High Voltage | V _{IN} = > 4.2V to 5.0V ³ | | 2.4 | | | V |
| I _{ON(SINK)} | ON (ON) Input leakage | $V_{ON(\overline{ON})} = 5.5V$ | | | 0.01 | 1 | μA |
| t _{RESP} | Current Loop Response | V _{IN} = 5V | | | 400 | | ns |
| T _{OFF} | Turn-Off Time | $V_{IN} = 5V$, $R_L = 20\Omega$ | | | 5 | | μs |
| T _{ON} | Turn-On Time | $V_{IN} = 5V$, $R_L = 20\Omega$ | | | 1 | | μs |
| V _{FAULTLOW} | FAULT Logic Output Low | I _{SINK} = 1mA | | | | 0.4 | V |
| I _{SINK} | FAULT Logic Output High V _{FAULT} = 5.5V | | | | 0.5 | 1 | μA |
| | Leakage Current | | | | | | |
| T _{blank} | Fault Blanking Time | | | 4 | | ms | |
| T _{SD} | Over-temperature threshold | V _{IN} = 5V | | | 125 | | °C |

Note 3: For V_{IN} outside this range consult Typical ON (ON) Threshold curve.

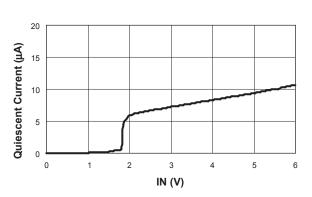


$\frac{\textbf{Typical Characteristics}}{(\text{Unless otherwise noted, V}_{\text{IN}} = 5\text{V}, T_{\text{A}} = 25^{\circ}\text{C})}$

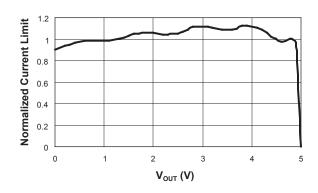
Quiescent Current vs. Temperature



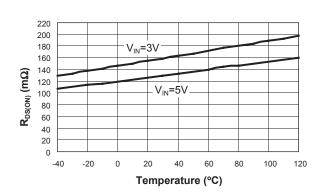
Quiescent Current vs. Input Voltage



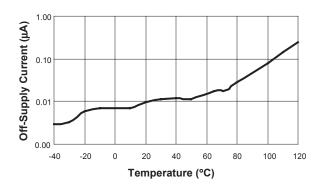
Normalized Current Limit vs. Vout



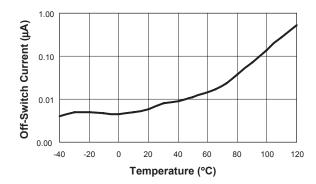
R_{DS(ON)} vs. Temperature



Off-Supply Current vs. Temperature



Off-Switch Current vs. Temperature

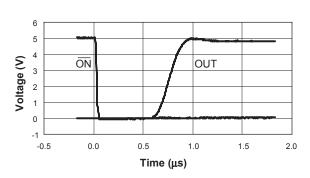




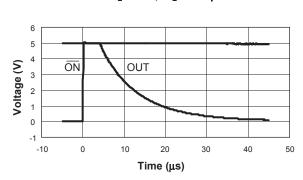
Typical Characteristics

(Unless otherwise noted, V_{IN} = 5V, T_A = 25°C)

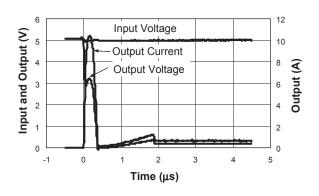
Turn-On R_L =20 Ω , C_L =0.47 μ F



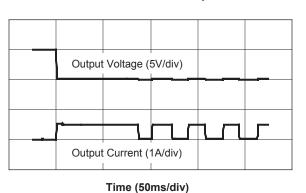
 $\begin{aligned} & \text{Turn-Off} \\ & \text{R}_{\text{L}}\text{=}20\Omega, \text{ C}_{\text{L}}\text{=}0.47\mu\text{F} \end{aligned}$



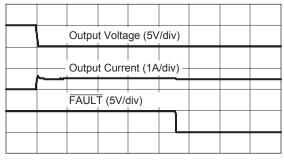
Short Circuit through 0.3 Ω



Thermal Shutdown Response

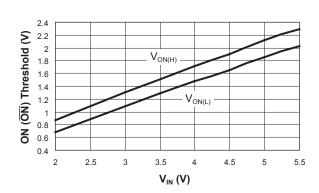


Fault Delay from Short Circuit



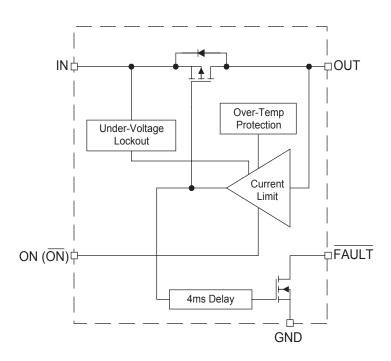
Time (1ms/div)

ON (ON) Threshold vs. VIN





Functional Block Diagram



Functional Description

The AAT4618 is a single channel current limited load switch that protects against short circuits by current limiting to a preset level. A patented fast acting current limit loop limits the current in a fraction of a micro second and instantly resets to low impedance once the short circuit condition is removed. The AAT4618 is internally protected from thermal damage by an over temperature detection circuit. If the die temperature reaches the internal thermal limit, the power device is switched off until the die temperature cools to a level below the thermal limit threshold. The device will thermal cycle indefinitely until the over current condition is removed. The AAT4618 operates with

input voltages ranging from 2.4V to 5.5V which, along with its extremely low operating current, makes it ideal for battery-powered applications. In cases where the input voltage drops below 2.4V, the AAT4618 MOSFET is protected from entering the saturated region of operation by being automatically shut down by an under voltage lockout circuit. A current limit condition is reported by the open drain FAULT output. A four millisecond blanking interval prevents false reporting during the charging of a capacitive load, which typically occurs during device turn-on, but may also occur during a port hot plug-in event.

The AAT4618 is ideally suited for protection of peripheral ports such as USB, RS232 and parallel ports.

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Applications Information

Input Capacitor

The input capacitor, C_{IN} protects the power supply from current transients generated by the load attached to the AAT4618. When a short circuit is suddenly applied to the output of the AAT4618, a large current, limited only by the $R_{DS(ON)}$ of the MOSFET, will flow for less than 1µs before the current limit circuitry activates. (See characteristic curve "Short Circuit Through 0.3Ω .") In this event, a moderately sized C_{IN} will dramatically reduce the voltage transient seen by the power supply and by other circuitry upstream from the AAT4618. The extremely fast short circuit response time of the AAT4618 reduces the size requirement of C_{IN} .

 C_{IN} should be located as close to the device VIN pin as practically possible. Ceramic, tantalum or aluminum electrolytic capacitors are appropriate for C_{IN} . There is no specific capacitor ESR requirement for C_{IN} . However, for higher current operation, ceramic capacitors are recommended for C_{IN} due to their inherent capability over tantalum capacitors to withstand input current surges from low impedance sources such as batteries in portable devices.

Output Capacitor

In order to insure stability while current limit is active, a small capacitance of approximately 0.47µF is required. No matter how large the output capacitor however, output current is limited to the value set by the AAT4618 current limiting circuitry, so very large output capacitors can be used.

For example, USB ports are specified to have at least 120µF of capacitance down stream from their controlling power switch. The current limiting circuit will allow an output capacitance of 1000µF or more without disturbing the upstream power supply.

ON (Enable Input)

In many systems, power planes are controlled by integrated circuits which run at lower voltages than the power plane itself. The enable input (ON) of the AAT4618 has low and high threshold voltages that accommodate this condition. The threshold voltages are compatible with 5 volt TTL, and 2.5 volt to 5 volt CMOS systems.

When switching the AAT4618 on into a capacitive load, the AAT4618 will charge the output capacitive load at a rate no greater than the current limit setting.

Attaching Loads

Capacitive loads attached to an enabled AAT4618 will charge at a rate no greater than the current limit setting.

FAULT Output

The FAULT flag is provided to alert the system if an AAT4618 load is not receiving sufficient voltage to operate properly. If current limit or over temperature circuits in any combination are active for more than approximately four milliseconds, the FAULT flag is pulled to ground through approximately 100Ω . The filtering of voltage or current transients of less than four milliseconds prevents capacitive loads connected to the AAT4618 output from activating the FAULT flag when they are initially attached. Pull up resistances of $1k\Omega$ to $100k\Omega$ are recommended. Since FAULT is an open drain terminal, it may be pulled up to any unrelated voltage less than the maximum operating voltage of 5.5V, allowing for level shifting between circuits.

Thermal Considerations

Since the AAT4618 has internal current limit and over temperature protection, junction temperature is rarely a concern. However, if the application requires large currents in a hot environment, it is possible that temperature rather than current limit will be the dominant regulating condition. In these applications, the maximum current available without risk of an over temperature condition must be calculated. The maximum internal temperature while current limit is not active can be calculated using Equation 1.

$$T_J(MAX) = I_{MAX}^2 \times R_{DS(ON)}(MAX) \times R_{\theta JA} + T_A(MAX)$$

In Equation 1, I_{MAX} is the maximum current required by the load. $R_{DS(ON)}(MAX)$ is the maximum rated $R_{DS(ON)}$ of the AAT4618 at high temperature. $R_{\theta JA}$ is the thermal resistance between the AAT4618 die and the board onto which it is mounted. $T_A(MAX)$ is the maximum temperature that the PCB under the AAT4618 would be if the AAT4618 were not dissipating power. Equation 1 can be rearranged to solve for I_{MAX} ; Equation 2.

$$I_{MAX} = \sqrt{\frac{T_{SD}(MIN) - T_{A}(MAX)}{R_{DS(ON)}(MAX) \times R_{\theta JA}}}$$



 $T_{SD}(MIN)$ is the minimum temperature required to activate the AAT4618 over temperature protection. With typical specification of 125°C, 115°C is a safe minimum value to use.

For example, if an application is specified to operate in 50°C environments, the PCB operates at temperatures as high as 85°C. The application is

sealed and its PCB is small, causing $R_{\theta JA}$ to be approximately 150 $^{\circ}\text{C/W}.$ Using Equation 2,

$$I_{MAX} = \sqrt{\frac{115 - 85}{220m \times 150}} = 950 \text{ mA}$$

The AAT4618 is safe to operate at the minimum current limit level of 500mA at 85° C.

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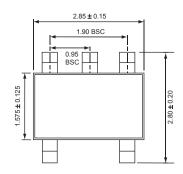
Ordering Information

| Package | Enable | Current Limit | Marking ¹ | Part Number (Tape and Reel) |
|----------|-------------|---------------|----------------------|-----------------------------|
| SOT23-5 | Active Low | 0.5 | AVXYY | AAT4618IGV-0.5-T1 |
| SOT23-5 | Active High | 0.5 | DWXYY | AAT4618IGV-0.5-1-T1 |
| SOT23-5 | Active Low | 1.0 | EVXYY | AAT4618IGV-1.0-T1 |
| SOT23-5 | Active High | 1.0 | EIXYY | AAT4618IGV-1.0-1-T1 |
| SC70JW-8 | Active Low | 0.4 | | AAT4618IJS-0.4-T1 |
| SC70JW-8 | Active High | 0.4 | | AAT4618IJS-0.4-1-T1 |
| SC70JW-8 | Active Low | 0.5 | AVXYY | AAT4618IJS-0.5-T1 |
| SC70JW-8 | Active High | 0.5 | DWXYY | AAT4618IJS-0.5-1-T1 |
| SC70JW-8 | Active Low | 1.0 | EVXYY | AAT4618IJS-1.0-T1 |
| SC70JW-8 | Active High | 1.0 | EIXYY | AAT4618IJS-1.0-1-T1 |

Note: Sample stock is generally held on all part numbers listed in ${\bf BOLD}.$ Note 1: XYY = assembly and date code.

Package Information

SOT23-5



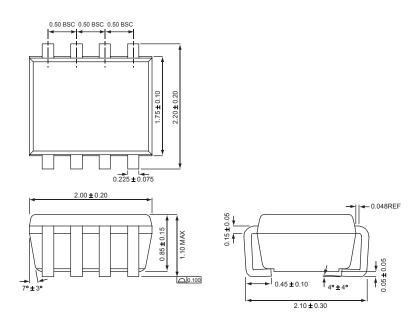




All dimensions in millimeters



SC70JW-8



All dimensions in millimeters.

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