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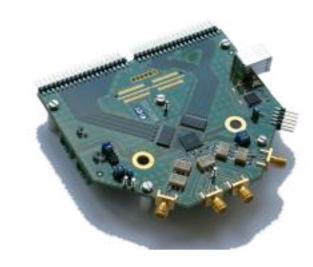**DOWNLOAD MANUAL - SOFTWARE** [ZIP]

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ADC1212D080F1
ADC1212D080F1 demo board; CMOS version; SPI and CMOS buffer on board

IDT's dual-channel ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

Features

- SMA connector for clock and analog inputs signal
- CMOS(buffered outputs)

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
ADC1212D080F1-DB	Active New	BD0	BOARD	0	C	N	Check Inventory

Documents

	TITLE	TYPE▲	FORMAT	FILE SIZE	DATE
🔒	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

Related Products

PRODUCT ID	TITLE
ADC1212D080HN	Dual 12-bit ADC; 80 Msps;

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