

SERIES: PEM2-D | **DESCRIPTION:** DC-DC CONVERTER

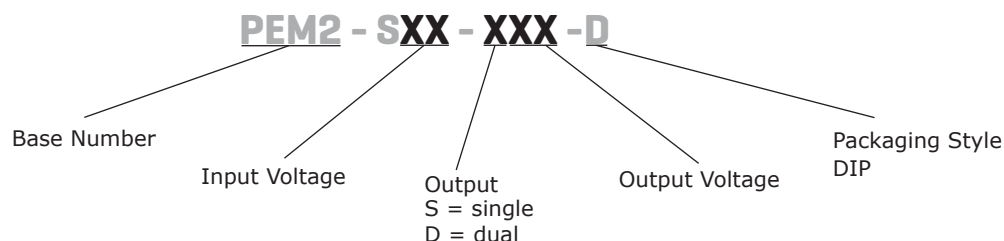
FEATURES

- 2 W isolated output
- smaller package
- single/dual unregulated output
- 3,000 Vdc isolation
- short circuit protection
- extended temperature range (-40~105°C)
- antistatic protection up to 8kV
- high efficiency at light load
- efficiency up to 85%



MODEL	input voltage		output voltage (Vdc)	output current		output power max (W)	ripple and noise ¹ typ (mVp-p)	efficiency typ (%)
	typ (Vdc)	range (Vdc)		min (mA)	max (mA)			
PEM2-S5-S5-D	5	4.5~5.5	5	40	400	2	60	85
PEM2-S5-S12-D	5	4.5~5.5	12	17	167	2	60	84
PEM2-S5-S15-D	5	4.5~5.5	15	13	133	2	75	84
PEM2-S5-D5-D	5	4.5~5.5	±5	±20	±200	2	60	80
PEM2-S5-D12-D	5	4.5~5.5	±12	±8	±83	2	60	84
PEM2-S5-D15-D	5	4.5~5.5	±15	±7	±67	2	75	84
PEM2-S12-S5-D	12	10.8~13.2	5	40	400	2	60	84
PEM2-S12-S15-D	12	10.8~13.2	15	13	133	2	60	84
PEM2-S12-D15-D	12	10.8~13.2	±15	±7	±67	2	75	84
PEM2-S15-D15-D	15	13.5~16.5	±15	±7	±67	2	75	85
PEM2-S24-S5-D	24	21.6~26.4	5	40	400	2	60	84
PEM2-S24-S9-D	24	21.6~26.4	9	22	222	2	60	84
PEM2-S24-S15-D	24	21.6~26.4	15	13	133	2	60	84
PEM2-S24-D12-D	24	21.6~26.4	±12	±8	±83	2	60	84
PEM2-S24-D15-D	24	21.6~26.4	±15	±7	±67	2	75	84

Notes: 1. ripple and noise are measured at 20 MHz BW by "parallel cable" method

PART NUMBER KEY


INPUT

parameter	conditions/description	min	typ	max	units
operating input voltage	5 V input models	4.5	5	5.5	Vdc
	12 V input models	10.8	12	13.2	Vdc
	15 V input models	13.5	15	16.5	Vdc
	24 V input models	21.6	24	26.4	Vdc
surge voltage	for maximum of 1 second				
	5 V input models	-0.7		9	Vdc
	12 V input models	-0.7		18	Vdc
	15 V input models	-0.7		21	Vdc
	24 V input models	-0.7		30	Vdc
filter	capacitance filter				

OUTPUT

parameter	conditions/description	min	typ	max	units
line regulation	for Vin change of 1%			±1.2	%
load regulation	measured from 10%~100% load				
	5 V output models		10		%
	9 V output models		9		%
	12 V output models		8		%
	15 V output models		7		%
voltage accuracy	see tolerance envelope curve				
switching frequency	100% load, nominal input voltage		100	300	kHz
temperature coefficient	at 100% load			±0.03	%/°C

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection	supply voltage must be removed at the end of the short circuit duration			1	s

SAFETY AND COMPLIANCE

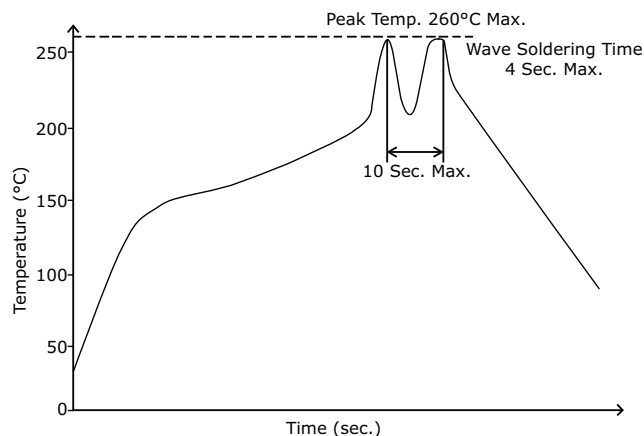
parameter	conditions/description	min	typ	max	units
isolation voltage	input to output for 1 minute, 1 mA max. leakage current	3,000			Vdc
isolation resistance	input to output at 500 Vdc	1,000			MΩ
conducted emissions	CISPR22/EN55022, class B, external circuit required				
radiated emissions	CISPR22/EN55022, class B, external circuit required				
ESD	IEC/EN61000-4-2, class B, contact ± 8kV for single outputs IEC/EN61000-4-2, class B, contact ± 6kV for dual outputs				
MTBF	as per MIL-HDBK-217F @ 25°C	3,500,000			hours
RoHS compliant	yes				

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		105	°C
storage temperature		-55		125	°C
storage humidity	non-condensing			95	%
temperature rise	Ta=25°C, 100% load		25		°C

SOLDERABILITY

parameter	conditions/description	min	typ	max	units
hand soldering	1.5 mm from case for 10 seconds			300	°C
wave soldering	see wave soldering profile			260	°C

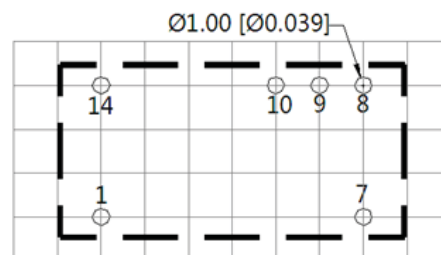
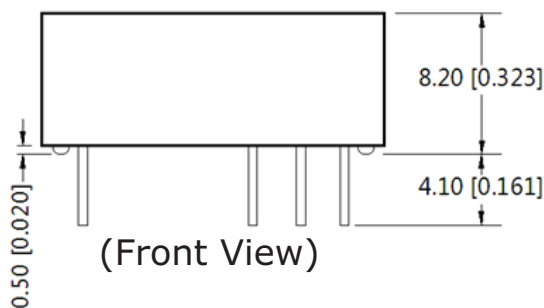


MECHANICAL

parameter	conditions/description	min	typ	max	units
dimensions	20 x 10 x 8.2 (0.787 x 0.394 x 0.323 inch)				mm
case material	plastic (UL94-V0)				
weight			2.4		g

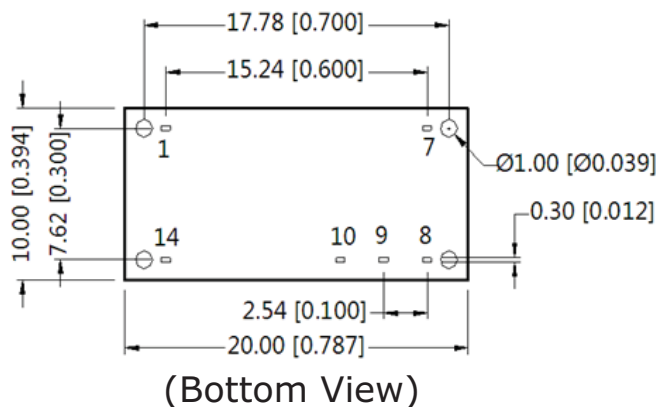
MECHANICAL DRAWING

units: mm[inch]

tolerance: $\pm 0.25[\pm 0.010]$ pin section tolerance: $\pm 0.10[\pm 0.004]$ 

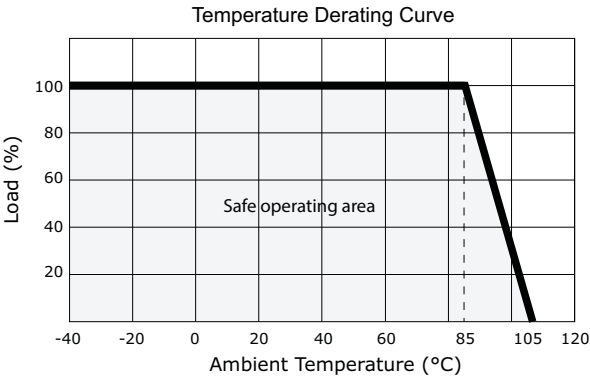
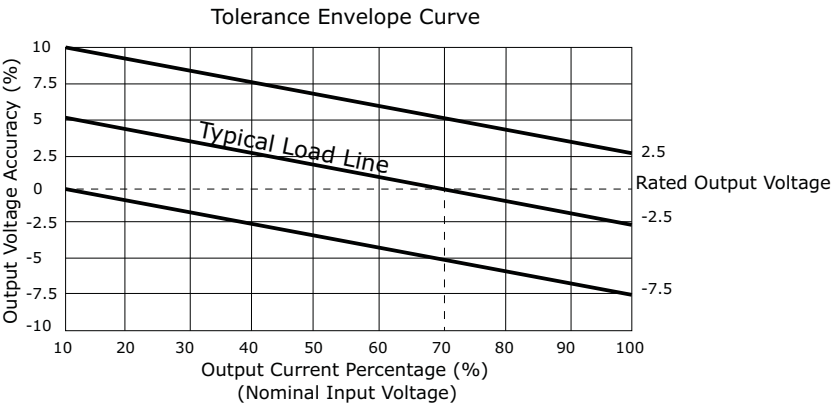
(Top View)

PCB Layout



PIN CONNECTIONS		
PIN	Single Output	Dual Output
1	GND	GND
7	NC	NC
8	+Vo	+Vo
9	NO PIN	0V
10	0V	-Vo
14	Vin	Vin

DERATING CURVES



EMC RECOMMENDED CIRCUIT

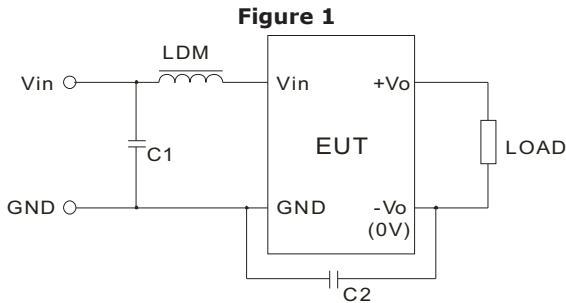


Table 1

Recommended external circuit components

Vin (Vdc)	C1	C2	LDM
5	4.7μF/50V	-	6.8μH
12	4.7μF/50V	470pF/3kV	6.8μH
15	4.7μF/50V	470pF/3kV	6.8μH
24	4.7μF/50V	470pF/3kV	6.8μH

TEST CONFIGURATION

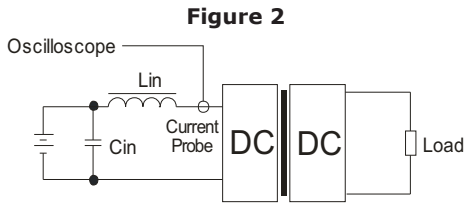


Table 2

External components

Lin	4.7μH
Cin	220μF, ESR < 1.0Ω at 100 KHz

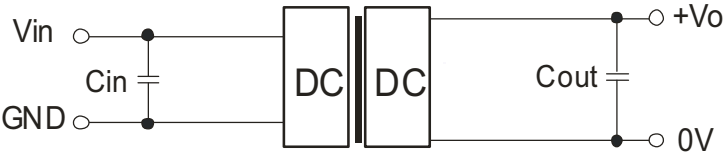
Note: 1. Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.

APPLICATION NOTES

1. **Output load requirement**
To ensure this module can operate efficiently and reliably, the minimum output load may not be less than 10% of the full load during operation. If the actual output power is low, connect a resistor at the output end in parallel to increase the load.
2. **Overload Protection**
Under normal operating conditions, the output circuit of this product has no protection against overload. The simplest method to add this is to add a circuit breaker to the circuit.
3. **Recommended circuit**
If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR(see Figure 3 & Table 3). However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see Table 4).

Figure 3

Single Output



Dual Output

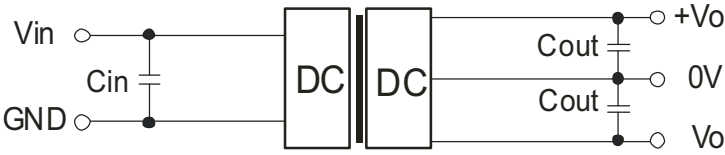


Table 3

Vin (Vdc)	Cin (μF)	Single Vo (Vdc)	Cout (μF)	Dual Vo (Vdc)	Cout (μF)
5	4.7	5	10	±5	4.7
12	2.2	9	4.7	±12	1
15	2.2	12	2.2	±15	0.47
24	1	15	1	--	--

Note: It's not recommended to connect any external capacitors in applications with less than 0.5 watt output.

Table 4

Single Vout (Vdc)	Max. Capacitive Load (μF)	Dual Vout (Vdc)	Max. Capacitive Load ¹ (μF)
5	220	5	100
9	220	--	--
12	220	12	100
15	220	15	100

Note: 1. For each output.

Note:

1. Operation under minimum load will not damage the converter; however, they may not meet all specifications listed.

2. Max. capacitive load tested at input voltage range and full load.

3. All specifications measured at: Ta=25°C, humidity<75%, nominal input voltage and rated output load, unless otherwise specified.

REVISION HISTORY

rev.	description	date
1.0	initial release	08/30/2013

The revision history provided is for informational purposes only and is believed to be accurate.

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