AN1339 (AN6912N), AN1339S

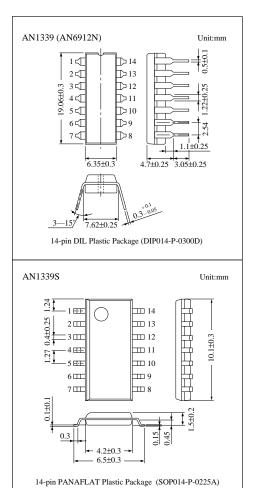
Quadruple Comparators

Overview

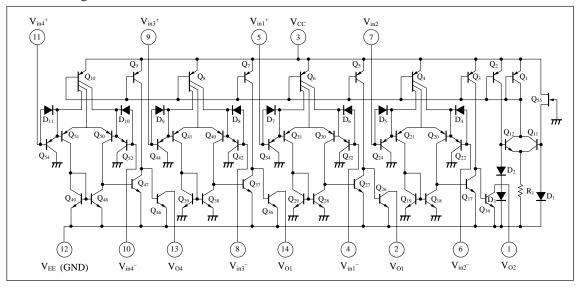
The AN1339 (AN6912N) and the AN1339S are quadruple (voltage) comparators with wide range of operating supply voltages.

■ Features

- Wide range of supply voltage Single supply:2 to 36V Dual supply:±1 to ±18V
- Low circuit current:0.8mA typ.
- Wide range of common-mode input voltages :0V to Vcc-1.5V (single supply)
- Open collector output



■ Block Diagram



■ Pin Descriptions

Pin No.	Pin name	Pin No.	Pin name
1	Ch.2 output	8	Ch.3 inverting input
2	Ch.1 output	9	Ch.3 non inverting input
3	V_{CC}	10	Ch.4 inverting input
4	Ch.1 inverting input	11	Ch.4 non inverting input
5	Ch.1 non inverting input	12	V _{EE} (GND)
6	Ch.2 inverting input	13	Ch.4 output
7	Ch.2 non inverting input	14	Ch.3 output

■ Absolute Maximum Ratings (Ta=25°C)

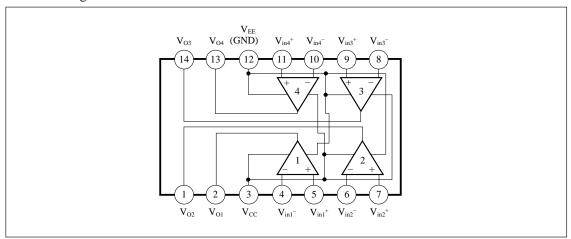
Pa	rameter	Symbol	Rating	Unit	
	Supply voltage	V_{CC}	36	V	
Voltage	Common-mode input voltage	$V_{ICM} *_1$	−0.3 to +36	V	
	Differential input voltage	V _{ID} *2	36	V	
D	AN1339 (AN6912N)	P_{D}	570		
Power dissipation	AN1339S		380	mW	
Operating ambient temperature		T_{opr}	-30 to +85	°C	
Storage	AN1339 (AN6912N)	$ T_{ m stg}$	-55 to +150	°C	
temperature	AN1339S		-55 to +125	C	

^{*1} The common mode input voltage is a voltage applied to the non-inverting input pin and inverting input pin simultaneously.
*2 Differential input is equivalent to the potential difference between the non-inverting input pin and inverting input pin.

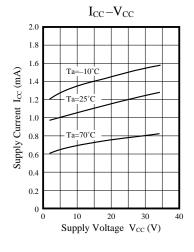
■ Electrical Characteristics (V_{CC}=5V, Ta=25±2°C)

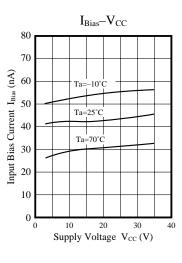
Parameter	Symbol	Condition	min	typ	max	Unit
Input offset voltage	V _{I (offset)}			2	5	mV
Input offset current	I _{IO}				50	nA
Input bias current	I_{Bias}				250	nA
Voltage gain	Gv	$R_L=15k\Omega$		200		V/mV
Common-mode input voltage range	V _{CM}		0		V _{CC} -1.5	V
Supply current	I_{CC}	R _L = ∞		1.1	2	mA
Response time	t _r	$R_L=5.1k\Omega, V_{RL}=5V$		1.3		μs
Output sink current	I _{SINK}	$V_{REF}=0V, V_{IN}=1V, V_{O} \le 1.5V$	10		_	mA
Low-level output voltage	V _{OL}	V _{REF} =0V, V _{IN} =1V, I _{SINK} =3mA		0.2	0.4	V
Output terminal leakage current	I _{O (Leak)}	$V_{IN}=0V, V_{REF}=1V, V_{O}=5V$		0.1		nA

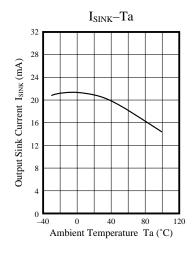
■ Block Diagram

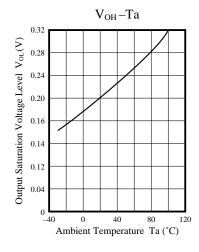


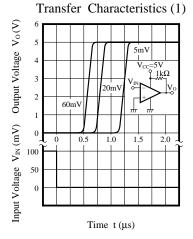
■ Characteristics Curve

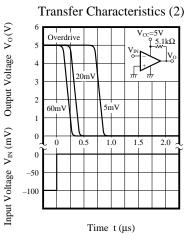












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