TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TA2055F**

## FILTER IC FOR $\Sigma$ - $\Delta$ MODULATION SYSTEM DA CONVERTER

TA2055F is an analog filter IC for  $\Sigma$ - $\Delta$  modulation system DA converter.

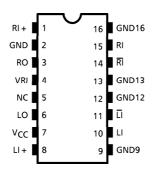
Using the TA2055F in combination the TC9237BF, TC9270F (the  $\Sigma$ - $\Delta$  modulation system DA converter with a built-in digital filter), it is possible to construct a DA conversion system with less external parts.

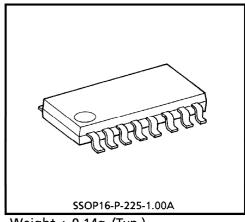
#### **FEATURES**

- Built-in CR for LPFs and output (differential) amplifiers for the left and right channel.
- Single power supply operation.
- Noise distortion factor and S/N ratio are as follows (when operating at +5V single power supply):

Noise distortion factor : -93dB (Typ.) S/N : 100dB (Typ.)

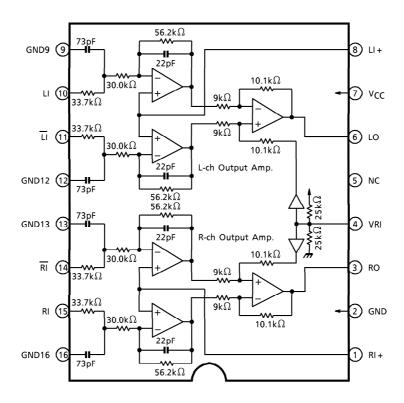
#### PIN CONNECTION (Top view)





Weight: 0.14g (Typ.)

## **BLOCK DIAGRAM**



## **DESCRIPTION OF PIN FUNCTIONS**

PIN No.	SYMBOL	1/0	FUNCTION & OPERATION	REMARKS		
1	RI+	I	R channel operational amplifier forward input pin. Connect to VRI.	_		
2	GND	_	Ground pin.	_		
3	RO	0	R channel analog output pin.	_		
4	VRI	_	Reference voltage pin. (V <sub>CC</sub> / 2) See the block diagram			
5	NC	_	Non-connecting pin. NC pin is used in the open state. —			
6	LO	0	L channel analog output pin. —			
7	Vcc	_	Supply voltage pin.	_		
8	LI +	ı	L channel operational amplifier forward input pin. Connect to VRI.	_		
9	GND9		Ground pin for L channel reverse input side filter.	_		
10	LI	ı	L channel forward input pin.	Connect to LO of TC9270F		
11	ĪĪ	Ì	L channel reverse input pin.	Connect to $\overline{\text{LO}}$ of TC9270F		
12	GND12	_	Ground pin for L channel forward input side filter. —			
13	GND13	_	Ground pin for R channel forward input side filter. —			
14	RI	İ	R channel reverse input pin.  Connect to RO of TC9270F			
15	RI	ĺ	R channel forward input pin.  Connect to RO of TC9270F			
16	GND16	_	Ground pin for R channel reverse input side filter. —			

## **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	11	V
Power Dissipation	PD	350 (*)	mW
Operating Temperature	T <sub>opr</sub>	<b>- 35∼85</b>	°C
Storage Temperature	T <sub>stg</sub>	<b>- 55∼150</b>	°C

(\*) Reduce  $2.8 \text{mW}/^{\circ}\text{C}$  at  $Ta = above 25^{\circ}\text{C}$ .

## **ELECTRICAL CHARACTERISTICS** (Unless otherwise specified, V<sub>CC</sub> = 5V, Ta = 25°C)

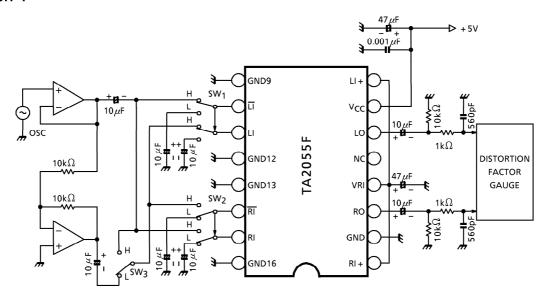
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Supply Voltage	V <sub>CC</sub>	_	Ta = −35~85°C	4.5	5.0	10	٧
Operating Supply	I <sub>CCQ</sub> (1)		At no signal	7.5	10.0	12.5	m A
Current	I <sub>CCQ</sub> (2)	] —	At no signal, $V_{CC} = 10V$	8.2	11.0	13.8	mA
Reference Voltage	VRI	_	_	2.45	2.50	2.55	٧
	THD (1)		$1kHz$ , $V_0 = 950mV_{rms}$	_	- 93	- 90	
Noise Distortion Factor	THD (2)	1	$10kHz$ , $V_0 = 950mV_{rms}$	_	- 93	- 90	dB
	THD (3)		1kHz, $V_0 = 95 \text{mV}_{rms}$	_	- 78	<b>-</b> 75	
Cross Talk	CT	1	$1kHz$ , $V_0 = 950mV_{rms}$	_	- 100	- 95	dB
Attenuation	ATT (1)	1	$40kHz$ , $V_0 = -10dBV_{rms}$	0.51	0.71	1.41	dB
Attenuation	ATT (2)	] '	80kHz, $V_0 = -10$ dB $V_{rms}$	1.50	2.70	4.50	ub
Max. Output Level	V <sub>omax</sub>	1	1kHz, THD = 1%	1.20	1.25	_	V <sub>rms</sub>
Differential Balance	G <sub>VB</sub>	1	1kHz, 1.1dBV <sub>rms</sub> In-phase input	_	_	- 40	dB
LR Output Difference	G <sub>VD</sub>	1	1kHz, 1.1dBV <sub>rms</sub> Differential input	_	0	0.5	dB

(Note 1) When the TC9270F and +5V single power supply are operated : Full scale =  $950mV_{rms}$  (Typ.).

(Note 2) The amount of attenuations is based on 1 kHz,  $V_0 = -10 dBV_{rms}$ .

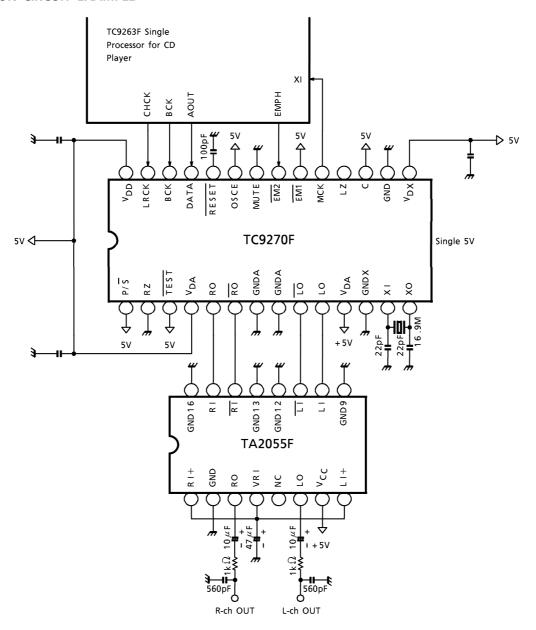
(Note 3) Measuring circuit-1: indicates the measuring circuit.

## **TEST CIRCUIT-1**



sw <sub>1</sub>	sw <sub>2</sub>	sw <sub>3</sub>	MEASURING ITEM
L	L	_	Operating supply voltage, Reference voltage
L	Н	L	Cross talk (R→L)
Н	L	L	Cross talk (L→R)
Н	Н	L	Noise distortion factor, Attenuation, Maximum output level, LR output difference
Н	Н	Н	Difference balance

## **APPLICATION CIRCUIT EXAMPLE**



#### (Cautions)

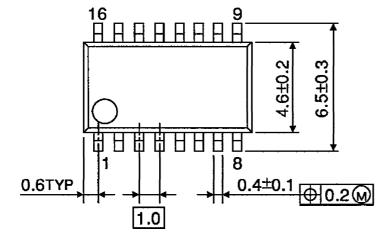
- Quality of crystal oscillation waveform largely effects S/N ratio.

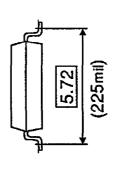
  Further, this is also true when system clock is input externally through the XI pin of pin<sup>®</sup>.
- Suppress glitch of input signals (LRCK, BCK, DATA) as could as possible.
- The wiring between the TC9270F output and the analog filter amplifier input must be made the shortest
- The capacitor between V<sub>DA</sub> and GNDA shall be connected as close to the pin as possible.
- NC pin is used in the open state.

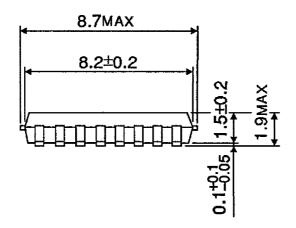
Unit: mm

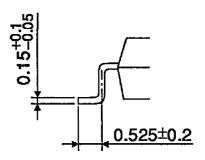
## **PACKAGE DIMENSIONS**

SSOP16-P-225-1.00A









Weight: 0.14g (Typ.)

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