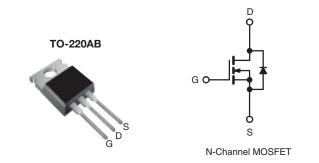


## Power MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	60	60				
$R_{DS(on)}(\Omega)$	V <sub>GS</sub> = 10 V	0.050				
Q <sub>g</sub> (Max.) (nC)	46	46				
Q <sub>gs</sub> (nC)	11	11				
Q <sub>gd</sub> (nC)	22					
Configuration	Single					



### **FEATURES**

- Dynamic dV/dt Rating
- 175 °C Operating Temperature
- Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC



#### **DESCRIPTION**

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION			
Package	TO-220AB		
Load (Dh) from	IRFZ34PbF		
Lead (Pb)-free	SiHFZ34-E3		
SnPb	IRFZ34		
JIII D	SiHFZ34		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V <sub>DS</sub>	60	,,	
Gate-Source Voltage			V <sub>GS</sub>	± 20	- V	
Continuous Drain Current	V <sub>GS</sub> at 10 V	$T_{\rm C} = 25 ^{\circ}{\rm C}$ $T_{\rm C} = 100 ^{\circ}{\rm C}$		30	А	
Continuous Drain Current		T <sub>C</sub> = 100 °C	I <sub>D</sub>	21		
Pulsed Drain Current <sup>a</sup>			I <sub>DM</sub>	120	-	
Linear Derating Factor				0.59	W/°C	
Single Pulse Avalanche Energy <sup>b</sup>			E <sub>AS</sub>	200	mJ	
Maximum Power Dissipation	mum Power Dissipation T <sub>C</sub> = 25 °C			88	W	
Peak Diode Recovery dV/dtc	dV/dt	4.5	V/ns			
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C	
Soldering Recommendations (Peak Temperature)	for	10 s		300 <sup>d</sup>	1	
Mounting Torque	6-32 or M3 screw			10	lbf ⋅ in	
Mounting rorque				1.1	N⋅m	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b.  $V_{DD} = 25 \text{ V}$ , starting  $T_J = 25 \,^{\circ}\text{C}$ ,  $L = 259 \,\mu\text{H}$ ,  $R_g = 25 \,\Omega$ ,  $I_{AS} = 30 \,\text{A}$  (see fig. 12).
- c.  $I_{SD} \le 30$  A,  $dI/dt \le 200$  A/ $\mu$ s,  $V_{DD} \le V_{DS}$ ,  $T_{J} \le 175$  °C.
- d. 1.6 mm from case.

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYP.	MAX.	UNIT		
Maximum Junction-to-Ambient	$R_{thJA}$	-	62			
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>	0.50	-	°C/W		
Maximum Junction-to-Case (Drain)	$R_{thJC}$	-	1.7			

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		60	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Referenc	e to 25 °C, I <sub>D</sub> = 1 mA	-	0.065	-	V/°C
Gate-Source Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =	$V_{GS}$ , $I_{D} = 250 \mu A$	2.0	-	4.0	V
Gate-Source Leakage	$I_{GSS}$		$V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		= 60 V, V <sub>GS</sub> = 0 V	-	-	25	μА
ű			V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C	-	-	250	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 18 A <sup>b</sup>	-	-	0.050	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub>	= 25 V, I <sub>D</sub> = 18 A	9.3	-	-	S
Dynamic		1		I	l	Ι	1
Input Capacitance	C <sub>iss</sub>	4	$V_{GS} = 0 V$ ,		1200	-	
Output Capacitance	C <sub>oss</sub>	f – 1	V <sub>DS</sub> = 25 V, 0 MHz, see fig. 5	-	600	-	pF
Reverse Transfer Capacitance	$C_{rss}$	1 – 1.	T = 1.0 MHz, see fig. 5		100	-	
Total Gate Charge	$Q_g$			-	-	46	
Gate-Source Charge	$Q_{gs}$	V <sub>GS</sub> = 10 V	$I_D = 30 \text{ A}, V_{DS} = 48 \text{ V},$ see fig. 6 and $13^b$	-	-	11	nC
Gate-Drain Charge	$Q_{gd}$		-	-	-	22	
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DD}$ = 30 V, $I_D$ = 30 A, $R_g$ = 12 Ω, $R_D$ = 1.0 Ω, see fig. 10 <sup>b</sup>		-	13	-	- ns
Rise Time	t <sub>r</sub>			-	100	-	
Turn-Off Delay Time	t <sub>d(off)</sub>			-	29	-	
Fall Time	t <sub>f</sub>			-	52	-	
Internal Drain Inductance	L <sub>D</sub>	6 mm (0.25") f	Between lead, 6 mm (0.25") from		4.5	-	-11
Internal Source Inductance	L <sub>S</sub>	package and center of die contact		-	7.5	-	- nH
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	-	30	A
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			-	-	120	
Body Diode Voltage	$V_{SD}$	$T_J = 25  ^{\circ}\text{C},  I_S = 30  \text{A},  V_{GS} = 0  \text{V}^{\text{b}}$		-	-	1.6	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C, I <sub>F</sub> = 30 A, dl/dt = 100 A/μs		-	120	230	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			-	0.7	1.4	nC
Forward Turn-On Time	t <sub>on</sub>	Intrincia tu	rn-on is dominated by L <sub>S</sub> and L <sub>D</sub> )				

#### **Notes**

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %.



102

101

10-1

D. Drain Current (Amps)

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

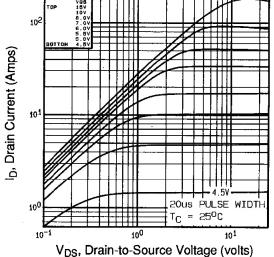
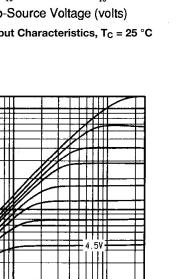


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C



20us PULSE 175°C

V<sub>DS</sub>, Drain-to-Source Voltage (volts) Fig. 2 - Typical Output Characteristics,  $T_C = 175$  °C

100

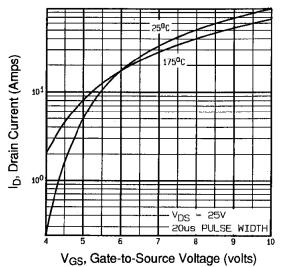


Fig. 3 - Typical Transfer Characteristics

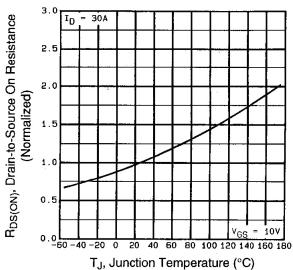


Fig. 4 - Normalized On-Resistance vs. Temperature



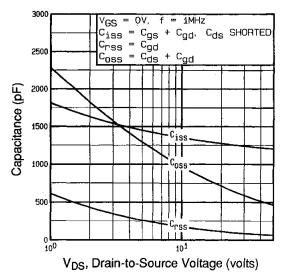


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

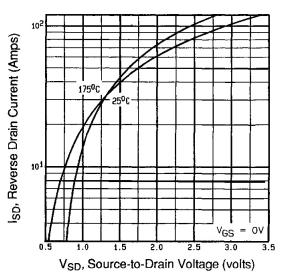


Fig. 7 - Typical Source-Drain Diode Forward Voltage

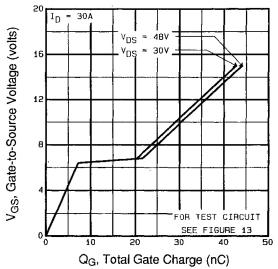


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

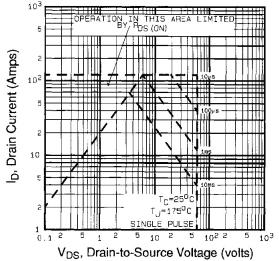


Fig. 8 - Maximum Safe Operating Area





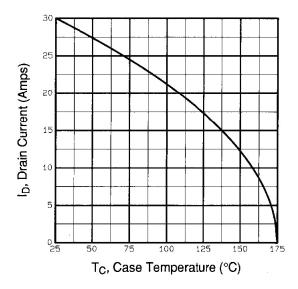


Fig. 9 - Maximum Drain Current vs. Case Temperature

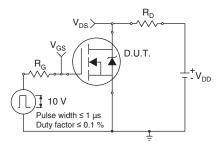


Fig. 10a - Switching Time Test Circuit

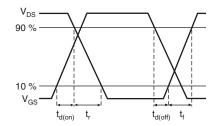


Fig. 10b - Switching Time Waveforms

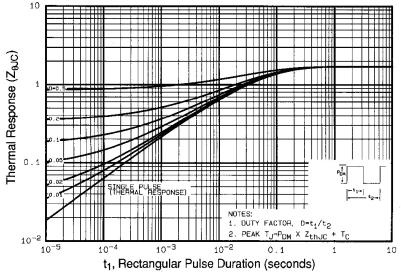
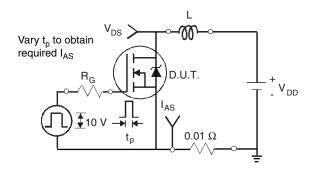


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case





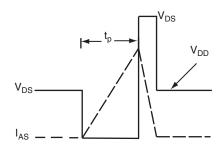


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

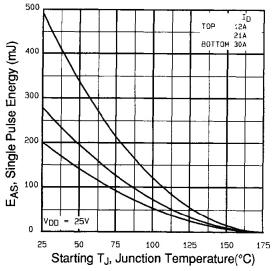


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

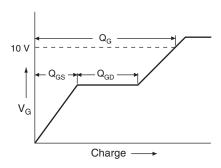


Fig. 13a - Basic Gate Charge Waveform

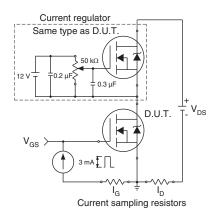
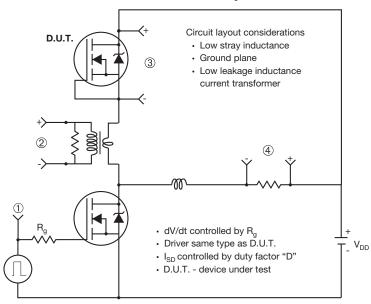


Fig. 13b - Gate Charge Test



### Peak Diode Recovery dV/dt Test Circuit



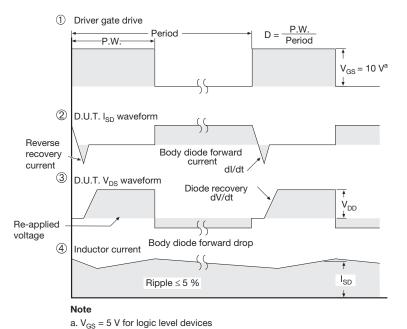


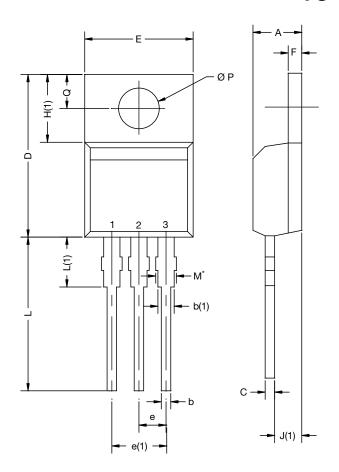
Fig. 14 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?91290.





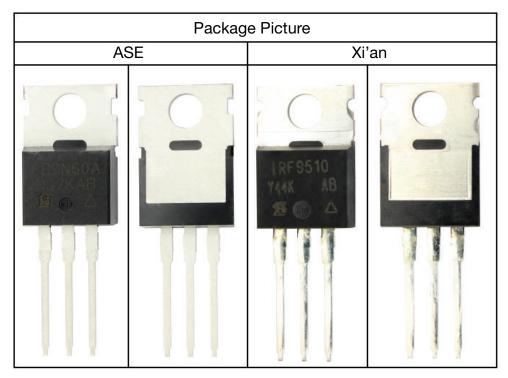
# TO-220-1



DIM.	MILLIM	IETERS	INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
Α	4.24	4.65	0.167	0.183		
b	0.69	1.02	0.027	0.040		
b(1)	1.14	1.78	0.045	0.070		
С	0.36	0.61	0.014	0.024		
D	14.33	15.85	0.564	0.624		
Е	9.96	10.52	0.392	0.414		
е	2.41	2.67	0.095	0.105		
e(1)	4.88	5.28	0.192	0.208		
F	1.14	1.40	0.045	0.055		
H(1)	6.10	6.71	0.240	0.264		
J(1)	2.41	2.92	0.095	0.115		
L	13.36	14.40	0.526	0.567		
L(1)	3.33	4.04	0.131	0.159		
ØР	3.53	3.94	0.139	0.155		
Q	2.54	3.00	0.100	0.118		
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031						

### Note

 M\* = 0.052 inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



Revison: 14-Dec-15 1 Document Number: 66542



# **Legal Disclaimer Notice**

Vishay

## **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.