Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

description

These devices contain 2-wide 2-input and 2-wide 3-input AND-OR-INVERT gates. They perform the following Boolean functions:

$$1Y = \overline{(1A \cdot 1B \cdot 1C) + (1D \cdot 1E \cdot 1F)}$$
$$2Y = \overline{(2A \cdot 2B) + (2C \cdot 2D)}$$

The SN54F51 is characterized for operation over the full military temperature range of -55° C to 125°C. The SN74F51 is characterized for operation from 0°C to 70°C.

FUNCTION TABLES

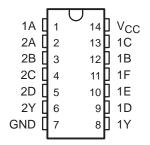
GATE 1

		OUTPUT				
1A	1B	1C	1D	1E	1F	1Y
Н	Н	Н	Χ	Χ	Х	L
Х	X	X	Н	Н	Н	L
	Н					

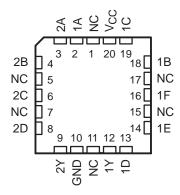
GATE 2

	INP	OUTPUT		
2A	2B	2C	2D	2Y
Н	Н	Х	Х	L
Х	Χ	Н	Н	L
All c	ther co	Н		

SN54F51 . . . J PACKAGE SN74F51 . . . D OR N PACKAGE (TOP VIEW)

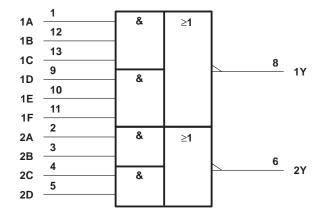


SN54F51 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

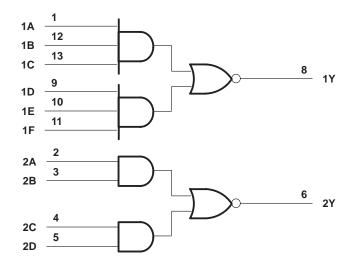
logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the D, J, and N packages.

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I (see Note 1)	–1.2 V to 7 V
Input current range	30 mA to 5 mA
Voltage range applied to any output in the high state	0.5 V to V _{CC}
Current into any output in the low state	40 mA
Operating free-air temperature range: SN54F51	–55°C to 125°C
SN74F51	0°C to 70°C
Storage temperature range	−65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage ratings may be exceeded provided the input current ratings are observed.

recommended operating conditions

			SN54F51			SN74F51		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V _{IL}	Low-level input voltage			0.8			0.8	V
liK	Input clamp current			-18			-18	mA
ІОН	High-level output current			-1			-1	mA
lOL	Low-level output current			20			20	mA
TA	Operating free-air temperature	-55		125	0		70	°C

SN54F51, SN74F51 DUAL 2-WIDE 2-INPUT, 2-WIDE 3-INPUT AND-OR-INVERT GATES

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS			SN54F51			SN74F51			
PARAMETER			MIN	TYP	MAX	MIN	TYP	MAX	UNIT	
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V	
Va.,	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -1 \text{ mA}$	2.5	3.4		2.5	3.4		V	
VOH	$V_{CC} = 4.75 \text{ V},$	$I_{OH} = -1 \text{ mA}$				2.7			V	
V _{OL}	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 20 \text{ mA}$		0.35	0.5		0.35	0.5	V	
I _I	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			100			100	μΑ	
lн	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20			20	μΑ	
Ι _{ΙL}	$V_{CC} = 5.5 \text{ V},$	V _I = 0.5 V			- 0.6			- 0.6	mA	
los [‡]	$V_{CC} = 5.5 \text{ V},$	V _O = 0	-60		-150	-60		-150	mA	
Іссн	$V_{CC} = 5.5 \text{ V},$	V _I = 0		1.8	3		1.8	3	mA	
ICCL	$V_{CC} = 5.5 \text{ V},$	V _I = 4.5 V		5.5	7.5		5.5	7.5	mA	

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 \text{ V},$ $C_{L} = 50 \text{ pF},$ $R_{L} = 500 \Omega,$ $T_{A} = 25^{\circ}\text{C}$			V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R_L = 500 Ω , T_A = MIN to MAX§				UNIT
			′F51		SN54F51		SN74F51]	
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	Any input		2	3.5	5.5	1.5	7.5	1.5	6.5	ns
t _{PHL}	Arry Input	'	1	2.5	4	1	5	1	4.5	115

[§] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and waveforms are shown in Section 1.



[‡] Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

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