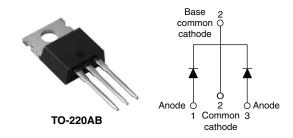


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 20 A			
V _R	15 V			
I _{RM}	600 mA at 100 °C			

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Center tap configuration
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	A		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	700	Α		
V _F	19 Apk, T _J = 125 °C (per leg)	0.25	V		
T _J	Range	- 55 to 125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	40L15CT	UNITS		
Maximum DC reverse voltage	V _R	15	V		
Maximum working peak reverse voltage	V_{RWM}	15	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg		50 °/ duty avalo at T ₂ = 95 °C r	at T 05 °C vactor gulav wayafava		
See fig. 5	per device	I _{F(AV)}	50 % duty cycle at T _C = 85 °C, rectangular waveform 40		40	Α
Maximum peak one cycle non-repetitive surge current per leg See fig. 7			5 μs sine or 3 μs rect. pulse Following any rated load condition and with	700	A	
		IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	330	
Non-repetitive avalanche	energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 6 \text{mH}$		10	mJ
Repetitive avalanche current per leg I _{AR}		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		2	Α

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	19 A	T _{.1} = 25 °C	-	0.41	- V
		40 A	1j=25 C	-	0.52	
		19 A	- T _J = 125 °C	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current per leg	1 (1)	T _J = 25 °C	V _R = Rated V _R	-	10	- mA
See fig. 2	I _{RM} ⁽¹⁾	T _J = 100 °C		-	600	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.1	82	V
Forward slope resistance	r _t			7	7.6	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8 -		nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/μ			V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and si temperature range	torage	T _J , T _{Stg}		- 55 to 125	°C	
Maximum thermal resistation junction to case per leg	ance,	R _{thJC}	DC operation	1.5	°C/W	
Typical thermal resistant case to heatsink	e,	R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV	
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque —	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf · in)	
Marking device			Case style TO-220AB	40L15CT		



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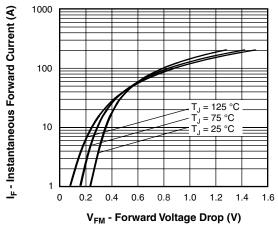


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

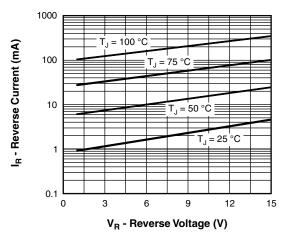


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

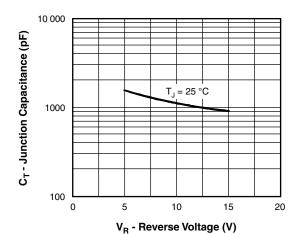


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

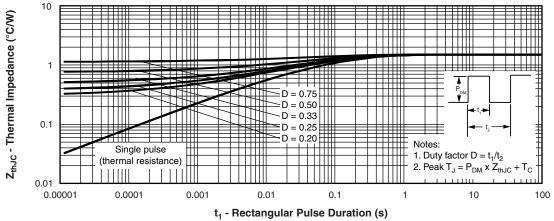


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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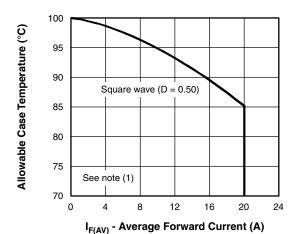


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

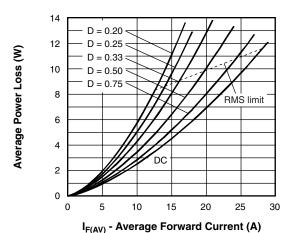


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

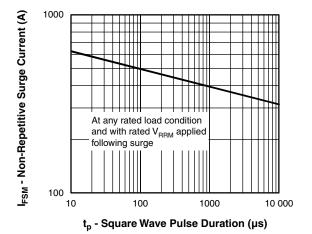


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

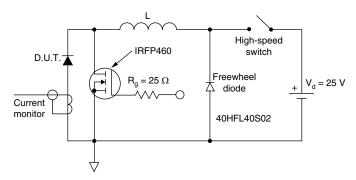


Fig. 8 - Unclamped Inductive Test Circuit

Note

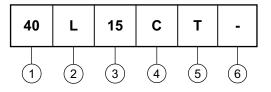
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (40 = 40 A)
- 2 Schottky "L" series
- 3 Voltage rating (15 = 15 V)
- C = Common cathode
- 5 Package:

T = TO-220

6 - None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95222					
Part marking information	http://www.vishay.com/doc?95225				



Vishay

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