

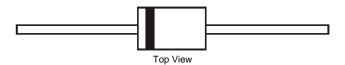
# 10A SBR<sup>®</sup> SUPER BARRIER RECTIFIER

#### **Features**

- Designed as Bypass Diodes for Solar Panels
- Complies with IEC 61730-2 Solar Bypass Diode Standards (T<sub>Jmax</sub> ≤ T<sub>J</sub> = T<sub>L/C</sub> + R<sub>thL/C</sub> \* V<sub>F</sub> \* I<sub>se</sub>, @ T<sub>A</sub> = 75°C, 1hr. Short Circuit)
- Patented Super Barrier Rectifier Technology
- High Forward Surge Capability
- Ultra Low Forward Voltage Drop
- Excellent High Temperature Stability
- Lead Free Finish, RoHS Compliant (Note 1)

#### **Mechanical Data**

- Case: DO-201AD
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Tin Plated Leads. Solderable per MIL-STD-202, Method 208 63
- Weight: 0.121 grams (approximate)



#### **Ordering Information** (Note 2)

Part Number	Case	Packaging
SBR1045SD1-T	DO-201AD	1200/Tape & Reel, 13-inch

Notes:

- 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.
- 2. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



SBR1045 = Product Type Marking Code

Olile Manufacturers' code marking

AB = Foundry and Assembly Code (if applicable)

YWW = Date Code Marking

Y = Last digit of year (ex: 7 for 2007)

WW = Week code (01 ~ 53)



### Maximum Ratings @TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	45	٧
RMS Reverse Voltage	V <sub>R(RMS)</sub>	32	V
Average Rectified Output Current @ T <sub>C</sub> = 110°C	lo	10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	180	А

#### **Thermal Characteristics**

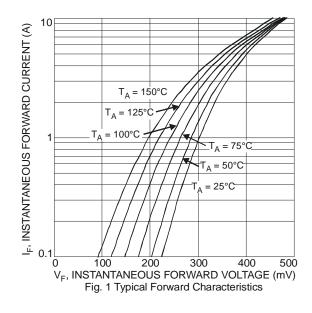
Characteristic		Symbol	Value	Unit
Maximum Thermal Resistance (per leg) (Note 3)		$R_{ hetaJA} \ R_{ hetaJL}$	54 9	°C/W
	V <sub>R</sub> ≤ 80% V <sub>RRM</sub>		-65 to +150	
Operating Temperature Range	V <sub>R</sub> ≤ 50% V <sub>RRM</sub>	$T_J$	≤180	°C
	DC Forward Mode		≤200	
Storage Temperature Range		T <sub>STG</sub>	-65 to +175	°C

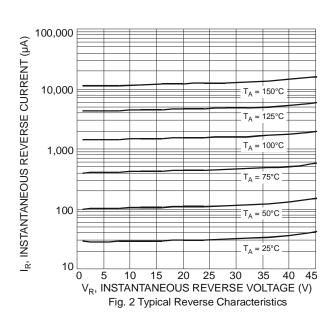
#### Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	$V_{(BR)R}$	45	-	1	V	$I_R = 0.5 \text{mA}$
Forward Voltage Drop	V <sub>F</sub>	- - -	0.46 0.50 0.48	0.51 0.55 0.53	V	I <sub>F</sub> = 8A, T <sub>J</sub> = 25°C I <sub>F</sub> = 10A, T <sub>J</sub> = 25°C I <sub>F</sub> = 10A, T <sub>J</sub> = 125°C
Leakage Current (Note 4)	I <sub>R</sub>	- - -	0.05 - 18	0.45 18 100	mA	$V_R = 45V, T_J = 25^{\circ}C$ $V_R = 45V, T_J = 100^{\circ}C$ $V_R = 45V, T_J = 150^{\circ}C$

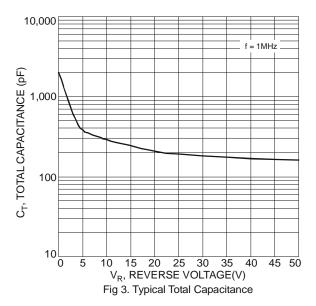
Notes:

- 3. FR-4 PCB, 2oz. Copper, with minimum recommended pad layout as show on Diodes, Inc. suggest pad layout AP02001 at http://www.diodes.com.
- 4. Short duration pulse test used to minimize self-heating effect.

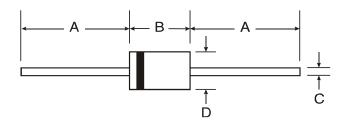








## **Package Outline Dimensions**



DO-201AD				
Dim	Min	Max		
Α	25.40	_		
В	7.20	9.50		
С	1.20	1.30		
D	4.80	5.30		
All Dimensions in mm				



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