



PBSS4620PA

20 V, 6 A NPN low V_{CEsat} transistor

18 October 2024

Product data sheet

1. General description

NPN low V_{CEsat} transistor, encapsulated in an ultra thin SOT1061 leadless small Surface-Mounted Device (SMD) plastic package with medium power capability.

PNP complement: PBSS5620PA

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- Exposed heat sink for excellent thermal and electrical conductivity
- Leadless small SMD plastic package with medium power capability
- AEC-Q101 qualified

3. Applications

- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

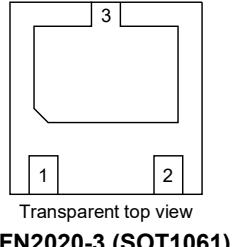
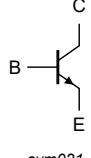
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	20	V
I _C	collector current		-	-	6	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	7	A
R _{CEsat}	collector-emitter saturation resistance	I _C = 6 A; I _B = 300 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	-	33	46	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 Transparent top view DFN2020-3 (SOT1061)	 sym021
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4620PA	DFN2020-3	plastic, leadless thermal enhanced ultra thin small outline package; 3 terminals; 1.3 mm pitch; 2 mm x 2 mm x 0.65 mm body	SOT1061

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4620PA	A6

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

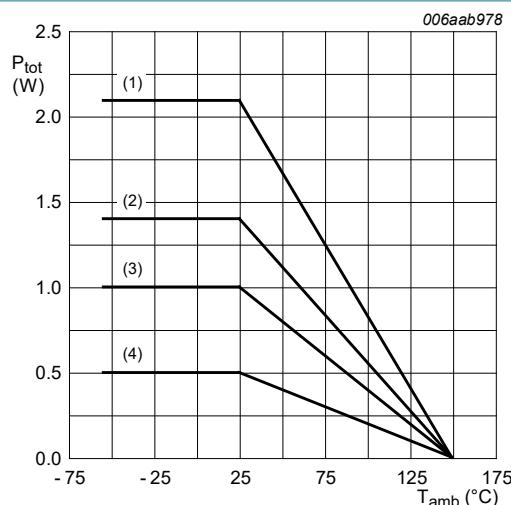
Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	20	V
V_{CEO}	collector-emitter voltage	open base		-	20	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I_C	collector current			-	6	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	7	A
I_B	base current			-	600	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	500	mW
			[2]	-	1	W
			[3]	-	1.4	W
			[4]	-	2.1	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

[4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



- (1) Ceramic PCB, Al₂O₃, standard footprint
- (2) FR4 PCB, mounting pad for collector 6 cm²
- (3) FR4 PCB, mounting pad for collector 1 cm²
- (4) FR4 PCB, standard footprint

Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

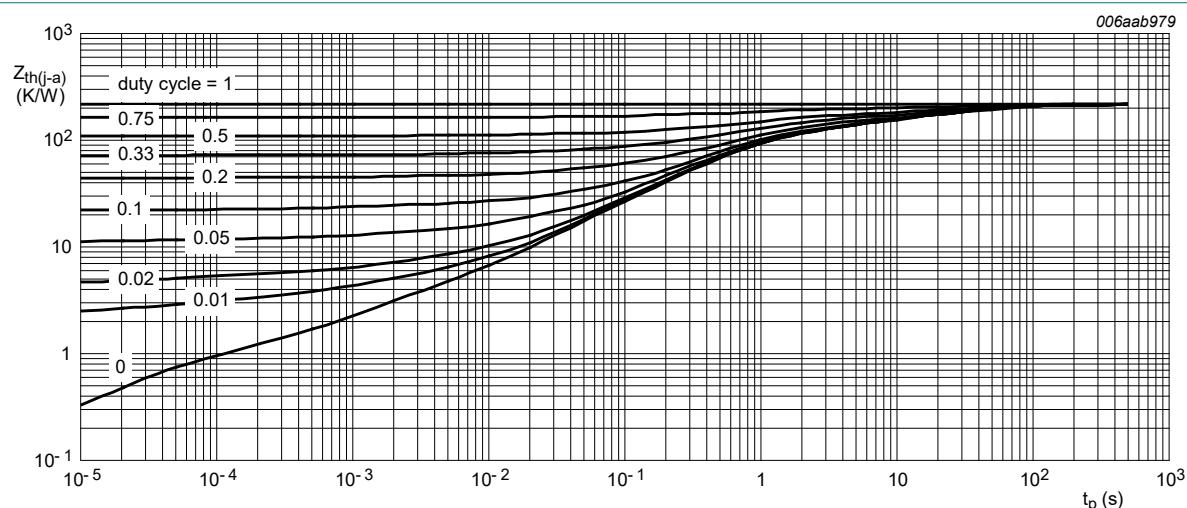
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
			[2]	-	-	125	K/W
			[3]	-	-	90	K/W
			[4]	-	-	60	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

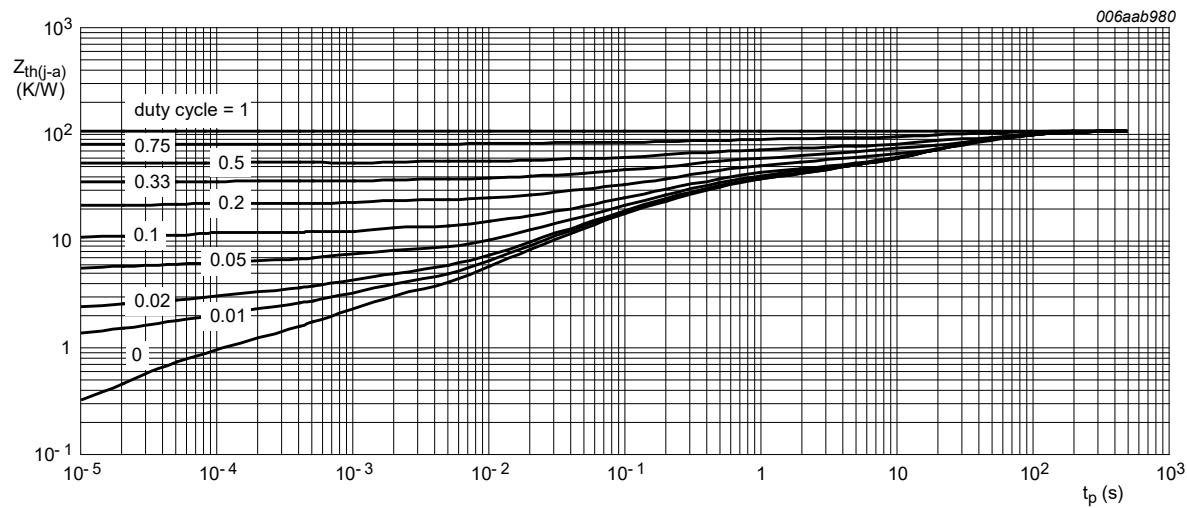
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

[4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



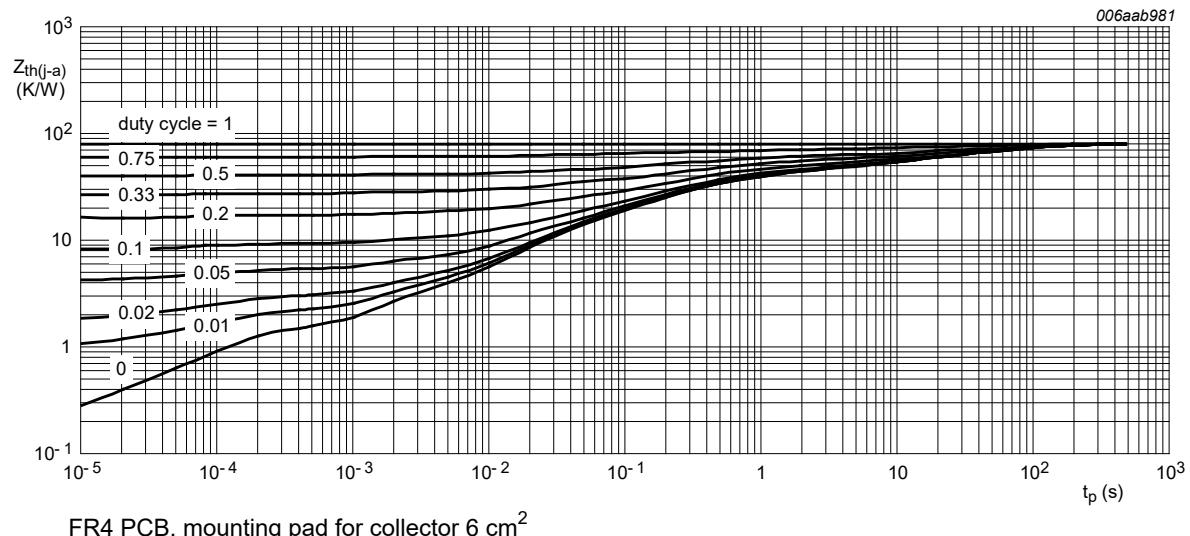
FR4 PCB, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



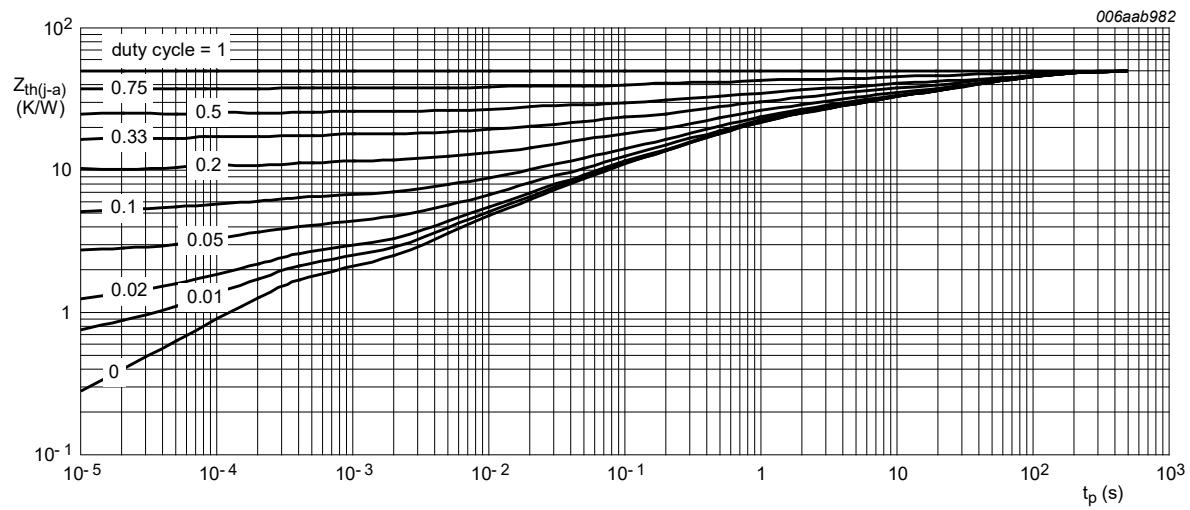
FR4 PCB, mounting pad for collector 1 cm²

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 6 cm^2

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



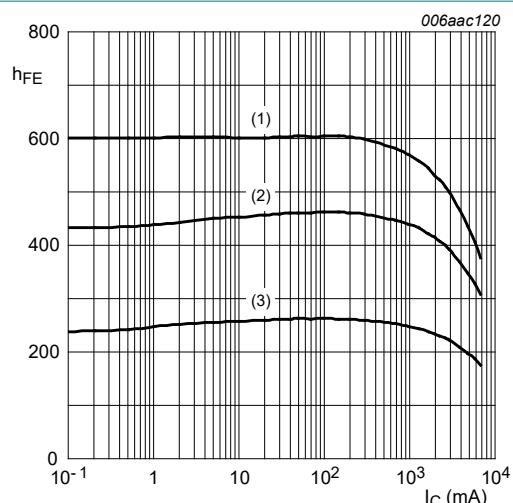
Ceramic PCB, Al_2O_3 , standard footprint

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

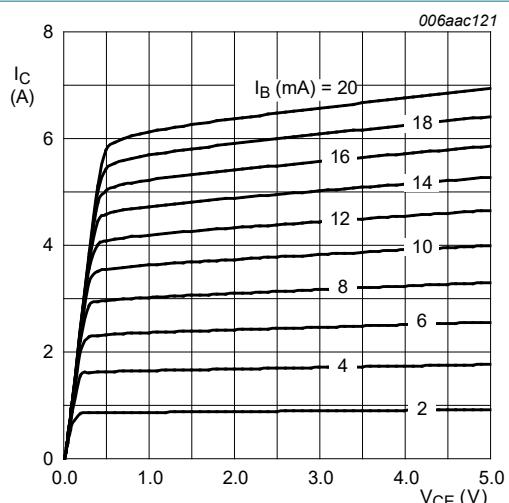
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = 16 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	100	nA
		$V_{CB} = 16 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ }^\circ\text{C}$	-	-	50	µA
I_{CES}	collector-emitter cut-off current	$V_{CE} = 16 \text{ V}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	100	nA
I_{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	100	nA
h_{FE}	DC current gain	$V_{CE} = 2 \text{ V}; I_C = 0.5 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	280	440	-	
		$V_{CE} = 2 \text{ V}; I_C = 1 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	270	430	-	
		$V_{CE} = 2 \text{ V}; I_C = 2 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	260	415	-	
		$V_{CE} = 2 \text{ V}; I_C = 6 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	200	330	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 0.5 \text{ A}; I_B = 50 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	20	30	mV
		$I_C = 1 \text{ A}; I_B = 50 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	37	55	mV
		$I_C = 1 \text{ A}; I_B = 10 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	50	70	mV
		$I_C = 2 \text{ A}; I_B = 20 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	85	120	mV
		$I_C = 3 \text{ A}; I_B = 30 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	120	170	mV
		$I_C = 4 \text{ A}; I_B = 400 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	135	185	mV
		$I_C = 6 \text{ A}; I_B = 300 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	200	275	mV
R_{CEsat}	collector-emitter saturation resistance		-	33	46	mΩ
V_{BEsat}	base-emitter saturation voltage	$I_C = 1 \text{ A}; I_B = 10 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	0.75	0.9	V
		$I_C = 6 \text{ A}; I_B = 300 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	0.97	1.1	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = 2 \text{ V}; I_C = 2 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	0.74	0.9	V
t_d	delay time	$V_{CC} = 9 \text{ V}; I_C = 2 \text{ A}; I_{Bon} = 0.1 \text{ A}; I_{Boff} = -0.1 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	25	-	ns
t_r	rise time		-	55	-	ns
t_{on}	turn-on time		-	80	-	ns
t_s	storage time		-	285	-	ns
t_f	fall time		-	50	-	ns
t_{off}	turn-off time		-	335	-	ns
f_T	transition frequency	$V_{CE} = 10 \text{ V}; I_C = 100 \text{ mA}; f = 100 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$	50	80	-	MHz
C_c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	80	95	pF



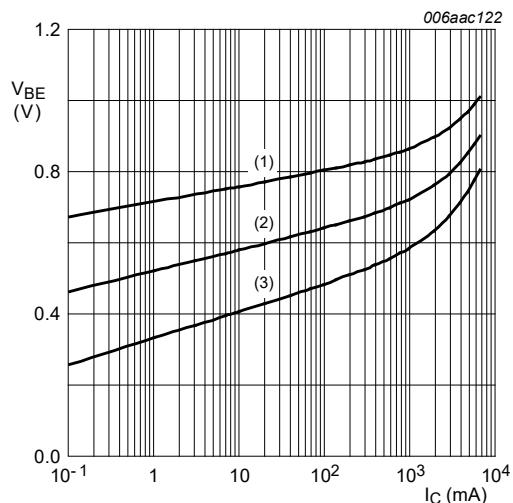
V_{CE} = 2 V
 (1) T_{amb} = 100 °C
 (2) T_{amb} = 25 °C
 (3) T_{amb} = -55 °C

Fig. 6. DC current gain as a function of collector current; typical values



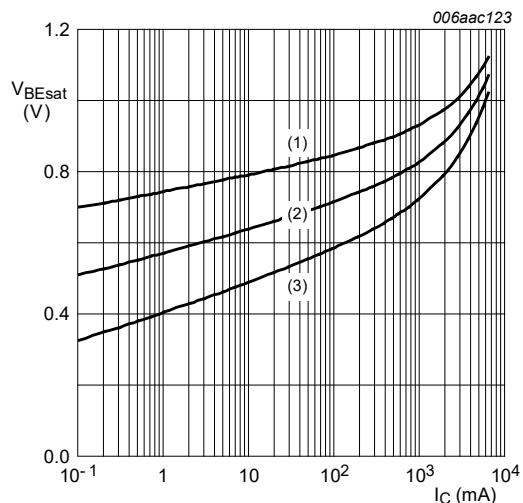
T_{amb} = 25 °C

Fig. 7. Collector current as a function of collector-emitter voltage; typical values



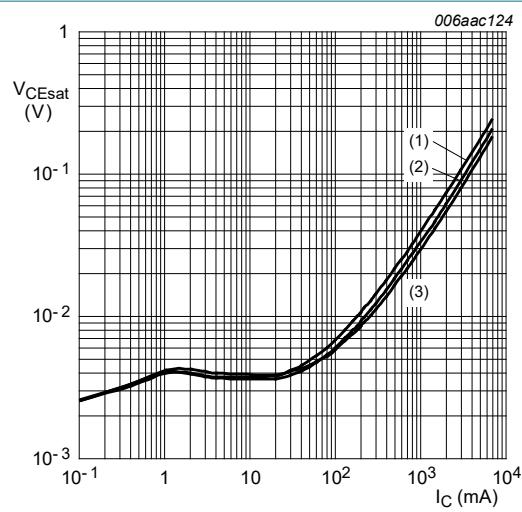
V_{CE} = 2 V
 (1) T_{amb} = -55 °C
 (2) T_{amb} = 25 °C
 (3) T_{amb} = 100 °C

Fig. 8. Base-emitter voltage as a function of collector current; typical values



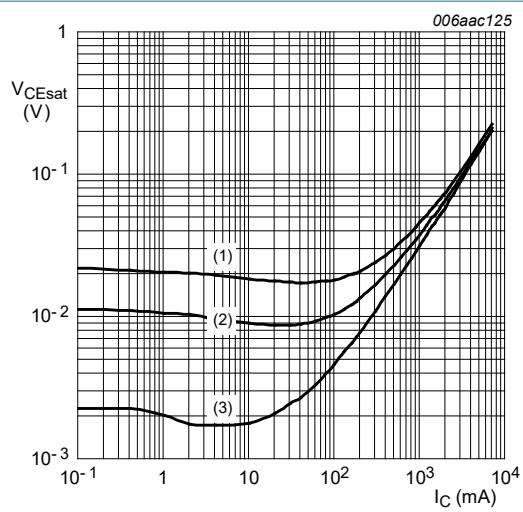
I_C/I_B = 20
 (1) T_{amb} = -55 °C
 (2) T_{amb} = 25 °C
 (3) T_{amb} = 100 °C

Fig. 9. Base-emitter saturation voltage as a function of collector current; typical values



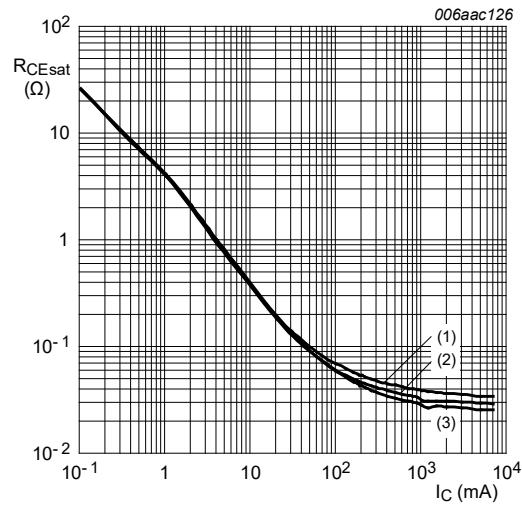
$I_C/I_B = 20$
 (1) $T_{amb} = 100^\circ C$
 (2) $T_{amb} = 25^\circ C$
 (3) $T_{amb} = -55^\circ C$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values



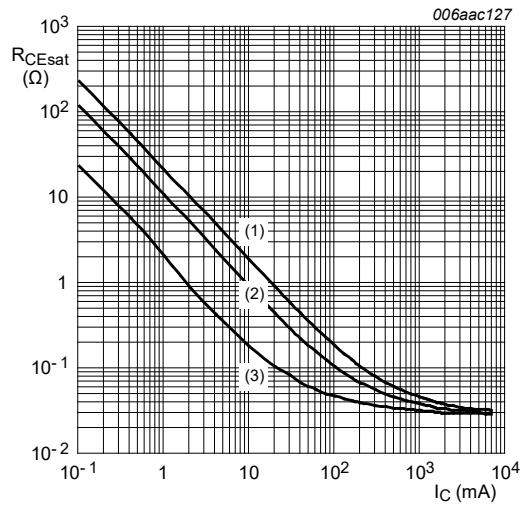
$T_{amb} = 25^\circ C$
 (1) $I_C/I_B = 100$
 (2) $I_C/I_B = 50$
 (3) $I_C/I_B = 10$

Fig. 11. Collector-emitter saturation voltage as a function of collector current; typical values



$I_C/I_B = 20$
 (1) $T_{amb} = 100^\circ C$
 (2) $T_{amb} = 25^\circ C$
 (3) $T_{amb} = -55^\circ C$

Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values



$T_{amb} = 25^\circ C$
 (1) $I_C/I_B = 100$
 (2) $I_C/I_B = 50$
 (3) $I_C/I_B = 10$

Fig. 13. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

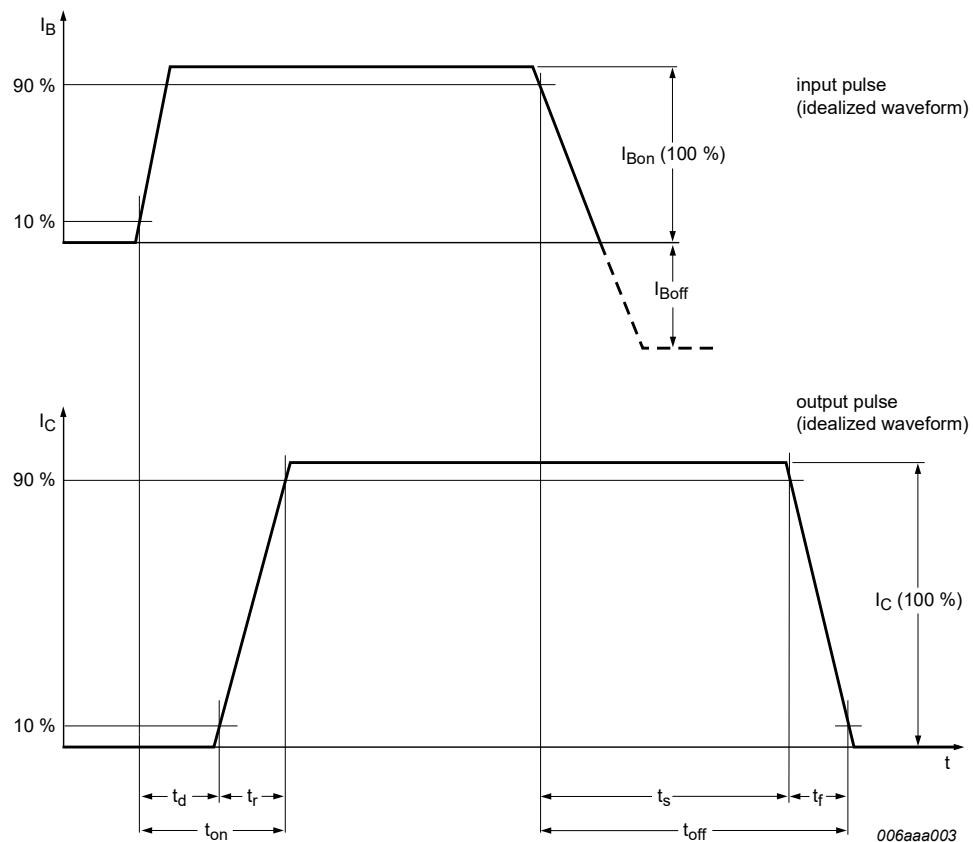
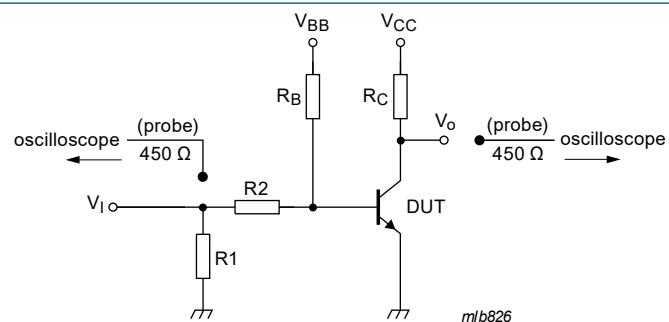


Fig. 14. Transistor switching time definition



$V_{CC} = 9 \text{ V}$; $I_C = 2 \text{ A}$; $I_{B(on)} = 0.1 \text{ A}$; $I_{B(off)} = -0.1 \text{ A}$

Fig. 15. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

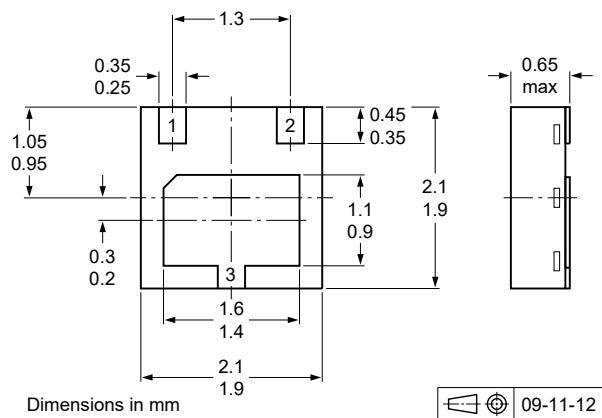


Fig. 16. Package outline DFN2020-3 (SOT1061)

13. Soldering

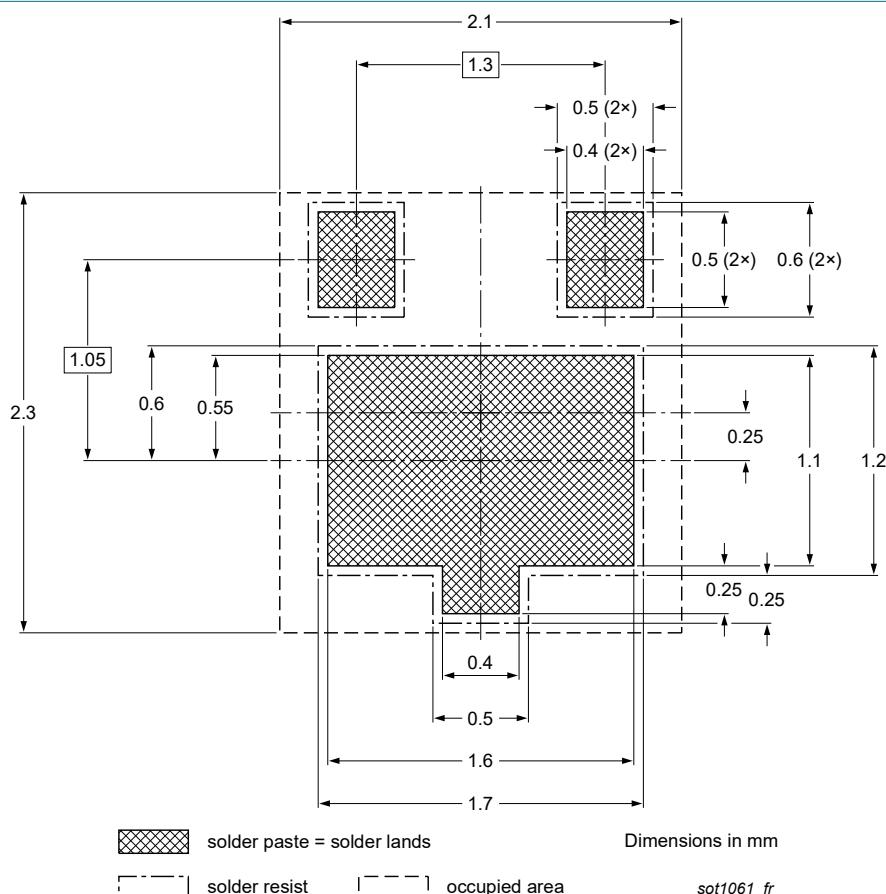


Fig. 17. Reflow soldering footprint for DFN2020-3 (SOT1061)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4620PA v.2	20241018	Product data sheet	-	PBSS4620PA v.1
Modifications:	<ul style="list-style-type: none">The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.Section "Packing information" removed			
PBSS4620PA v.1	20100518	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 18 October 2024
