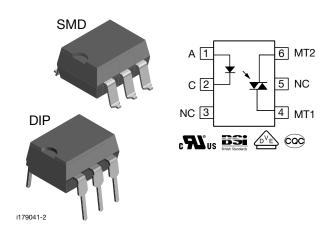


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Vishay Semiconductors

Optocoupler, Phototriac Output, Non-Zero Crossing, 250 V_{DRM}



DESCRIPTION

The K3010P, K3010PG series consists of a photo-transistor optically coupled to a gallium arsenide infrared-emitting diode in a 6-pin plastic dual inline package

AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- BSI: BS EN60065:2002 and IEC 60065:2001, certificate number 7955. An BS EN60950-1:2006 certificate number 7956
- DIN EN 60747-5-5
- CQC: GB8898-2001

FEATURES

- Isolation materials according to UL 94 V-0
- Special construction: therefore, extra low coupling capacity of typical 0.2 pF, high common mode rejection



- I_{FT} of 5 mA, 10 mA, and 15 mA
- Rated impulse voltage (transient overvoltage)
 V_{IOTM} = 8 kV_{peak}
- Isolation test voltage, 5300 V_{RMS}, t = 1 s
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):

- for appl. class I to IV at mains voltage ≤ 300 V
- for appl. class I to IV at mains voltage ≤ 600 V according to DIN EN60747-5-5 (VDE0884), suitable for:
- Monitors
- Air conditioners
- Line switches
- Solid state relay
- Microwave

ORDERING INFORMATIO	N		
PART NUMBER	O P # X TRIGGER CURRENT BIN	0 # # T PACKAGE OPTION TAPE AND REEL	7.62 mm Option 7 > 0.7 mm
AGENCY CERTIFIED/PACKAGE		TRIGGER CURRENT, I _{FT}	
VDE, cUL, BSI	5 mA	10 mA	15 mA
DIP-6	K3012P	K3011P	K3010P
DIP-6, 400 mil	K3012PG	K3011PG	K3010PG
SMD-6, option 7	K3012P-X007T	-	K3010P-X007T

Note

• G = leadform 10.16 mm; G is not marked on the body.



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V_R	5	V	
Forward current		I _F	80	mA	
Forward surge current	t _p ≤ 10 μs	I _{FSM}	3	Α	
Power dissipation		P _{diss}	100	mW	
Junction temperature		T _j	100	°C	
OUTPUT					
Off state output terminal voltage		V_{DRM}	250	V	
On state RMS current		I _{TRM}	100	mA	
Peak surge current, non-repetitive	t _p ≤ 10 ms	I _{TMS}	1.5	Α	
Power dissipation		P _{diss}	300	mW	
Junction temperature		T _j	100	°C	
COUPLER					
Isolation test voltage (RMS)	t = 1 s	V _{ISO}	5300	V _{RMS}	
Total power dissipation		P _{tot}	350	mW	
Storage temperature range		T _{stg}	- 55 to + 150	°C	
Ambient temperature range		T _{amb}	- 55 to + 100	°C	
Soldering temperature (1)	2 mm from case, t ≤ 10 s	T _{sld}	260	°C	

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to wave profile for soldering conditions for through hole devices (DIP) "Assembly Instructions" (www.vishay.com/doc?80054)

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = 50 mA		V _F		1.25	1.6	V
Junction capacitance	$V_R = 0$, $f = 1 MHz$		C _j		50		pF
OUTPUT							
Forward peak off-state voltage (repetitive)	I _{RDM} = 100 nA		V _{DRM} ⁽¹⁾	250			V
Peak on-state voltage	I _{TM} = 100 mA		V _{TM}		1.5	3	V
Critical rate of rice of off state valtage	I _{FT} = 0, I _{FT} = 30 mA		dV/d _{tcr}		10		V/µs
Critical rate of rise of off-state voltage			dV/d _{tcrq}	0.1	0.2		V/µs
COUPLER (2)							
Collector emitter trigger current	$V_S = 3 \text{ V}, R_L = 150 \Omega$	K3010P	I _{FT}		8	15	mA
		K3010PG	I _{FT}		8	15	mA
		K3011P	I _{FT}		5	10	mA
		K3011PG	I _{FT}		5	10	mA
		K3012P	I _{FT}		2	5	mA
		K3012PG	I _{FT}		2	5	mA
Holding current	$I_F = 10 \text{ mA}, V_S \ge 3 \text{ V}$		I _H		100		μΑ

Notes

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
 evaluation. Typical values are for information only and are not part of the testing requirements.
- (1) Test voltage must be applied within dV/dt ratings.
- (2) IFT is defined as a minimum trigger current.



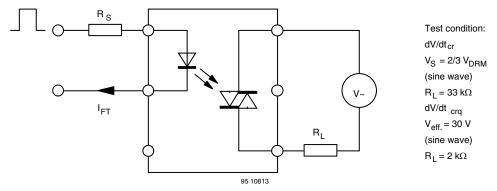
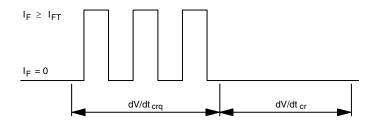


Fig. 1 - Test Circuit for dV/dt_{cr} and dV/dt_{cr}



dV/dt cr

Highest value of the "rate of rise of off-state voltage" which does not cause any switching from the off state to the on state

dV/dt _{crq} 95 10814

Highest value of the "rate of rise of communicating voltage" which does not switch on the device again, after the voltage has decreased to zero and the trigger current is switched from I_{FT} to zero

Fig. 2

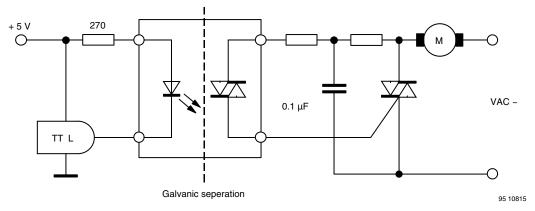


Fig. 3 - Motor Control Circuit

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SAFETY AND INSULATION RATINGS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)			55/100/21		
Pollution degree (DIN VDE 0109)			2		
Comparative tracking index	CTI	175			
Peak transient overvoltage	V _{IOTM}			8000	V _{peak}
Peak working insulation voltage	V _{IORM}			890	V _{peak}
Partial discharge test voltage (method a, $V_{pd} = V_{IORM} \times 1.875$)	V_{pd}			1669	V _{peak}
Isolation resistance at T _{amb} = 100 °C, V _{DC} = 500 V	R _{IO}	10 ¹¹			Ω
Isolation resistance at T _{amb} = 25 °C, V _{DC} = 500 V	R _{IO}	10 ¹²			Ω
Safety rating - power	P _{SO}			265	mW
Safety rating - input current	I _{SI}			130	mA
Safety rating - temperature	T _{SI}			150	°C
Clearance distance (Standard DIP-6)		7			mm
Creepage distance (Standard DIP-6)		7			mm
Clearance distance (400 mil DIP-6)		8			mm
Creepage distance (400 mil DIP-6)		8			mm

Note

According to DIN EN60747-5-5 (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings.
 Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

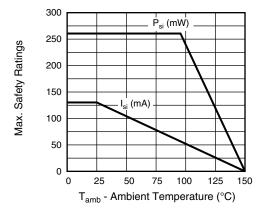


Fig. 4 - Safety Parameter Derating Diagram

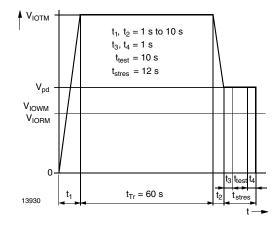


Fig. 5 - Test Pulse Diagram for Sample Test according to DIN EN60747-5-5/DIN EN60747-; IEC 60747

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

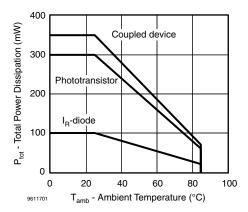


Fig. 6 - Total Power Dissipation vs. Ambient Temperature

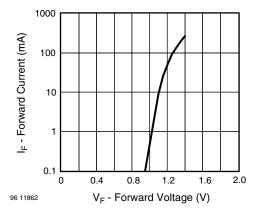


Fig. 7 - Forward Current vs. Forward Voltage

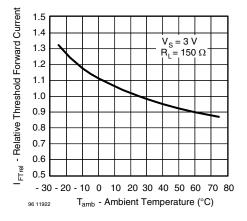


Fig. 8 - Relative Threshold Forward Current vs.
Ambient Temperature

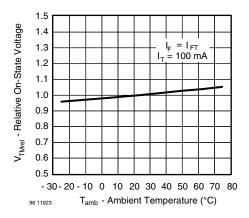


Fig. 9 - Relative On-State vs. Ambient Temperature

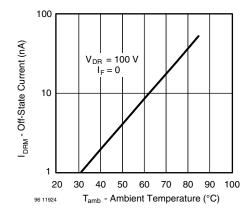


Fig. 10 - Off-State Current vs. Ambient Temperature

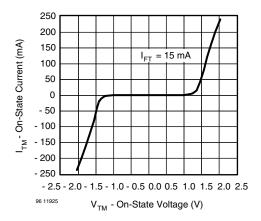
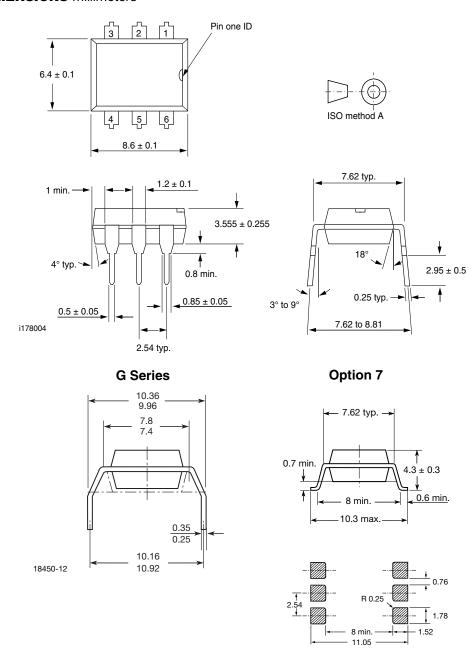


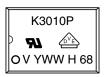
Fig. 11 - Collector Current vs. Forward Current

ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

PACKAGE DIMENSIONS millimeters



PACKAGE MARKING (example)



Notes

- The "G" of the 400 mil G leadform type is not marked on the body.
- The VDE logo is only marked on option1 parts.



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