

FEATURES

- Low Operating Voltage: $\pm 5\text{V}$ to $\pm 15\text{V}$
- $500\mu\text{A}$ Supply Current
- Zero Supply Current when Shut Down
- Outputs Can Be Driven $\pm 30\text{V}$
- Thermal Limiting
- Output "Open" when Off (Three-State)
- 10mA Output Drive
- Pinout Similar to 1488

APPLICATIONS

- RS232
- Power Supply Inverter
- Micropower Interface
- Level Translator

DESCRIPTION

The LT[®]1032 is a RS232 and RS423 line driver that operates over a $\pm 5\text{V}$ to $\pm 15\text{V}$ range on low supply current and can be shut down to zero supply current. Outputs are fully protected from externally applied voltages of $\pm 30\text{V}$ by both current and thermal limiting. Since the output swings to within 200mV of the positive supply and 600mV of the negative supply, power supply needs are minimized.

Also included is a strobe pin to force all outputs low independent of input or shutdown conditions. Further, slew rate can be adjusted with a resistor connected to the supply.

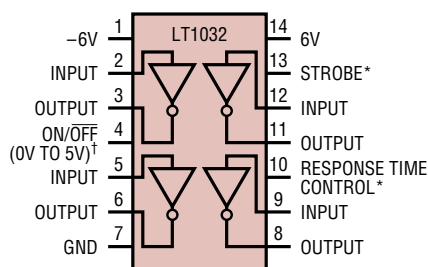
A major advantage of the LT1032 is the high impedance output state when off or powered down.

For applications requiring dual or triple RS232 driver/receiver devices, see the LT1180A (dual), LT1039A (triple) or the LT1130A data sheets.

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TYPICAL APPLICATION

RS232 Line Driver

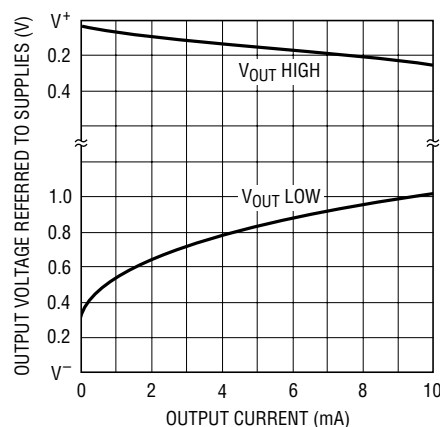


*NO CONNECTION NEEDED WHEN NOT USED

†5V = ON

LT1032 • TA01

Output Swing vs Output Current



LT1032 • TA02

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage $\pm 15\text{V}$
 Logic Input Pins V^- to 25V
 ON/OFF Pin GND to 15V
 Output (Forced) $V^- + 30\text{V}$, $V^+ - 30\text{V}$
 Response Pin $\pm 6\text{V}$
 Short-Circuit Duration (to $\pm 30\text{V}$) Indefinite
 Storage Temperature Range -65°C to 150°C

Operating Temperature Range

LT1032C 0°C to 70°C

LT1032I -40°C to 85°C

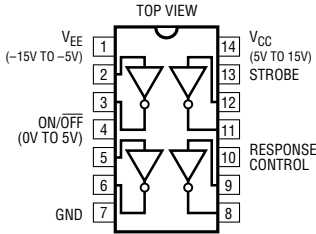
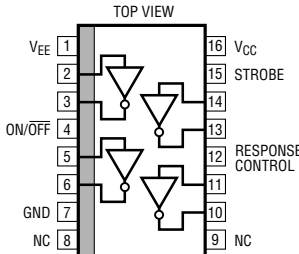
LT1032M -55°C to 125°C

C Grade Guaranteed

Functional by Design -25°C to 85°C

Lead Temperature (Soldering, 10 sec) 300°C

PACKAGE/ORDER INFORMATION

 TOP VIEW N PACKAGE 14-LEAD PDIP $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 130^\circ\text{C/W}$ (CN)	ORDER PART NUMBER	 TOP VIEW SW PACKAGE 16-LEAD PLASTIC SO (WIDE) $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 90^\circ\text{C/W}$	ORDER PART NUMBER
J PACKAGE 14-LEAD CERDIP $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 95^\circ\text{C/W}$ (CJ) $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 95^\circ\text{C/W}$ (MJ) OBsolete PACKAGE Consider the N Package as an Alternate Source	LT1032CN LT1032IN LT1032CJ LT1032MJ		LT1032CSW LT1032ISW

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS The ● denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. Supply voltage = $\pm 5\text{V}$ to $\pm 15\text{V}$

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Current	$V_{ON/OFF} \geq 2.4\text{V}$, $I_{OUT} = 0$, All Outputs Low ●		500	1000	μA
Power Supply Leakage Current	$V_{ON/OFF} \leq 0.4\text{V}$ $V_{ON/OFF} \leq 0.1\text{V}$, $T_A = 125^\circ\text{C}$ ●		1 10	10 50	μA μA
Output Voltage Swing	Load = 2mA Positive Negative	$V^+ - 0.3\text{V}$	$V^+ - 0.1\text{V}$ $V^- + 0.7\text{V}$	$V^- + 0.9\text{V}$	V V
Output Current (Active)	$V_{SUPPLY} = \pm 5\text{V}$ to $\pm 15\text{V}$	10	22		mA
Output Current (Shutdown)	$V_{SUPPLY} = 0\text{V}$, $V_{OUT} = \pm 30\text{V}$ $V_{SUPPLY} = \pm 15\text{V}$, $V_{OUT} = \pm 20\text{V}$		2 2	100 100	μA μA
Output Overload Voltage (Forced)	Operating or Shutdown ●	$V^+ - 30\text{V}$		$V^- + 30\text{V}$	V
Input Overload Voltage (Forced)	Operating or Shutdown ●	V^-		30V	V
Logic Input Levels	Low Input ($V_{OUT} = \text{High}$) High Input ($V_{OUT} = \text{Low}$) ● ●		1.4 1.4	0.8	V V
Logic Input Current	$V_{IN} > 2.0\text{V}$ $V_{IN} < 0.8\text{V}$		2 10	20 20	μA μA
ON/OFF Pin Current	$0 \leq V_{IN} \leq 5\text{V}$ ●	-10	3	50	μA
Slew Rate	$I_{RESPONSE} = 0$, $R_L = 3\text{k}$	4	15	30	V/ μs

ELECTRICAL CHARACTERISTICS

The ● denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. Supply voltage = $\pm 5\text{V}$ to $\pm 15\text{V}$

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Change in Slew Rate (Note 3)	$I_{\text{RESPONSE}} = 50\mu\text{A}$ $I_{\text{RESPONSE}} = -50\mu\text{A}$		50 -50		% %
Response Pin Leakage	$V_{\text{SUPPLY}} = \pm 6\text{V}$, $V_{\text{ON/OFF}} \leq 0.4\text{V}$ $V_{\text{RESPONSE}} = \pm 6\text{V}$		1		μA

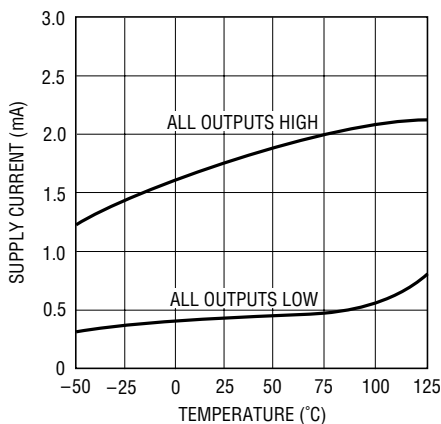
Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: 3V applied to the strobe pin will force all outputs low. Strobe pin input impedance is about 2k to ground. Leave open when not used.

Note 3: Response can be changed by connecting a resistor to the supply. For supplies less than $\pm 6\text{V}$ this current is disconnected when shut down. Leave open when not used.

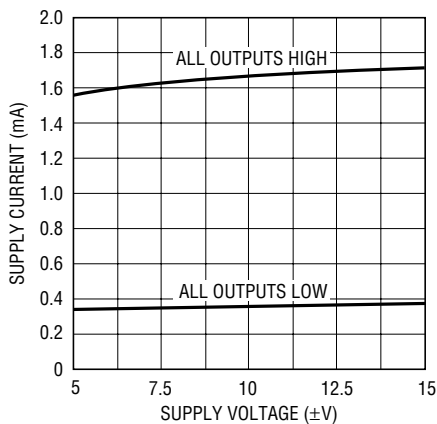
TYPICAL PERFORMANCE CHARACTERISTICS

On Supply Current vs Temperature



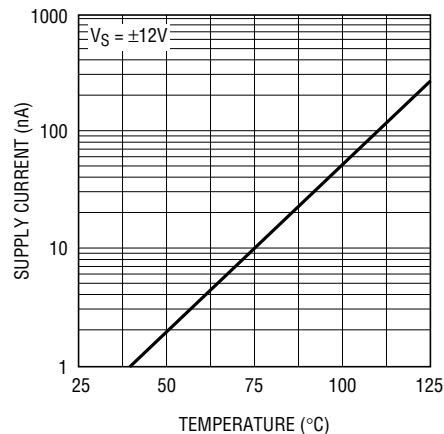
LT1032 • TPC01

Supply Current vs Supply Voltage



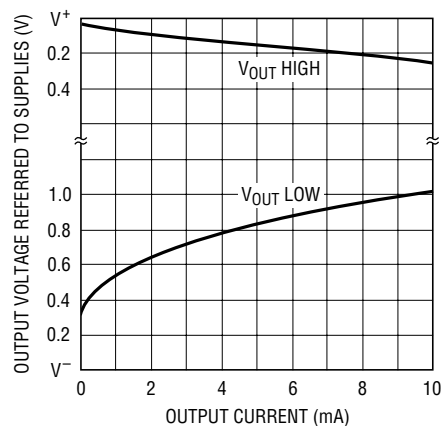
LT1032 • TPC02

Off Supply Current vs Temperature



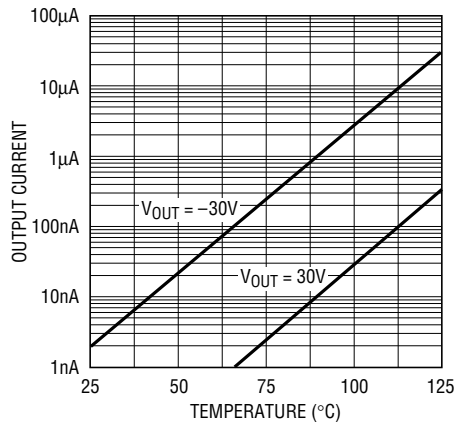
LT1032 • TPC03

Output Swing vs Output Current



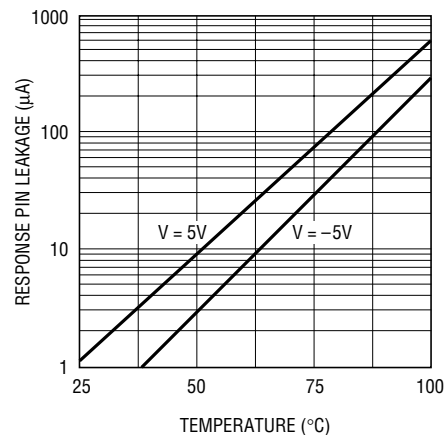
LT1032 • TA02

Output Leakage vs Temperature



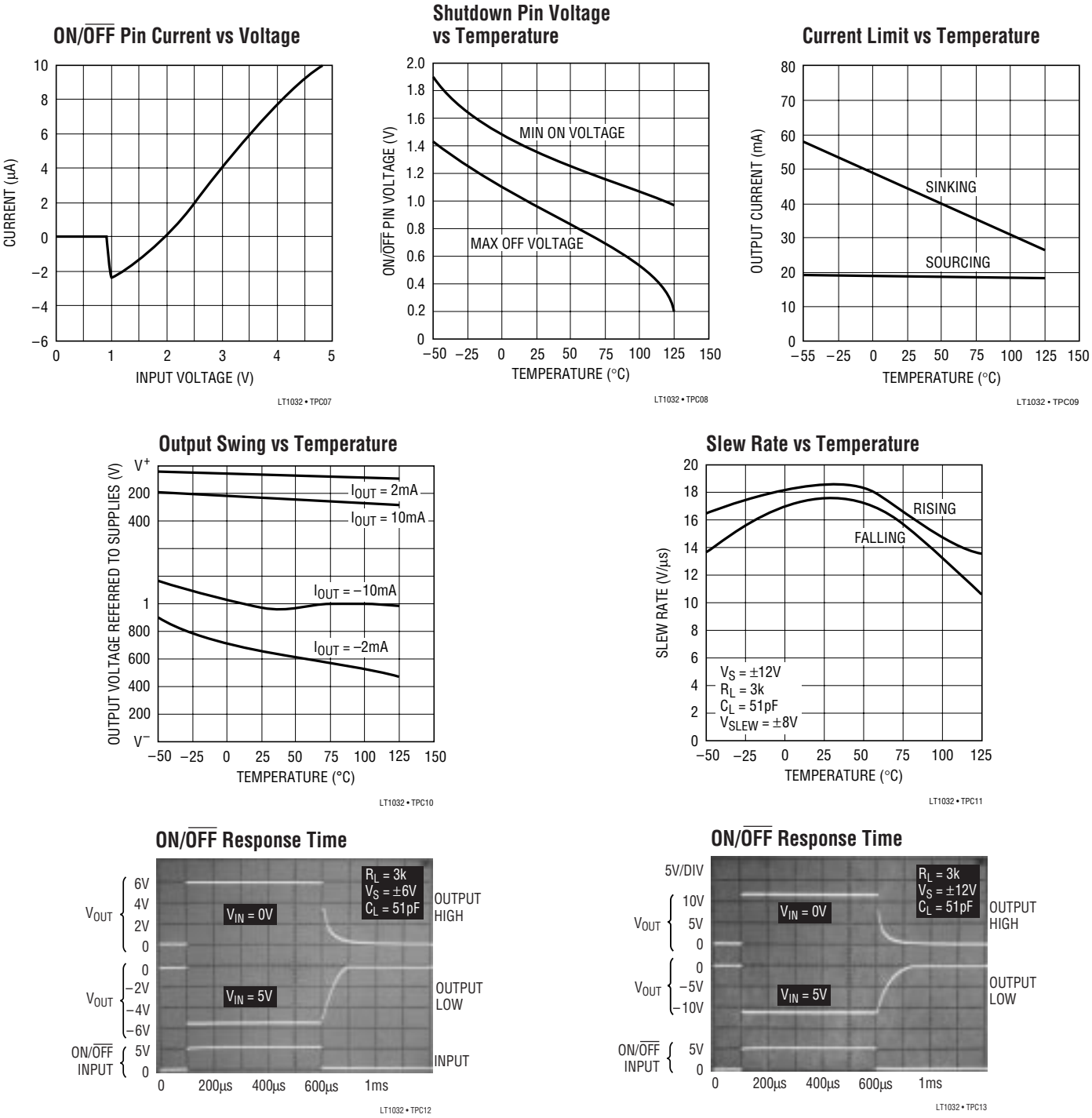
LT1032 • TPC05

Response Pin Leakage vs Temperature (Device Off)

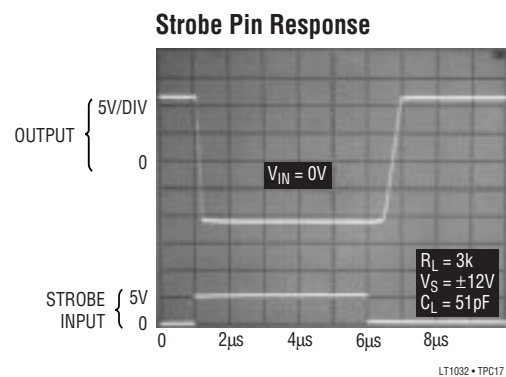
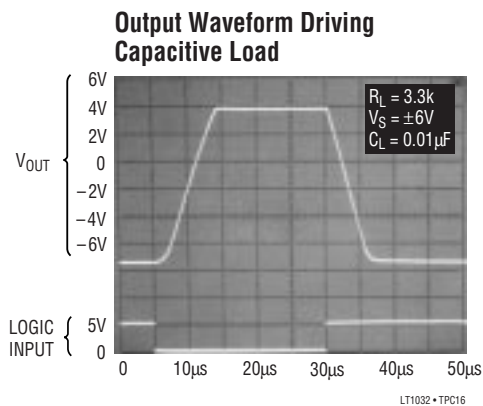
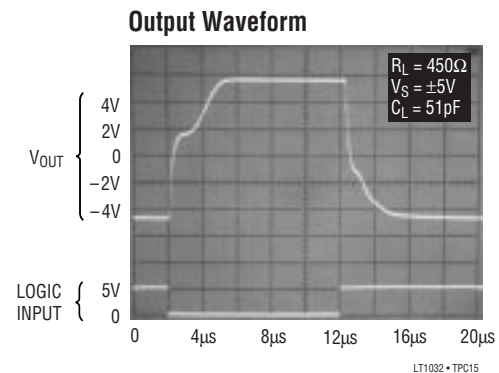
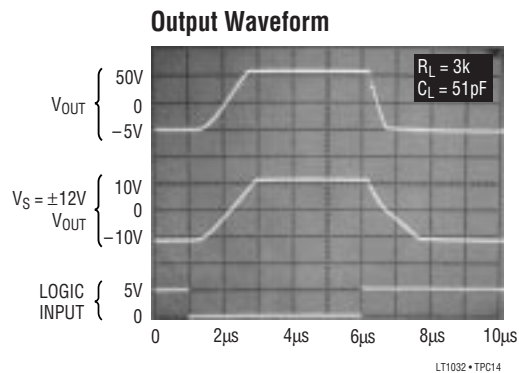


LT1032 • G06

TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS



PIN FUNCTIONS

V_{EE} (J, N & S: Pin 1): Minus Supply. Operates $-2V$ to $-15V$.

(J & N: Pins 2, 5, 9, 12/S: Pins 2, 5, 11, 14): Logic Input. Operates properly on TTL or CMOS levels. Output valid from $(V^- + 2V) \leq V_{IN} \leq 15V$. Connect to ground when not used.

(J & N: Pins 3, 6, 8, 11/S: Pins 3, 6, 10, 13): Output. Line drive output.

ON/OFF (J, N & S: Pin 4): Shuts down entire circuit. Cannot be left open. For "normally on" operation, connect to V^+ .

GND (J, N & S: Pin 7): Ground. Ground must be more positive than V^- .

Response Control (J & N: Pin 10/S: Pin 12): Allows limited change of slew rate. Leave open when not used.

Strobe (J & N: Pin 13/S: Pin 15): Forces all outputs low. Drive with 3V.

V_{CC} (J & N: Pin 14/S: Pin 15): Positive Supply. Operates 5V to 15V.

APPLICATIONS INFORMATION

Application Hints

The LT1032 is exceptionally easy to use when compared to older drivers. Operating supply voltage can be as low as $\pm 3\text{V}$ or as high as $\pm 15\text{V}$. Input levels are referred to ground.

The logic inputs are internally set at TTL levels. Outputs are valid for input voltages from 1V above V^- to 25V. Driving the logic inputs to V^- turns off the output stage. The $\text{ON}/\overline{\text{OFF}}$ control completely turns off all supply current of the LT1032. The levels required to drive the device on or off are set by internal emitter-based voltages. Since the current into the $\text{ON}/\overline{\text{OFF}}$ pin is so low, TTL or CMOS drivers have no problem controlling the device.

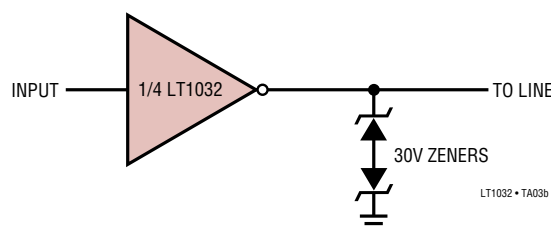
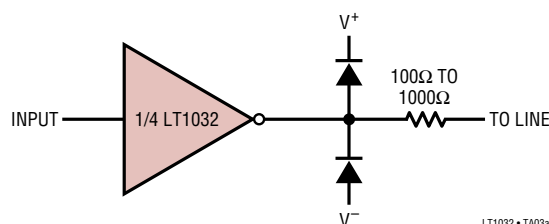
The strobe pin is not fully logic compatible. The impedance of the strobe pin is about $2\text{k}\Omega$ to ground. Driving the strobe pin positive forces the output stages low—even if the device is shut off. Under worst case conditions, 3V minimum at 2mA are needed driving the strobe pin to insure strobing.

The response pin can be used to make some adjustment in slew rate. A resistor can be connected between the response pin and the power supplies to drive $50\mu\text{A}$ to $100\mu\text{A}$ into the pin. The response pin is a low impedance point operating at about 0.75V above ground. For supply voltage up to $\pm 6\text{V}$, current is turned off when the device is turned off. For higher supply voltages, a Zener should be connected in series with the resistor to limit the voltage applied to the response pin to 6V. Also, for temperatures above 100°C , using the response pin is not recommended. The leakage current into the response pin at high temperatures is excessive.

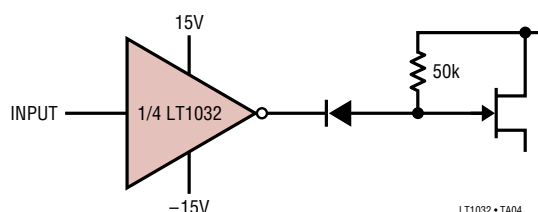
Outputs are well protected against shorts or externally applied voltage. Tested limits are $\pm 30\text{V}$, but the device can withstand external voltages up to breakdown of the transistors (typically about 50V). The LT1032 is usually immune to ESD up to 2500V on the outputs with no damage.

TYPICAL APPLICATIONS

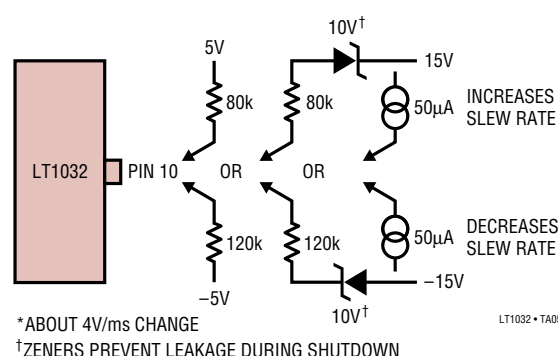
Protecting Against More Than $\pm 30\text{V}$ Output Overload



FET Driver



Slew Rate Adjustment*

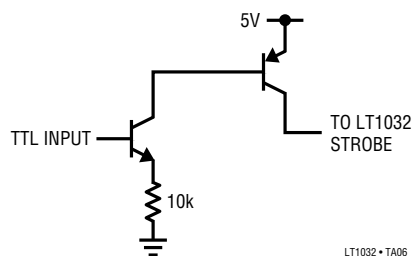


* ABOUT 4V/ms CHANGE

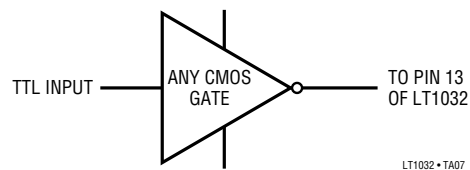
† ZENERS PREVENT LEAKAGE DURING SHUTDOWN

TYPICAL APPLICATIONS

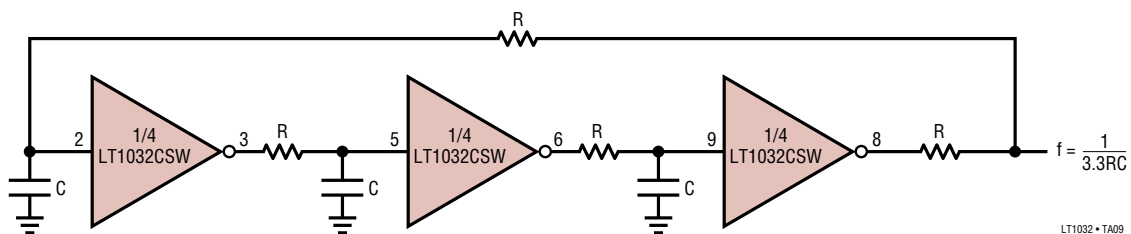
TTL/CMOS Compatible Strobe



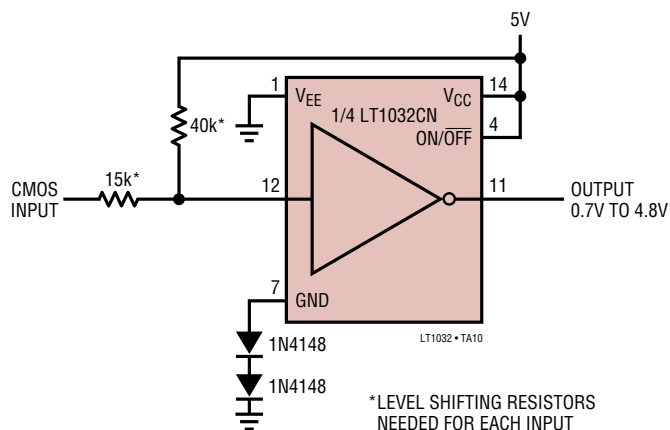
Strobing with CMOS



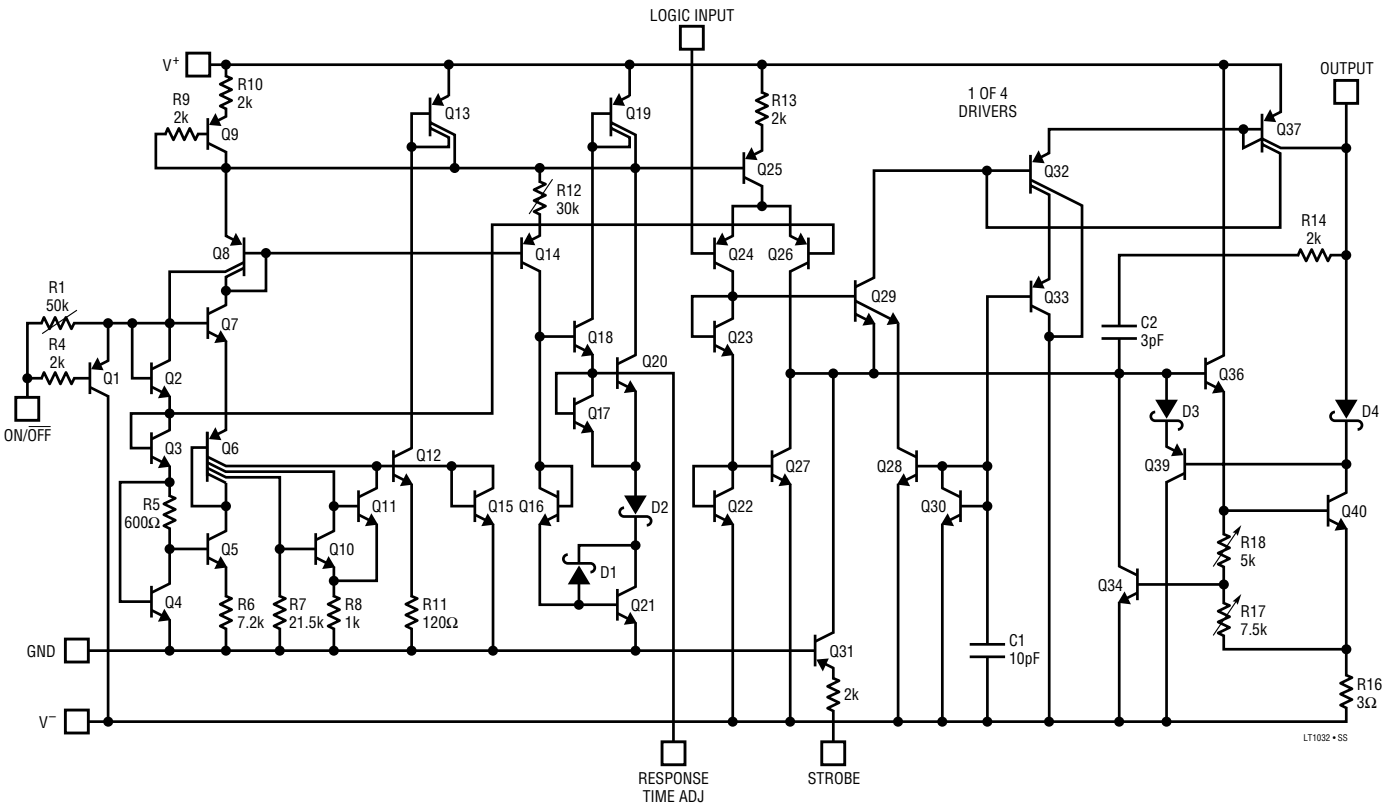
Phase Shift Oscillator



Operating from a Single 5V Supply

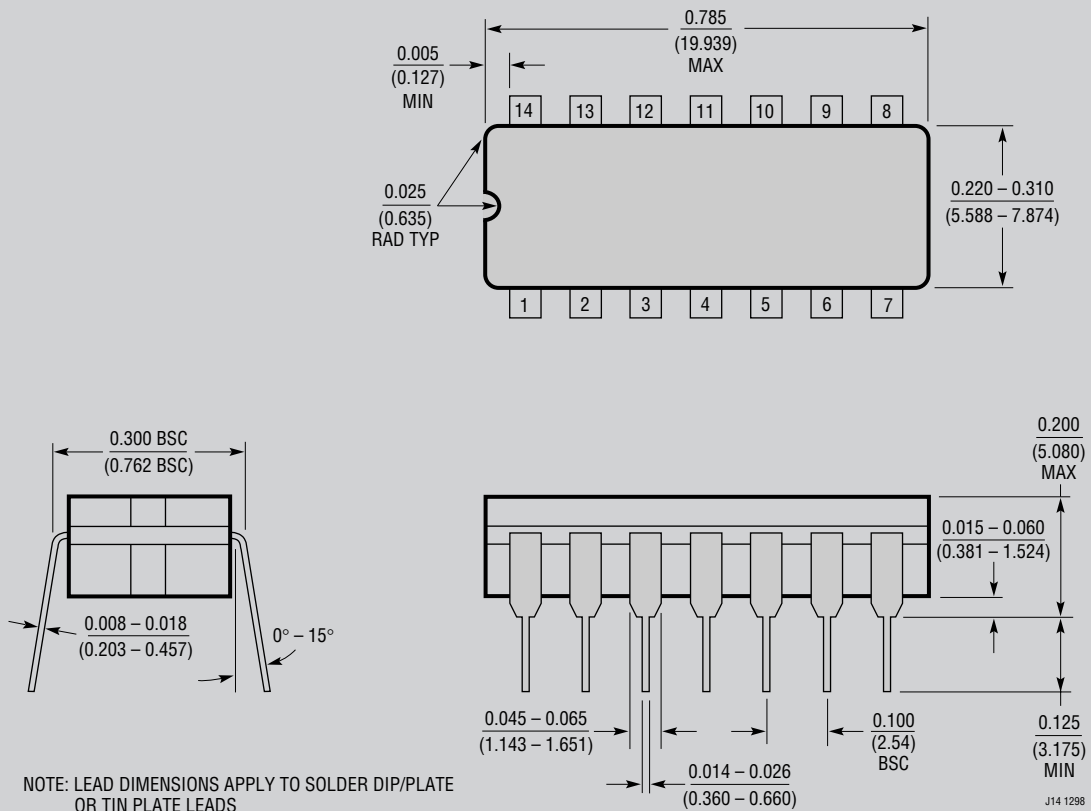


SIMPLIFIED SCHEMATIC



PACKAGE DESCRIPTION

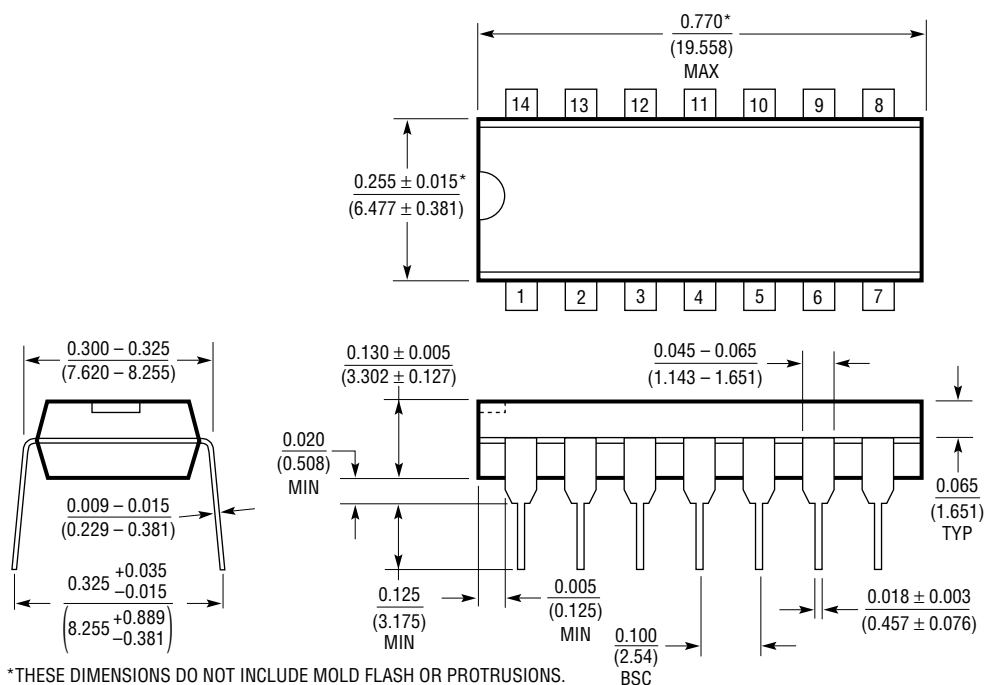
J Package
14-Lead Cerdip (Narrow .300 Inch, Hermetic)
 (Reference LTC DWG # 05-08-1110)



OBSOLETE PACKAGE

PACKAGE DESCRIPTION

N Package
14-Lead PDIP (Narrow .300 Inch)
 (Reference LTC DWG # 05-08-1510)

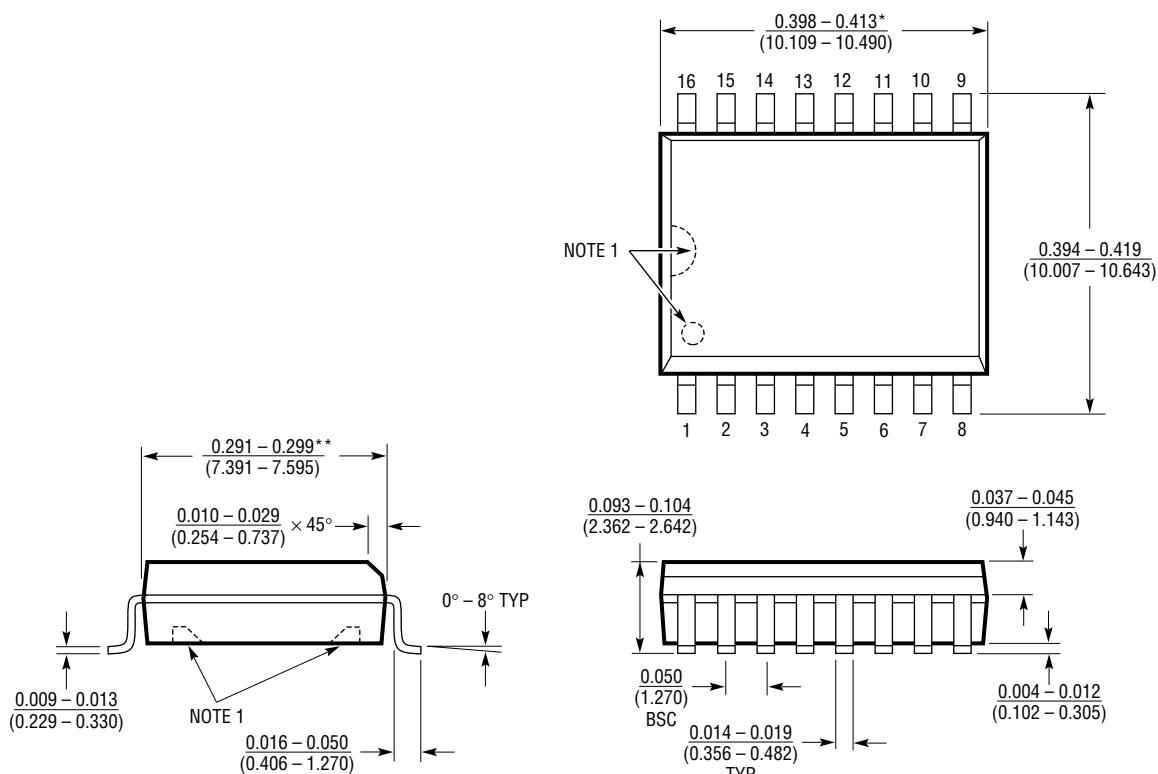


*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
 MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

N14 1098

PACKAGE DESCRIPTION

SW Package
16-Lead Plastic Small Outline (Wide .300 Inch)
 (Reference LTC DWG # 05-08-1620)



NOTE:

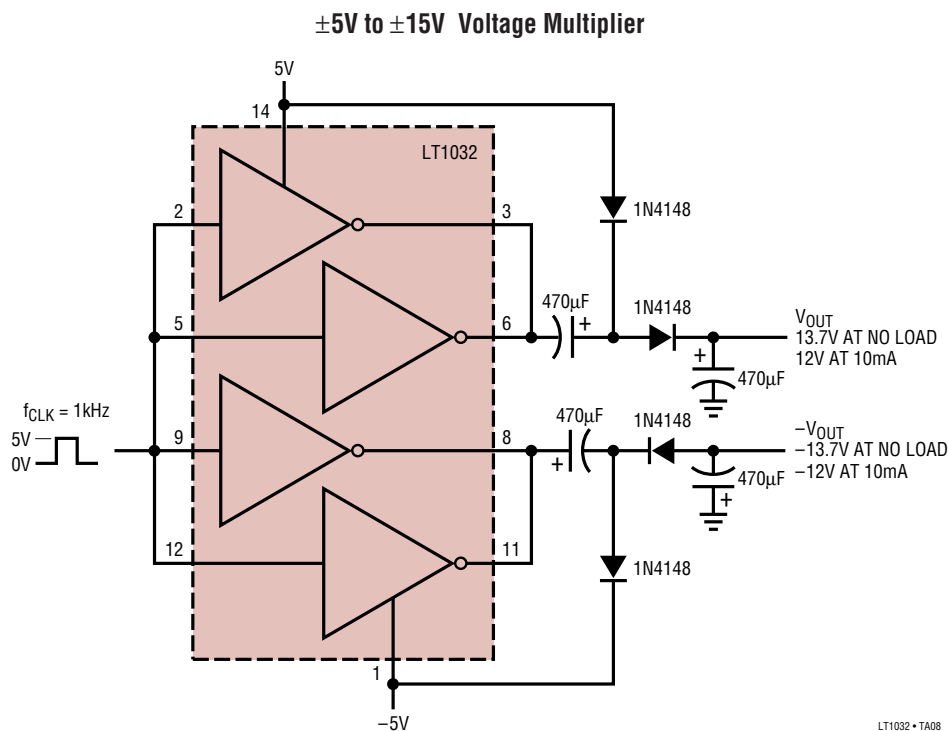
1. PIN 1 IDENT, NOTCH ON TOP AND CAVITIES ON THE BOTTOM OF PACKAGES ARE THE MANUFACTURING OPTIONS. THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS

*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

**DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

S16 (WIDE) 1098

TYPICAL APPLICATION



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1039A	Triple RS232 Driver/Receiver with Shutdown	±15kV ESD Protection
LT1130A/LT1140A	5V Supply, Multiple RS232 Driver/Receiver	±15kV 3 Driver/5 Receiver (LT1133A, LT1137A, LT1141A)
LT1780/LT1781A	5V Supply, 2 Driver/2 Receiver RS232	±15kV Pin Compatible Upgrade to LT1080/LT1081 and LT1180A/LT1181A