

TOSHIBA TRANSISTOR SILICON NPN TRIPLE DIFFUSED MESA TYPE

## 2SD2498

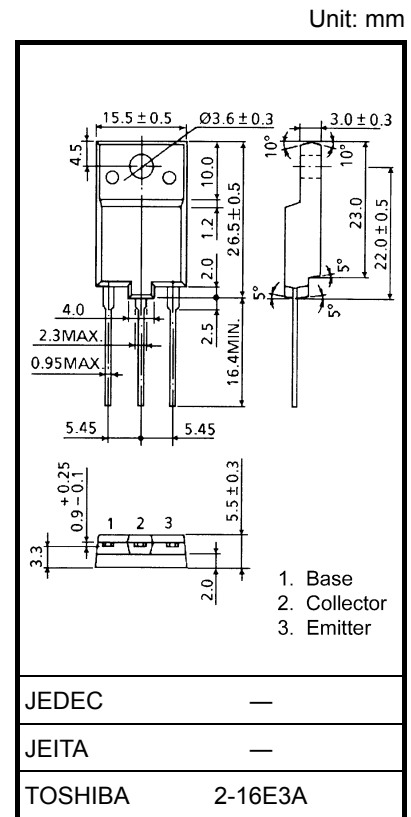
HORIZONTAL DEFLECTION OUTPUT FOR HIGH  
RESOLUTION DISPLAY, COLOR TV

HIGH SPEED SWITCHING APPLICATIONS

- High Voltage :  $V_{CBO} = 1500 \text{ V}$
- Low Saturation Voltage :  $V_{CE(sat)} = 5 \text{ V (Max.)}$
- High Speed :  $t_f = 0.4 \mu\text{s (Typ.)}$
- Collector Metal (Fin) is Fully Covered with Mold Resin

### ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ )

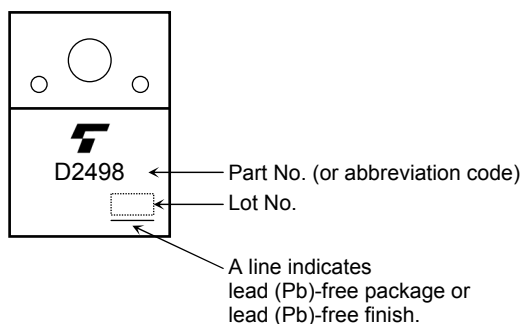
CHARACTERISTIC		SYMBOL	RATING	UNIT
Collector-Base Voltage		$V_{CBO}$	1500	V
Collector-Emitter Voltage		$V_{CEO}$	600	V
Emitter-Base Voltage		$V_{EBO}$	5	V
Collector Current	DC	$I_C$	6	A
	Pulse	$I_{CP}$	12	
Base Current		$I_B$	3	A
Collector Power Dissipation		$P_C$	50	W
Junction Temperature		$T_j$	150	$^\circ\text{C}$
Storage Temperature Range		$T_{stg}$	$-55 \sim 150$	$^\circ\text{C}$



Weight: 5.5 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.  
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

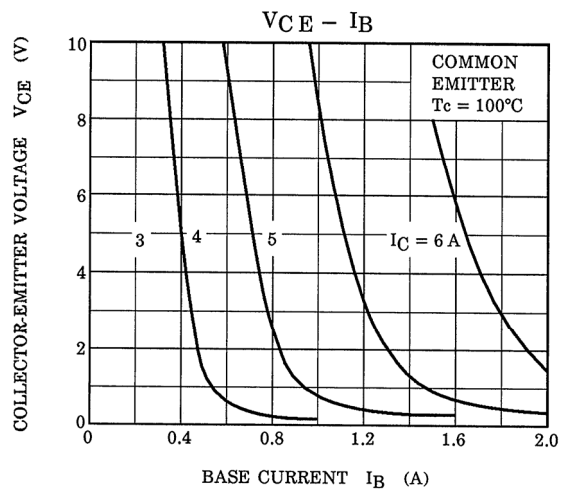
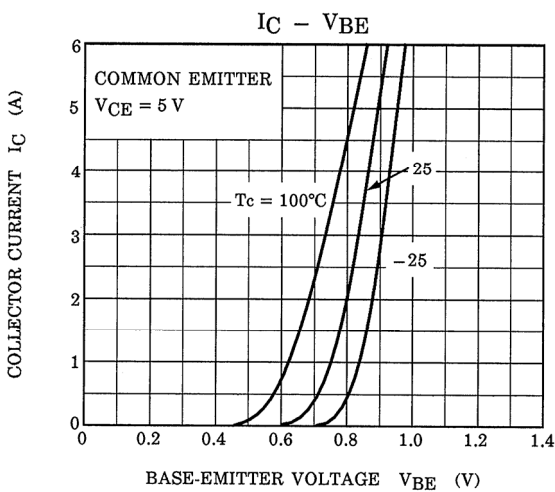
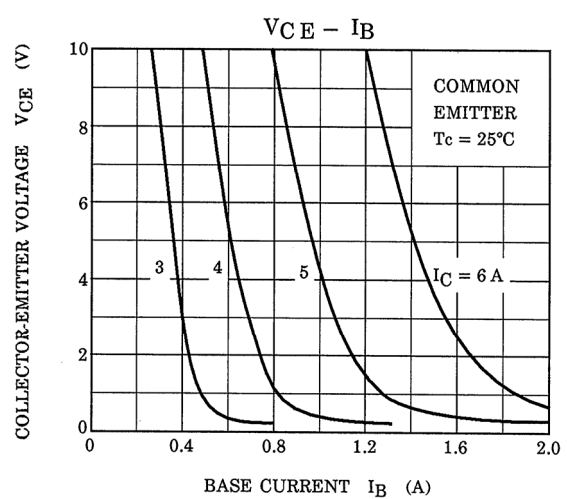
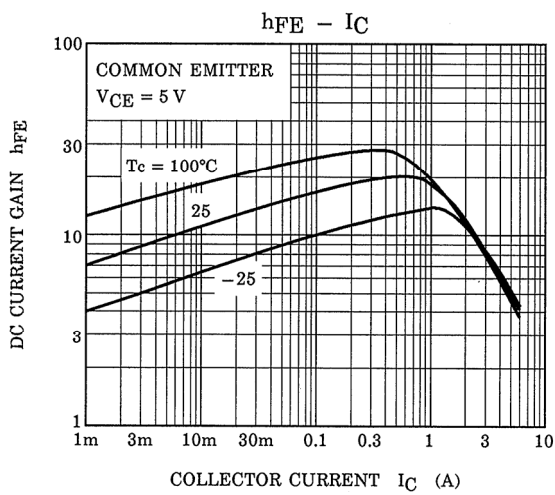
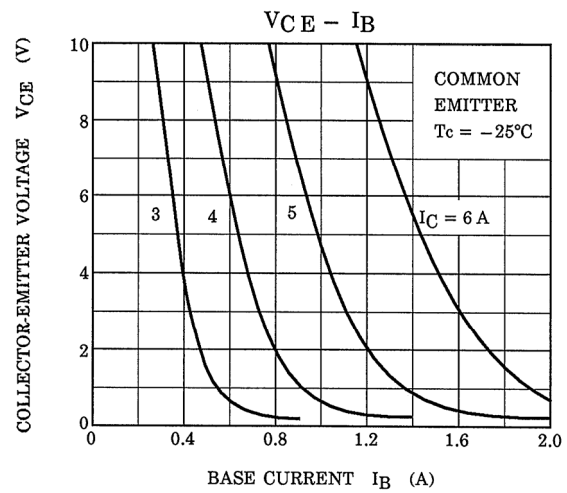
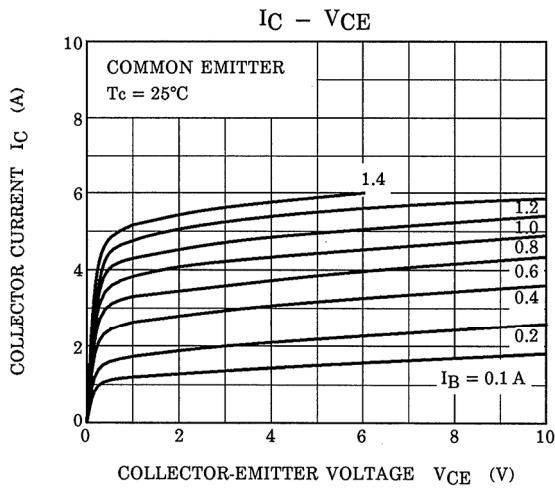
### MARKING

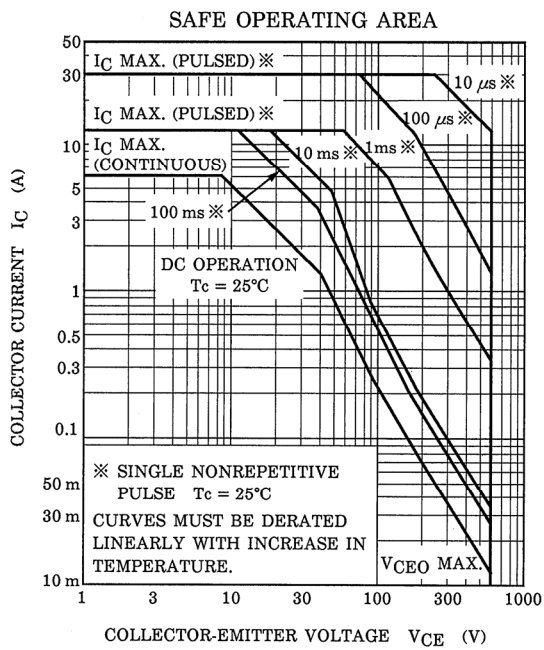
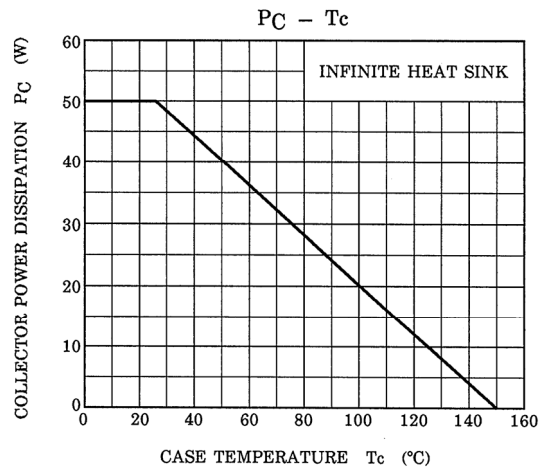
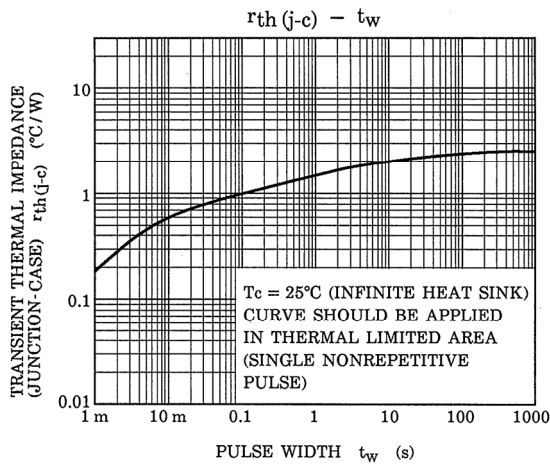


CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Collector Cut-off Current		$I_{CBO}$	$V_{CB} = 1500 \text{ V}, I_E = 0$	—	—	1	mA
Emitter Cut-off Current		$I_{EBO}$	$V_{EB} = 5 \text{ V}, I_C = 0$	—	—	10	$\mu\text{A}$
Collector-Emitter Breakdown Voltage		$V_{(BR) \text{ CEO}}$	$I_C = 10 \text{ mA}, I_B = 0$	600	—	—	V
DC Current Gain		$h_{FE} (1)$	$V_{CE} = 5 \text{ V}, I_C = 1 \text{ A}$	10	—	30	—
		$h_{FE} (2)$	$V_{CE} = 5 \text{ V}, I_C = 4 \text{ A}$	5	—	9	
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$	$I_C = 4 \text{ A}, I_B = 0.8 \text{ A}$	—	—	5	V
Base-Emitter Saturation Voltage		$V_{BE(sat)}$	$I_C = 4 \text{ A}, I_B = 0.8 \text{ A}$	—	0.9	1.2	V
Transition Frequency		$f_T$	$V_{CE} = 10 \text{ V}, I_C = 0.1 \text{ A}$	—	2	—	MHz
Collector Output Capacitance		$C_{ob}$	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	—	95	—	pF
Switching Time (Fig.1)	Storage Time	$t_{stg}$	$I_{CP} = 4 \text{ A}, I_{B1}(\text{end}) = 0.8 \text{ A}$	—	7	10	$\mu\text{s}$
	Fall Time	$t_f$	$f_H = 15.75 \text{ kHz}$	—	0.4	0.7	

The circuit diagram shows a 2SC2482 transistor configured as an inverter. The input is a square wave with a pulse width of  $25\ \mu\text{s}$  and a period of  $63.5\ \mu\text{s}$ . The base is biased using a  $3\text{ k}\Omega$  resistor and a  $100\ \mu\text{F}$  capacitor connected to  $V_{DD}$ . A  $250\ \Omega$  resistor is connected between the base and emitter. The base-emitter junction is bypassed with a  $10\ \mu\text{H}$  inductor and a  $100\ \mu\text{F}$  capacitor. The collector is connected to  $V_{CC}$  through a  $1\text{ M}\Omega$  resistor and a  $0.012\ \mu\text{F}$  capacitor. The output is taken from the collector. The timing waveforms show the base current  $I_{B1}$  and  $I_{B2}$  during the input pulse, and the collector current  $I_{CP}$  during the output transition. The storage time  $t_{stg}$  and fall time  $t_f$  are indicated on the collector current waveform.

$$dI_B/dt = \frac{I_{B1} + I_{B2}}{t_{stg}} \text{ (A/}\mu\text{s)}$$





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