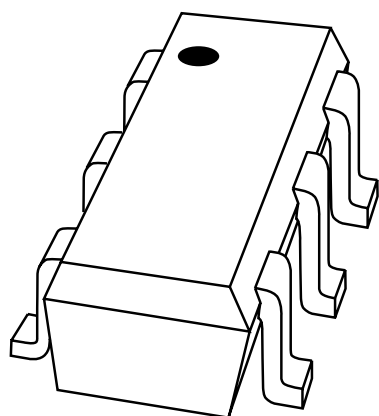


DATA SHEET



PUMH10

NPN resistor-equipped transistors

Product specification

2000 Aug 01

NPN resistor-equipped transistors

PUMH10

FEATURES

- Transistors with built-in bias resistors R1 and R2 (typ. 2.2 and 47 kΩ)
- No mutual interference between the transistors
- Reduces number of components and board space
- Simplification of circuit design.

APPLICATION

- Especially suitable for space reduction in portable equipment
- Inverter circuit configurations without use of external resistors.

DESCRIPTION

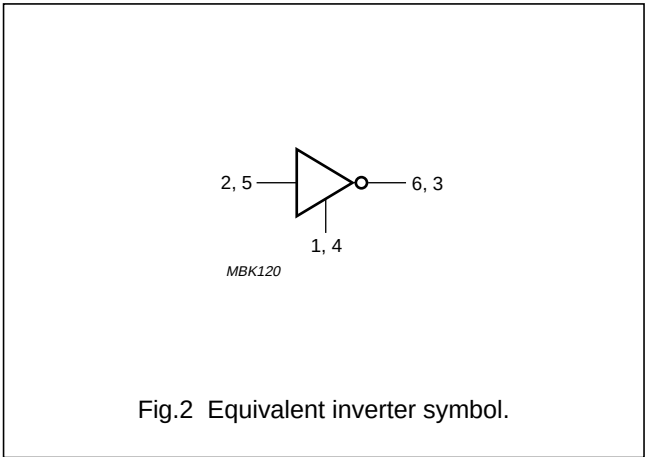
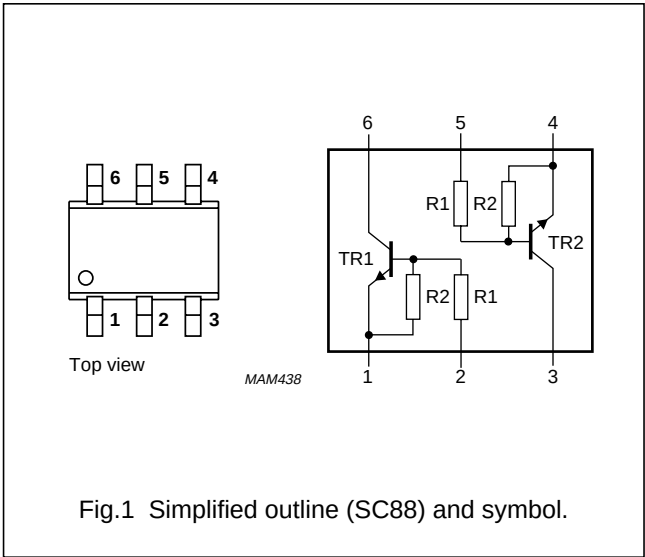
NPN resistor-equipped transistors in an SC88 (SOT363) plastic package.

PINNING

PIN	DESCRIPTION
1 and 4	emitter TR1; TR2
2 and 5	base TR1; TR2
6 and 3	collector TR1; TR2

MARKING

TYPE NUMBER	MARKING CODE
PUMH10	Ht0



NPN resistor-equipped transistors

PUMH10

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transistor					
V_{CBO}	collector-base voltage	open emitter	–	50	V
V_{CEO}	collector-emitter voltage	open base	–	50	V
V_{EBO}	emitter-base voltage	open collector	–	10	V
V_i	input voltage				
	positive		–	+12	V
	negative		–	–5	V
I_o	output current (DC)		–	100	mA
I_{CM}	peak collector current		–	100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$; note 1	–	200	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C
Per device					
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$; note 1	–	300	mW

Note

1. See standard mounting conditions SC88.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	from junction to ambient	in free air; note 1	416	K/W

Note

1. See standard mounting conditions SC88.

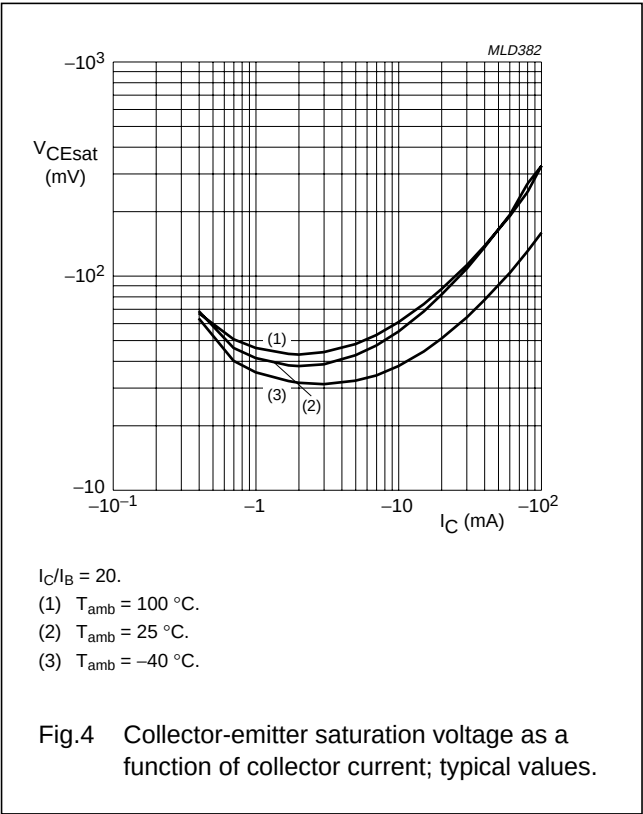
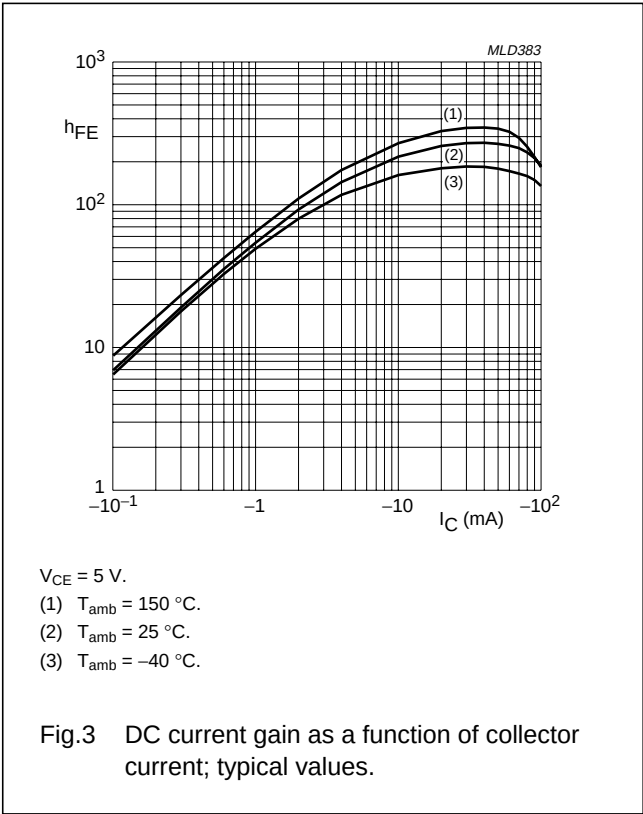
NPN resistor-equipped transistors

PUMH10

CHARACTERISTICS

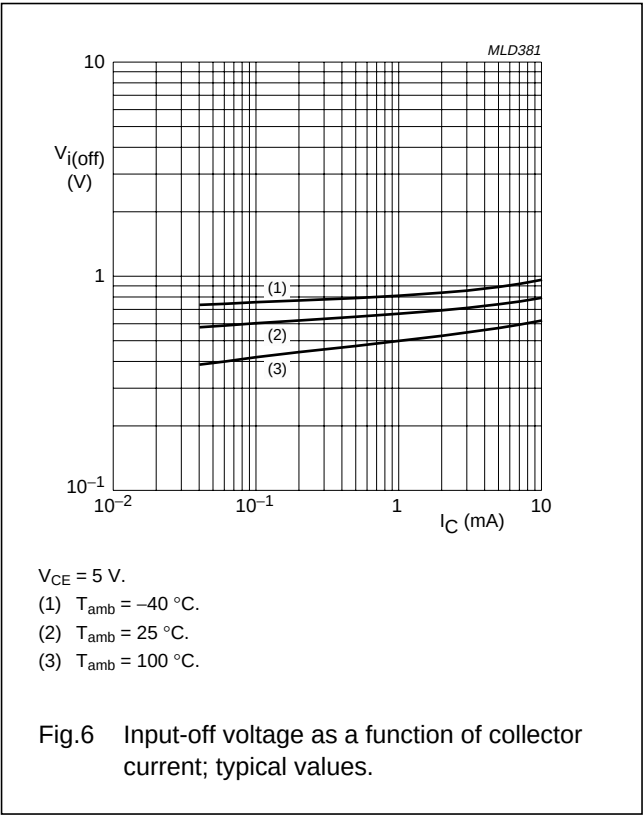
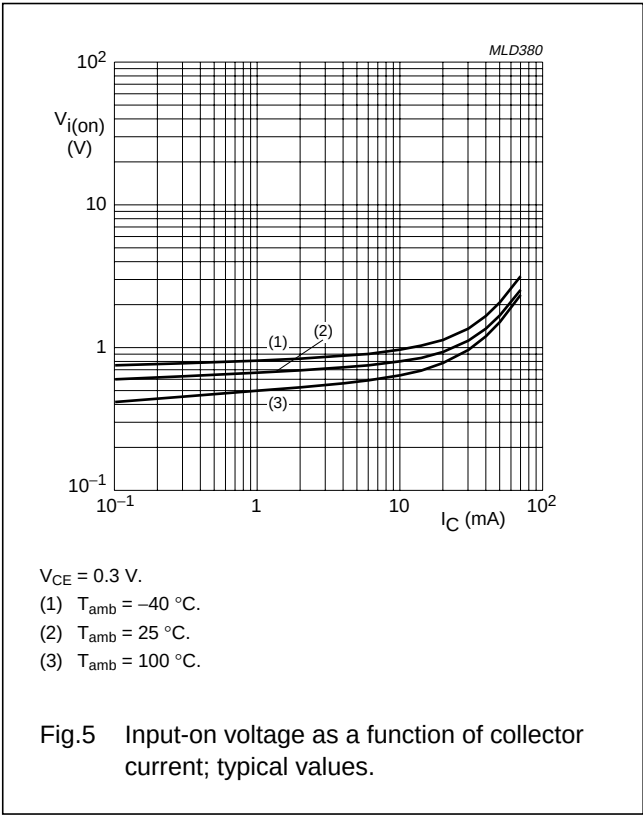
T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per transistor						
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = 50 V	–	–	100	nA
I _{CEO}	collector-emitter cut-off current	I _B = 0; V _{CE} = 30 V	–	–	1	μA
		I _B = 0; V _{CE} = 30 V; T _j = 150 °C	–	–	50	μA
I _{EBO}	emitter-base cut-off current	I _C = 0; V _{EB} = 5 V	–	–	180	μA
h _{FE}	DC current gain	I _C = 10 mA; V _{CE} = 5 V	100	–	–	
V _{CEsat}	saturation voltage	I _C = 5 mA; I _B = 0.25 mA	–	–	100	mV
V _{i(off)}	input-off voltage	I _C = 100 μA; V _{CE} = 5 V	–	0.6	0.5	V
V _{i(on)}	input-on voltage	I _C = 5 mA; V _{CE} = 0.3 V	1.1	0.75	–	V
R1	input resistor		1.54	2.2	2.86	kΩ
$\frac{R2}{R1}$	resistor ratio		17	21	26	kΩ
C _c	collector capacitance	I _E = I _e = 0; V _{CB} = 10 V; f = 1 MHz	–	–	2.5	pF



NPN resistor-equipped transistors

PUMH10



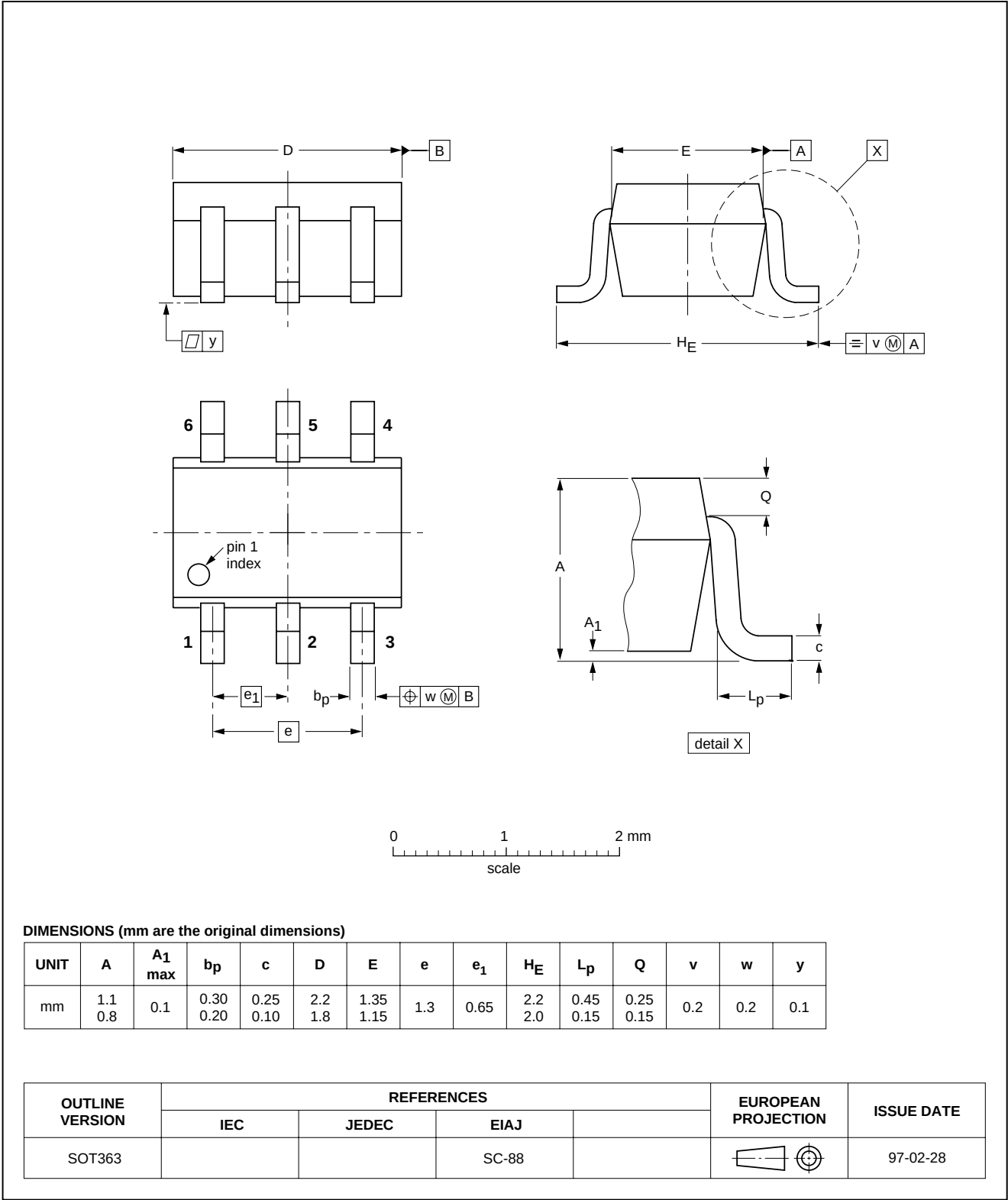
NPN resistor-equipped transistors

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PACKAGE OUTLINE

Plastic surface mounted package; 6 leads

SOT363



NPN resistor-equipped transistors

PUMH10

DATA SHEET STATUS

DATA SHEET STATUS	PRODUCT STATUS	DEFINITIONS ⁽¹⁾
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

Note

1. Please consult the most recently issued data sheet before initiating or completing a design.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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