

DUAL OPERATIONAL AMPLIFIER

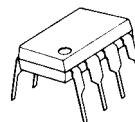
■ GENERAL DESCRIPTION

NJM2115 is a low operating Voltage ($\pm 1.0V$ min.) and low saturation output voltage ($\pm 2.0V_{P-P}$ at supply voltage $\pm 2.5V$) operational amplifier. It is applicable to HANDY TYPE CD, RADIO CASSETTE CD, and PORTABLE DAT, that are digital audio apparatus which require the 5V single supply operation and high output voltage. The NJM2115 is improved version of the NJM2100 about BIAS CIRCUIT. So, NJM2115 is low saturation compared to the NJM2100 under the condition of low supply voltage ($< \pm 2.5V$). The NJM2115 is stable about the oscillation compared to the NJM2100 under the condition of $V^+/V^- > 2.5V$.

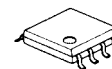
■ FEATURES

- Operating Voltage ($\pm 1V \sim \pm 7V$)
- Low Saturation Output Voltage ($\pm 2.0V_{P-P}$ @ $V^+ = \pm 2.5V$)
- Slew Rate ($4V/\mu s$ typ.)
- Unity Gain Bandwidth ($12MHz$ typ.)
- Package Outline DIP8, DMP8, SIP8, SSOP8
- Bipolar Technology

■ PACKAGE OUTLINE



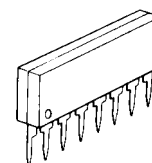
NJM2115D



NJM2115M

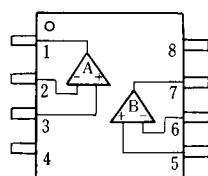


NJM2115V

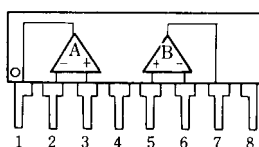


NJM2115L

■ PIN CONFIGURATION



NJM2115D
NJM2115M
NJM2115V

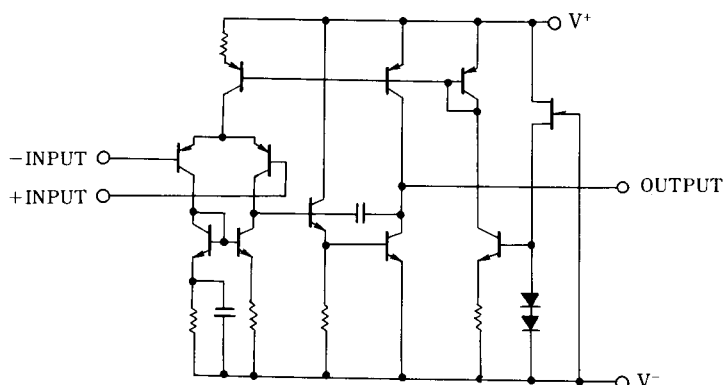


NJM2115L

PIN FUNCTION

1. A OUTPUT
2. A -INPUT
3. A +INPUT
4. V^-
5. B +INPUT
6. B -INPUT
7. B OUTPUT
8. V^+

■ EQUIVALENT CIRCUIT (1/2 Shown)



NJM2115

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V^+/V^-	± 7.0	V
Differential Input Voltage	V_{ID}	± 14	V
Power Dissipation	P_D	(DIP8) 500 (DMP8) 300 (SIP8) 800 (SSOP8) 250	mW
Operating Temperature Range	T_{opr}	-40~+85	°C
Storage Temperature Range	T_{stg}	-40~+125	°C

■ ELECTRICAL CHARACTERISTICS

($V^+/V^- = \pm 2.5V$, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V_{IO}	$R_S \leq 10k\Omega$	-	1	6	mV
Input Bias Current	I_B		-	100	300	nA
Large Signal Voltage Gain	A_V	$R_L \geq 10k\Omega$	60	80	-	dB
Maximum Output Voltage Swing	V_{OM}	$R_L \geq 2.5k\Omega$	± 2	± 2.2	-	V
Input Common Mode Voltage Range	V_{ICM}		± 1.5	-	-	V
Common Mode Rejection Ratio	CMR		60	74	-	dB
Supply Voltage Rejection Ratio	SVR		60	80	-	dB
Operating Current	I_{CC}	$V_{IN}=0, R_L=\infty$	-	3.5	5	mA
Slew Rate	SR	$A_V=1, V_{IN}=\pm 1V$	-	4	-	V/ μs
Gain Bandwidth product	GB	$f=10kHz$	-	12	-	MHz

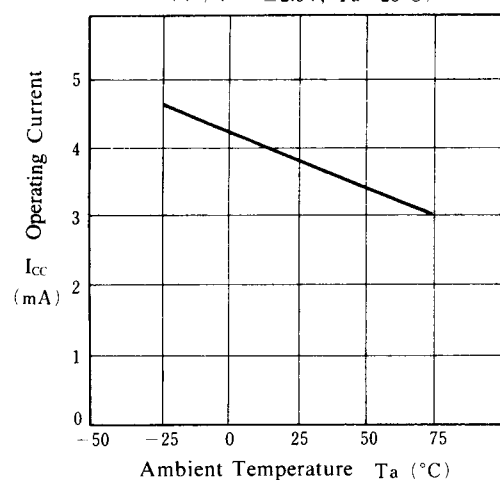
(Note1) Applied circuit voltage gain is desired to be operated within the range of 3dB to 30 dB.

(Note2) Special care being required for input common mode voltage range and the oscillation due to the capacitive load when operating on voltage follower.

■ TYPICAL CHARACTERISTICS

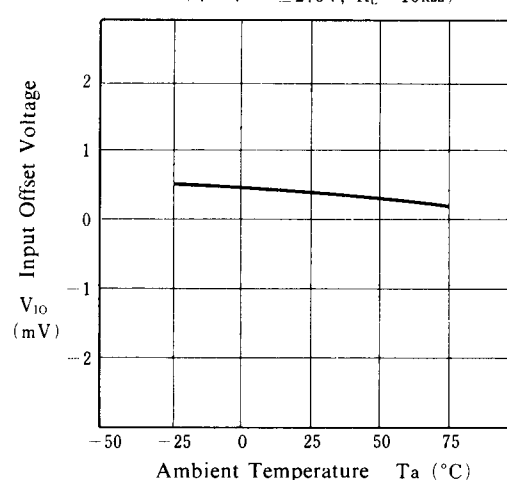
Operating Current vs. Temperature

($V^+/V^- = \pm 2.5V$, $T_a = 25^\circ C$)



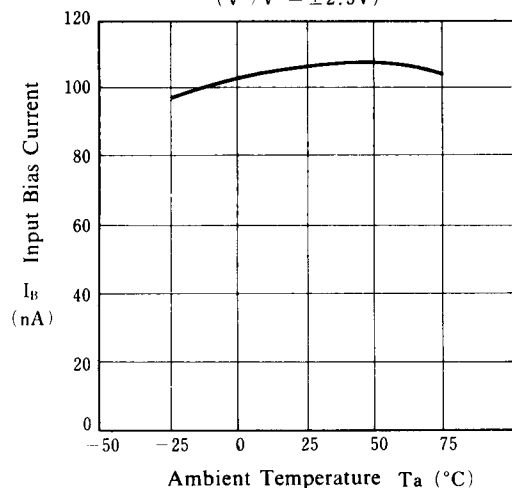
Input Offset Voltage vs. Temperature

($V^+/V^- = \pm 2.5V$, $R_L = 10k\Omega$)



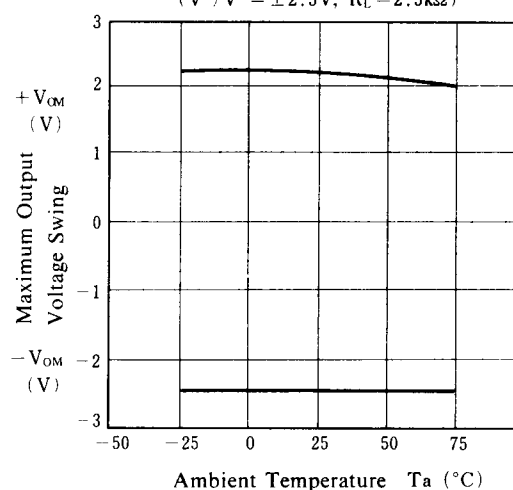
Input Bias Current vs. Temperature

($V^+/V^- = \pm 2.5V$)



Maximum Output Voltage Swing vs. Temperature

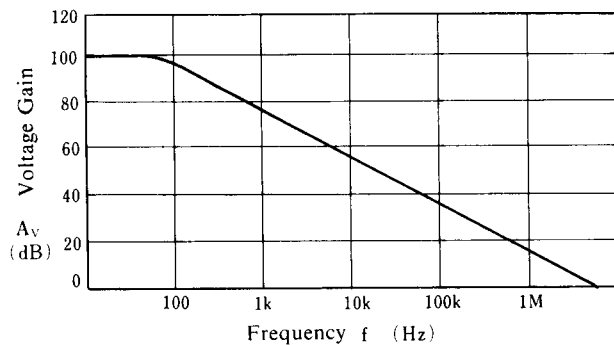
($V^+/V^- = \pm 2.5V$, $R_L = 2.5k\Omega$)



■ TYPICAL CHARACTERISTICS

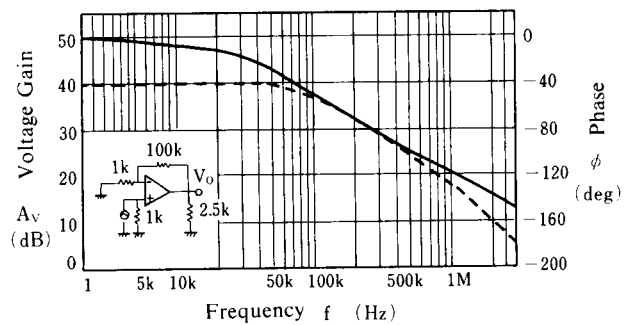
Voltage Gain vs. Frequency

($V^+/V^- = \pm 12.5V$, $T_a = 25^\circ C$)



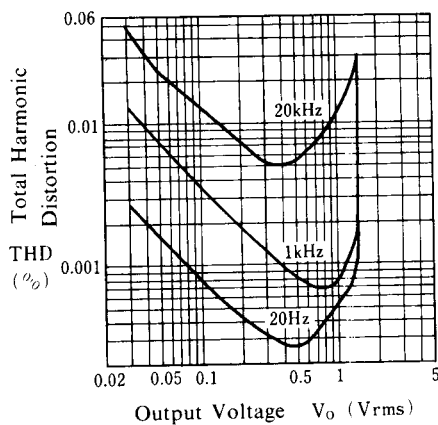
Voltage Gain, Phase vs. Frequency

($T_a = 25^\circ C$, $V^+/V^- = \pm 2.5V$)



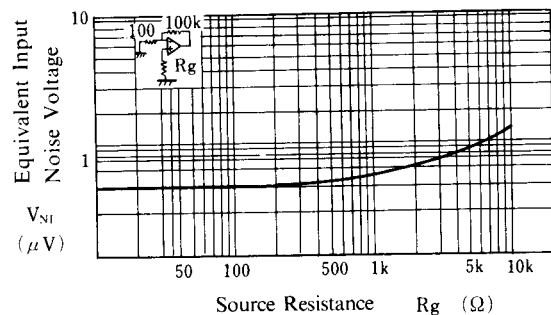
Total Harmonic Distortion vs. Output Voltage

($V^+/V^- = \pm 3V$, $R_L = 2.4k\Omega$, $\text{Gain} = 10\text{dB}$, $T_a = 25^\circ C$)



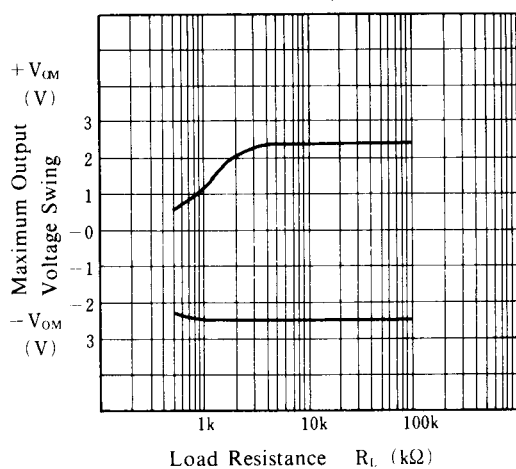
Equivalent Input Noise Voltage vs. Source Resistance

($V^+/V^- = \pm 3V$, $T_a = 25^\circ C$)



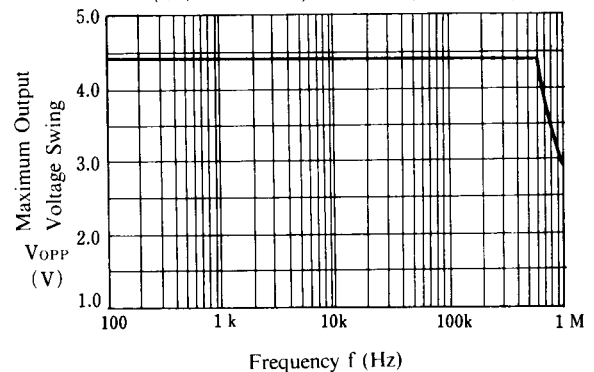
Maximum Output Voltage Swing vs. Load Resistance

($V^+/V^- = \pm 2.5V$, $T_a = 25^\circ C$)



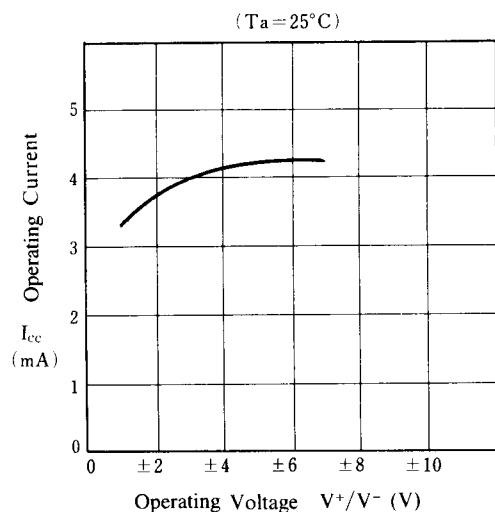
Maximum Output Voltage Swing vs. Frequency

($V^+/V^- = \pm 2.5V$, $R_L = 2.5k\Omega$, $T_a = 25^\circ C$)

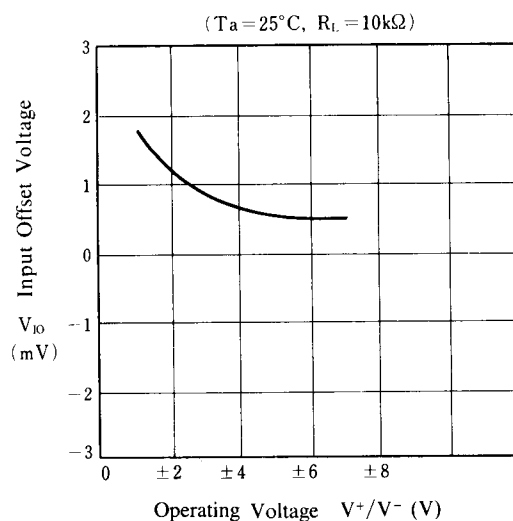


■ TYPICAL CHARACTERISTICS

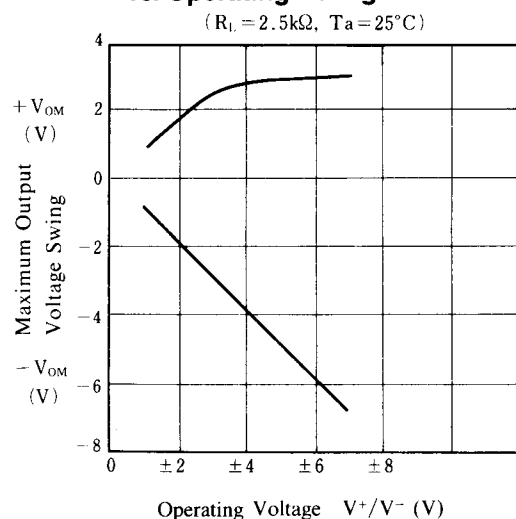
Operating Current vs. Operating Voltage



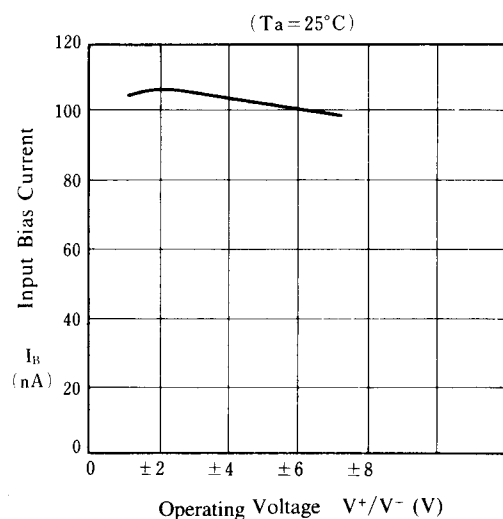
Input Offset Voltage vs. Operating Voltage



Maximum Output Voltage Swing vs. Operating Voltage



Input Bias Current vs. Operating Voltage



[CAUTION]

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