

Typical unit

## FEATURES

- Wide Vout trim range (see specifications)
- Industry standard "half brick" package
- High efficiency: up to 93.5%
- Outstanding thermal performance
- Standard baseplate for conduction cooled applications
- No output reverse conduction
- Input to output isolation, 2250Vdc (Basic)
- Input under-voltage lockout
- On/off control (positive or negative logic)
- Output over-voltage protection
- Thermal shutdown
- Output short circuit protection (hiccup technique)
- Certified to UL/EN 60950-1, CSA-C22.2 No. 60950-1, 2nd edition safety approvals

## PRODUCT OVERVIEW

For applications requiring improved electrical and thermal performance, consider Murata's new PAH series "half brick" DC-DC power converters. These compact modules measure 2.3" X 2.4" X 0.5" (58 X 61 X 12.7mm) and offer the industry-standard Half Brick footprint.

The PAH Series is ideal for power amplifier applications, wireless networks, and telecom applications. The baseplate provides a means for conduction cooling in demanding thermal environment conditions.

The module provides a 28Vdc (@16A) output and accepts a wide input voltage range of 36-75Vdc. The PAH topology offers high efficiency (up to 93.5%), tight line and load regulation, low ripple/noise, and a fast dynamic load response. A single-board, highly optimized thermal design contributes to the superior thermal performance.

These half-bricks provide output trim, sense pins, and primary side on/off control. Standard features also include input under-voltage shutdown, output over-voltage protection, output short-circuit/ current limiting protection, and thermal shutdown.

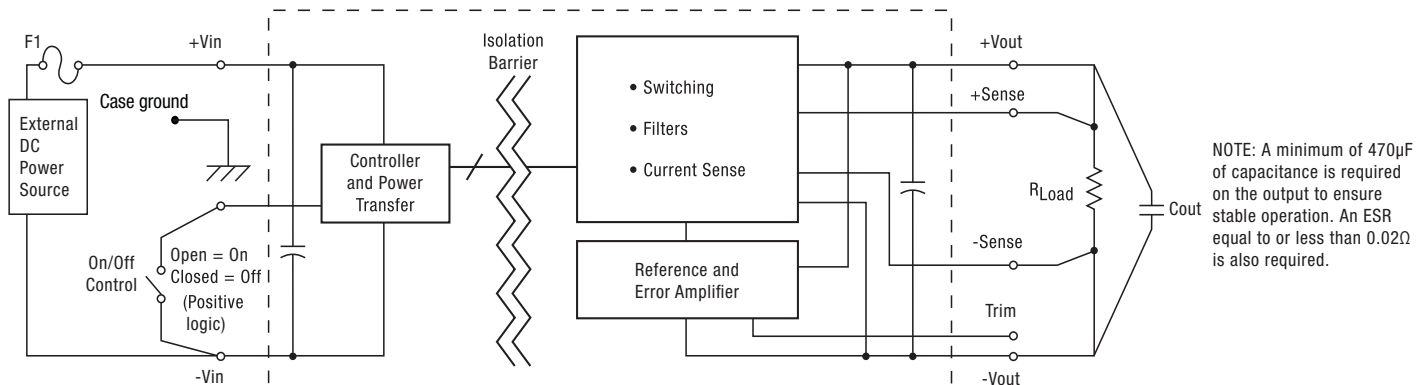


Figure 1. Simplified Schematic  
Typical topology is shown. Some models may vary slightly.



## PERFORMANCE SPECIFICATIONS SUMMARY AND ORDERING GUIDE

Root Model ①	Output							Input				Efficiency		Dimensions (Inches)
	V <sub>OUT</sub> (Volts)	I <sub>OUT</sub> (Amps, Max.)	Power	R/N (mV pk-pk)		Regulation (Max.)		V <sub>IN</sub> Nom. (Volts)	Range (Volts)	I <sub>IN</sub> , no load (mA)	I <sub>IN</sub> , full load (Amps)	Min. ④	Typ.	
			(Watts)	Typ.	Max.	Line	Load							
PAH-28/16-D48	28	16	450	100	300	±0.25%	±0.25%	48	36-75	80	9.98	93%	93.5%	2.3x2.4x0.5

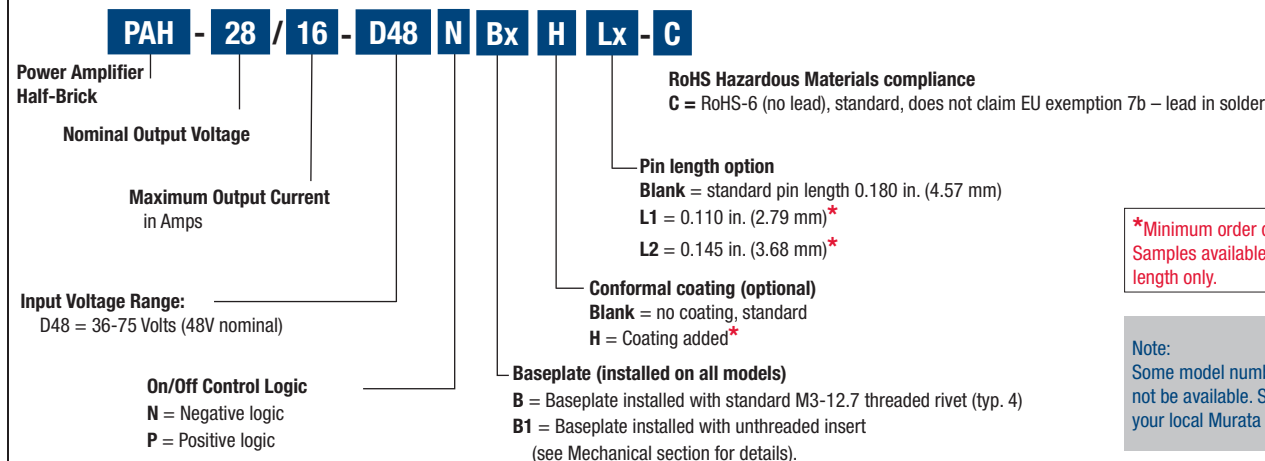
① Please refer to the part number structure for additional ordering part numbers and options.

② All specifications are at nominal line voltage and full load, +25°C. unless otherwise noted. See detailed specifications.

③ Full power continuous output requires baseplate installation. Please refer to the derating curves.

④ Minimum efficiency applies to all input voltages and working temperatures.

## PART NUMBER STRUCTURE



## FUNCTIONAL SPECIFICATIONS, PAH-28/16-D48

ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Input Voltage, Continuous	Full power operation	0		80	Vdc
Input Voltage, Transient	Operating or non-operating, tested: 100 mS max. duration	0		100	Vdc
Isolation Voltage	Input to output			2250	Vdc
Input Reverse Polarity	None, install external fuse		None		Vdc
On/Off Remote Control	Power on or off, referred to -Vin	0		15	Vdc
Output Power		0	448	454.72	W
Output Current	Current-limited, no damage, short-circuit protected	0		16	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied nor recommended.					
<b>INPUT</b>					
Operating voltage range		36	48	75	Vdc
Recommended External Fuse	Fast blow			20	A
Turn On/Start-up threshold tested at 1/2 load	Rising input voltage	33	34	35	Vdc
Turn Off/Undervoltage lockout tested at 1/2 load	Falling input voltage	31	32	33	Vdc
Reverse Polarity Protection	None, install external fuse		None		Vdc
Internal Filter Type			Pi		
<b>Input current</b>					
Full Load Conditions	Vin = nominal		9.98	10.19	A
Low Line	Vin = minimum		13.53	13.88	A
Inrush Transient			5	7	A <sup>2</sup> -Sec.
Output in Short Circuit			60	200	mA
No Load Input Current	Iout = minimum, unit=ON		80	100	mA
Shut-Down Mode Input Current			5	10	mA
Reflected (back) ripple current ②	Measured at input with specified filter		40	80	mA, pk-pk
<b>GENERAL and SAFETY</b>					
Efficiency	Vin=48V, full load, +25°C.	93	93.5		%
	@ Vin=Max	91.0	92.0		%
<b>Isolation</b>					
Isolation Voltage	Input to output, continuous		2250		Vdc
	Input to Baseplate, continuous		1500		
	Output to Baseplate, continuous		1500		
Insulation Safety Rating			basic		
Isolation Resistance			10		Mohm
Isolation Capacitance			1,000		pF
Safety	Certified to UL-60950-1, CSA-C22.2 No.60950-1, IEC/EN60950-1, 2nd edition		Yes		
Calculated MTBF	Per Telcordia SR332, issue 1 class 3, ground fixed, Tambient=+25°C		1.4		Hours x 10 <sup>6</sup>
<b>DYNAMIC CHARACTERISTICS</b>					
Fixed Switching Frequency		380	420	460	KHz
Startup Time	Power On to Vout regulated (100% resistive load)		25	50	mS
Startup Time	Remote ON to 10% Vout (50% resistive load)		25	50	mS
Dynamic Load Response	50-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/μSec		2500	3500	μSec
Dynamic Load Peak Deviation	same as above		±500	±1000	mV
<b>FEATURES and OPTIONS</b>					
<b>Remote On/Off Control ③</b>					
“N” suffix:					
Negative Logic, ON state	Pin open=ON or	-0.1		0.8	V
Negative Logic, OFF state		2.5		15	V
Control Current	open collector/drain		1	2	mA
“P” suffix:					
Positive Logic, ON state	Pin open=ON or	3.5		15	V
Positive Logic, OFF state		0		1	V
Control Current	open collector/drain		1	2	mA
Remote Sense Compliance	Vsense=Vout-Vload, Sense connected at load			10	% of Vout

### FUNCTIONAL SPECIFICATIONS, PAH-28/16-D48 (CONT.)

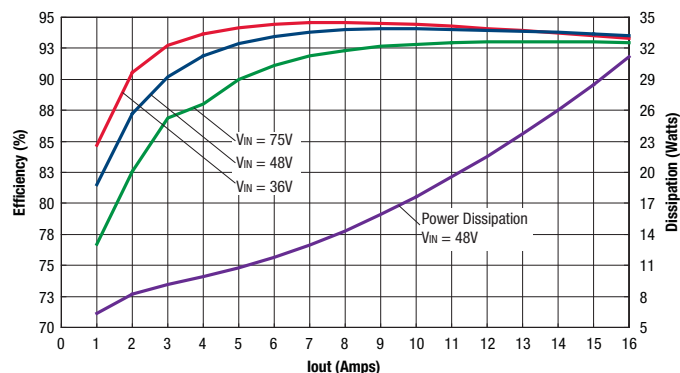
OUTPUT					
Total Output Power	See Derating	0.0	448	454.72	W
Voltage					
Nominal Output Voltage	No trim	27.58	28	28.42	Vdc
Setting Accuracy	At 50% load	-1.5		1.5	% of Vnom.
Output Voltage Range	User-adjustable	16.8(-40%)		32.2(+15%)	Vdc
Overvoltage Protection	Via magnetic feedback	34	36	50	Vdc
Current					
Output Current Range		0		16	A
Minimum Load			No minimum load		
Current Limit Inception ④	98% of Vnom., after warmup	17	19	25	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within $\pm 1\%$ of Vout		0.5	1.0	A
Short Circuit Duration (remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Current limiting				
Hiccup autorestart duty cycle			10		%
Regulation ⑤					
Line Regulation	Vin=min. to max. Vout=nom.			$\pm 0.25$	%
Load Regulation	Iout=min. to max. Vin=48V.			$\pm 0.25$	%
Ripple and Noise	5 Hz- 20 MHz BW		100	300	mV pk-pk
Temperature Coefficient	At all outputs		$\pm 0.015$		% of Vnom./°C
External output capacitance required ⑥	Cap. ESR= $<0.02\Omega$ , Full resistive load	470		4700	$\mu\text{F}$
MECHANICAL (Through Hole Models)					
Outline Dimensions	with baseplate; see mechanical drawings.		2.3 X 2.4 X 0.5		Inches
			58.4x60.96x12.7		mm
Weight			3.67		Ounces
			104		Grams
Through Hole Pin Diameter	Pins 1-4, 6-8/5,9		0.040/0.06		Inches
			1.016/1.524		mm
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate		100-299		$\mu$ -inches
	Gold overplate		10.31		$\mu$ -inches
Case or Baseplate Material			Aluminum		
ENVIRONMENTAL					
Operating Ambient Temperature Range	With derating, full power, measured at Tref	-40		85	°C
Operating Case Temperature		-40		120	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured in center	115	125	130	°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter required		B		Class
Radiated, EN55022/CISPR22			B		Class
Relative humidity, non-condensing	To +85°C	10		90	%RH
Altitude		-500		10,000	feet
(must derate -1%/1000 feet)		-152		3048	meters
RoHS rating			RoHS-6		

### Notes

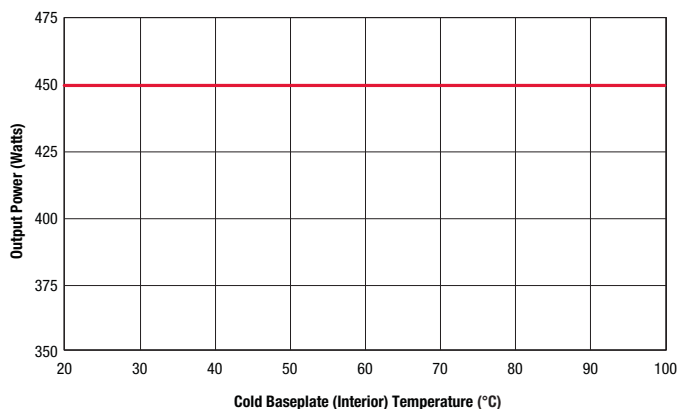
- ① Unless otherwise noted, all specifications are at nominal input voltage, nominal output voltage and full load. General conditions are +25° Celsius ambient temperature, near sea level altitude, natural convection airflow. All models are tested and specified with external parallel 1  $\mu\text{F}$  and 470  $\mu\text{F}$  output capacitors. A 220 $\mu\text{F}$  external input capacitors is required. All capacitors are low-ESR types wired close to the converter.
- ② Input (back) ripple current is tested and specified over 5 Hz to 20 MHz bandwidth. Input filtering is Cbus=220  $\mu\text{F}/100\text{V}$ , Cin=470  $\mu\text{F}/100\text{V}$  and Lbus=12  $\mu\text{H}$ .
- ③ The Remote On/Off Control is referred to -Vin.
- ④ Over-current protection is non-latching with auto recovery (Hiccup)
- ⑤ Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.
- ⑥ Required minimum output capacitance is 470  $\mu\text{F}$ , low ESR.

## TYPICAL PERFORMANCE DATA, PAH-28/16-D48

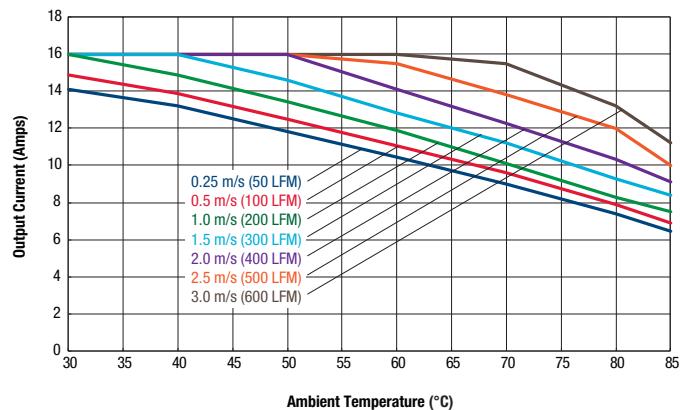
Efficiency and Power Dissipation @  $T_a = +25^\circ\text{C}$



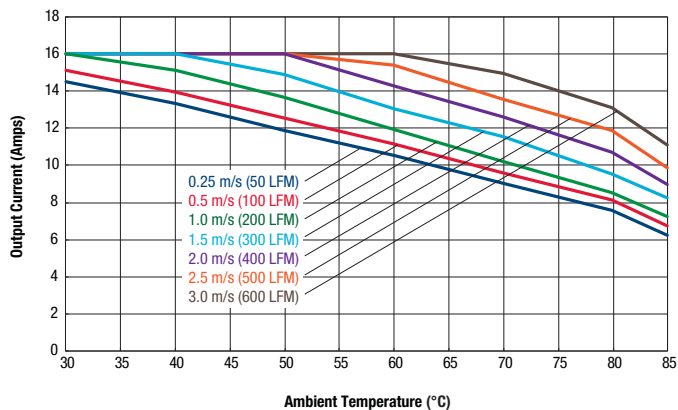
Output Power Derating in Conduction Cooling (Cold Baseplate) Applications  
( $V_{in}=48\text{V}$ , Ambient Temperature  $<70^\circ\text{C}$ )



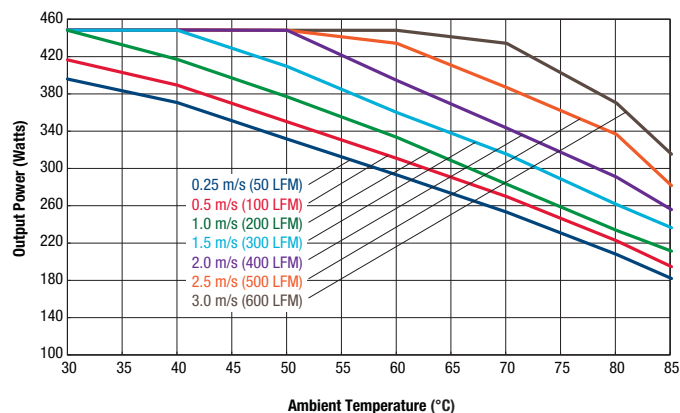
Maximum Current Temperature Derating at sea level  
( $V_{in}=48\text{V}$ , transverse airflow, on  $10'' \times 10''$  PCB with baseplate)



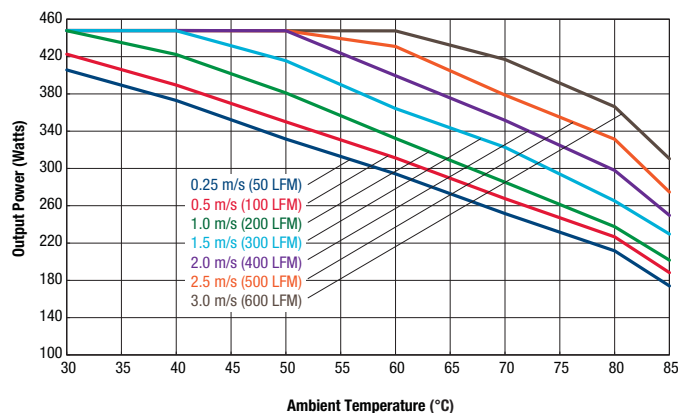
Maximum Current Temperature Derating at sea level  
( $V_{in}=48\text{V}$ , longitudinal airflow, on  $10'' \times 10''$  PCB with baseplate)



Maximum Power Temperature Derating at sea level  
( $V_{in}=48\text{V}$ , transverse airflow, on  $10'' \times 10''$  PCB with baseplate)

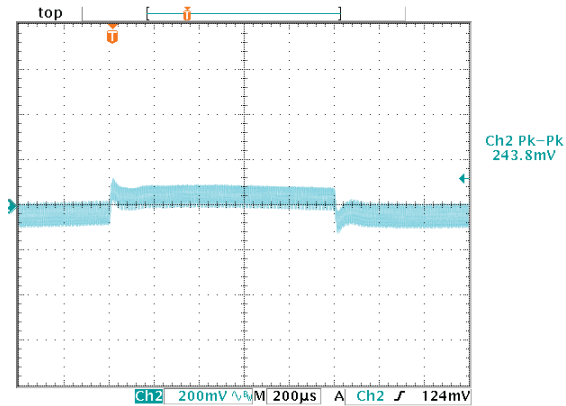


Maximum Power Temperature Derating at sea level  
( $V_{in}=48\text{V}$ , longitudinal airflow, on  $10'' \times 10''$  PCB with baseplate)

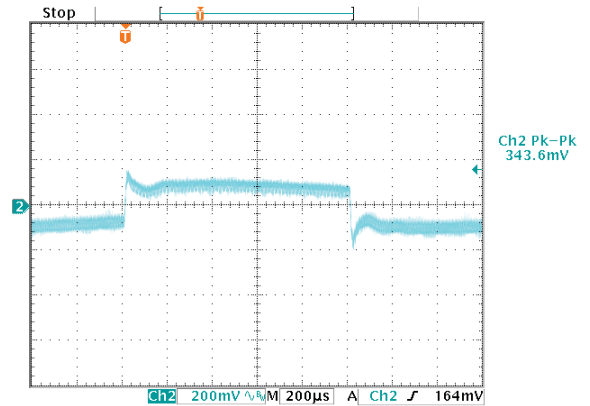


## OSCILLOGRAMS, PAH-28/16-D48

### Stepload Transient Response

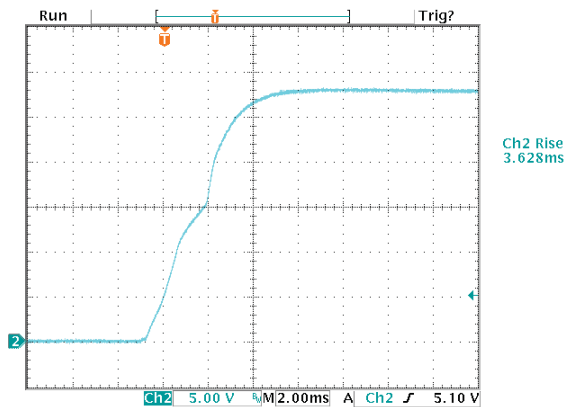


Transient Response (Load 50% to 75%, 470µF electrolytic cap and 1µF ceramic cap)

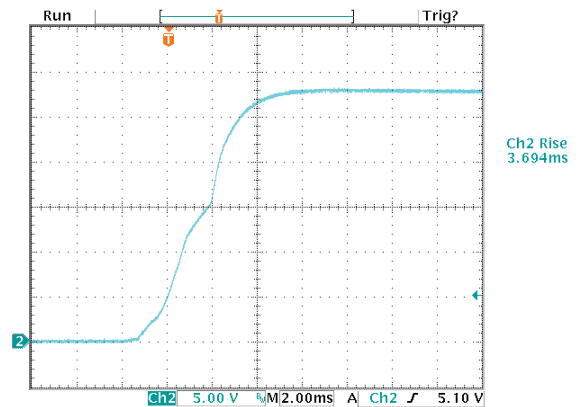


Transient Response (Load 25% to 75%, 470µF electrolytic cap and 1µF ceramic cap)

### On/Off Enable Start-up

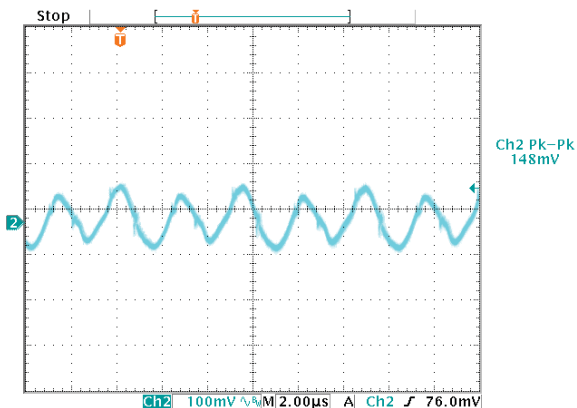


Enable Start-up (Vin=48V Iout=16A)

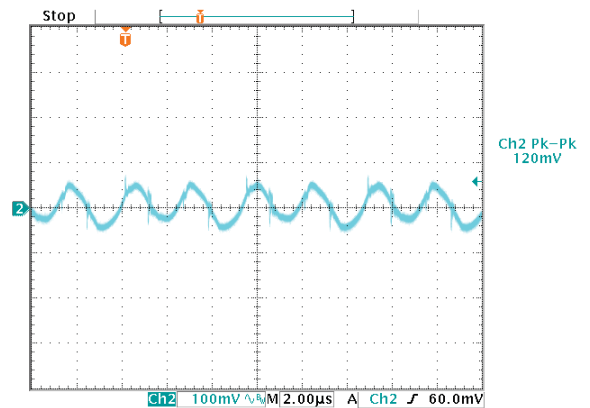


Enable Start-up (Vin=48V Iout=0A)

### Ripple and Noise



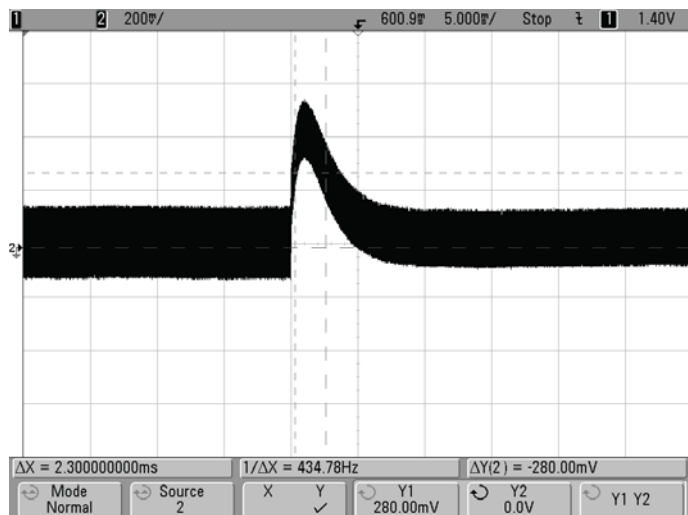
Ripple and Noise Waveform (Vin=48V Iout=16A)



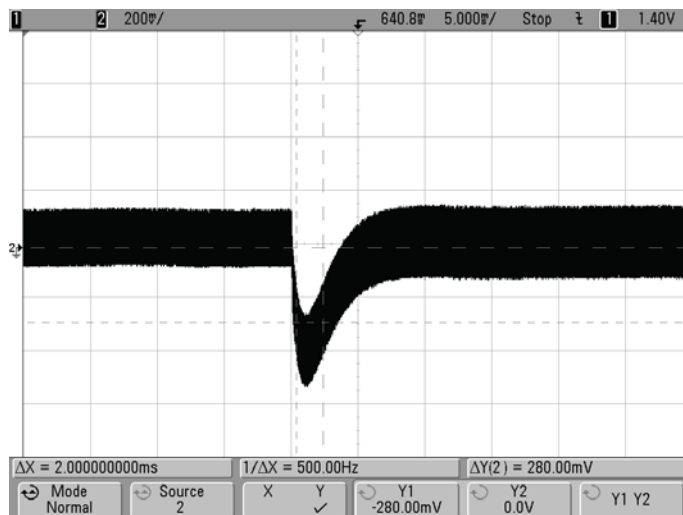
Ripple and Noise Waveform (Vin=48V Iout=0A)

## OSCILLOGRAMS, PAH-28/16-D48

Step Load Transient Response (Vin=48V, Vout=nominal, Cload=0, Iout=12A to 8A, Ta=+25°C.) Trace 2=Vout, 200 mV/div., 5 mS/div.



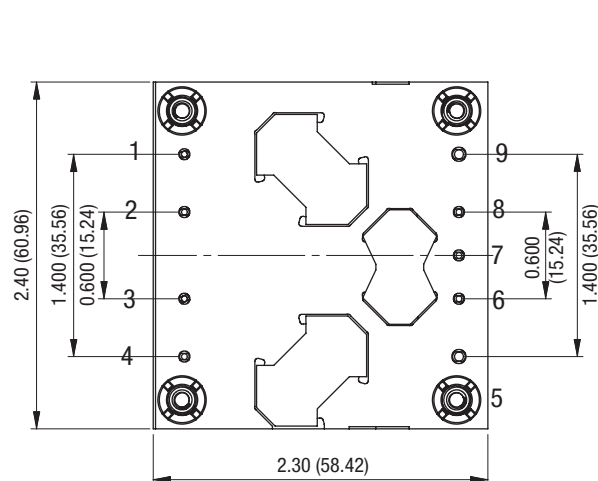
Step Load Transient Response (Vin=48V, Vout=nominal, Cload=0, Iout=8A to 12A, Ta=+25°C.) Trace 2=Vout, 200 mV/div., 5 mS/div.



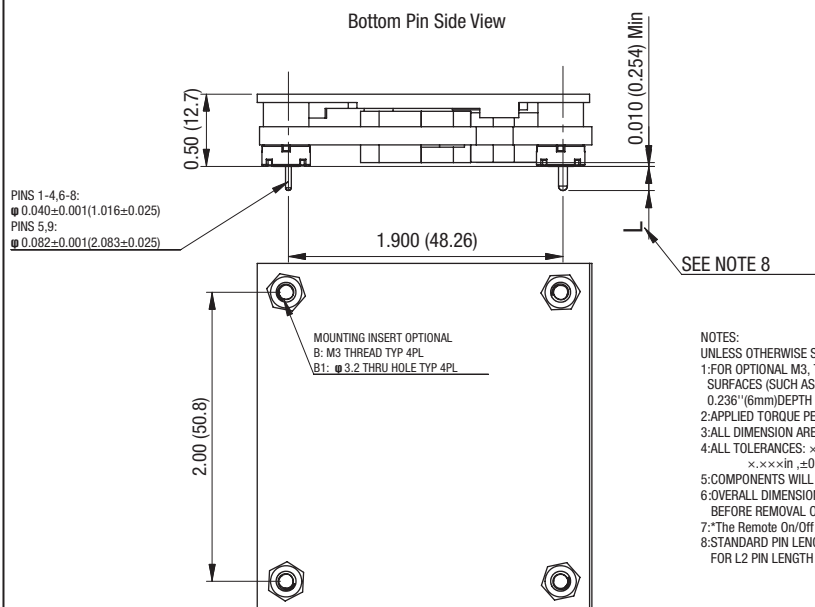
Step Load Transient Response (Vin=48V, Vout=nominal, Cload=0, Iout=12A to 8A to 12A, Ta=+25°C.) Trace 2=Vout, 200 mV/div., 20 mS/div.



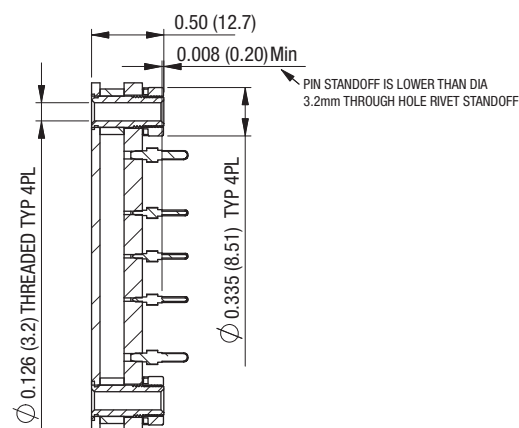
## MECHANICAL SPECIFICATIONS



### Bottom Pin Side View

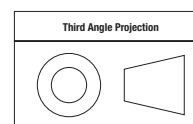


Top View



SECTION A-A

Dimensions are in inches (mm shown for ref. only).



Tolerances (unless otherwise specified):

.XX  $\pm$  0.02 (0.5)  
.XXX  $\pm$  0.010 (0.25)  
Angles  $\pm$  2°

Components are shown for reference only.

## INPUT/OUTPUT CONNECTIONS

Pin	Function
1	–Vin
2	Case ground
3	On/Off Control
4	+Vin
5	+Vout
6	+Sense
7	Trim
8	–Sense
9	–Vout

Since there is some pinout inconsistency between manufacturers of half brick converters, be sure to follow the pin function, not the pin number, when laying out your board.

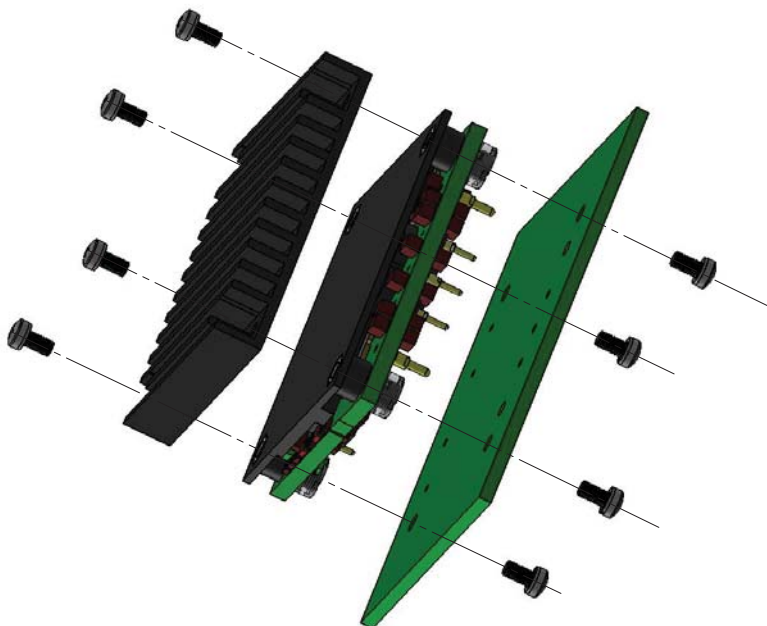
Standard pin length is shown. Please refer to the Part Number Structure for special order pin lengths.

\* Note that the “case” connects to the baseplate (when installed). This case connection is isolated from the rest of the converter. Pin 2 may be deleted under special order. Please contact Murata Power Solutions for information.

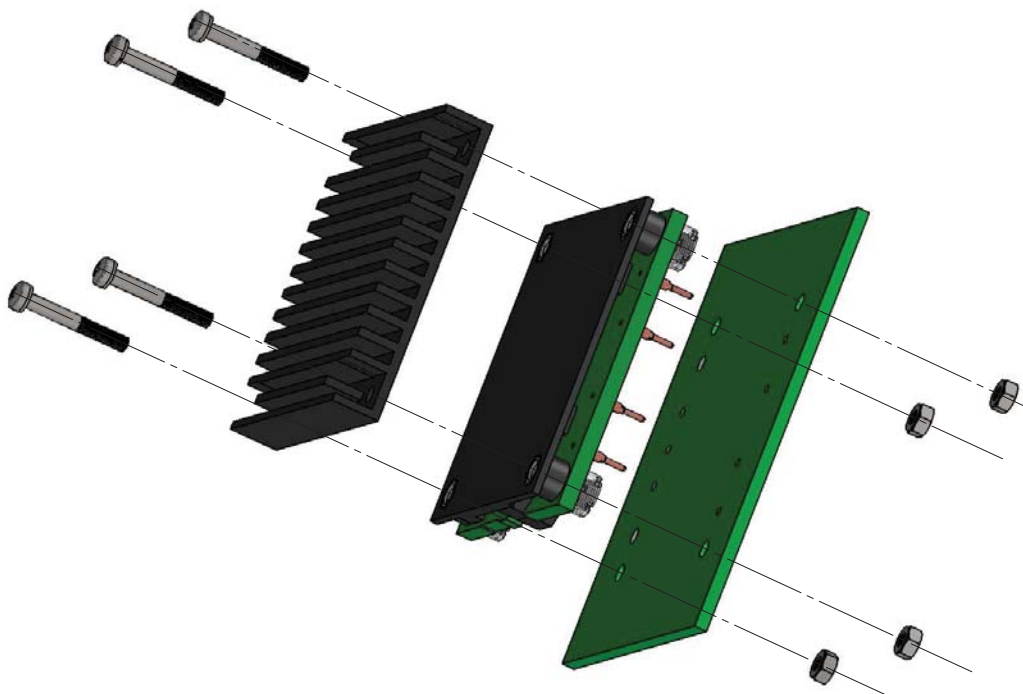
The Trim connection may be left open and the converter will achieve its rated output voltage.



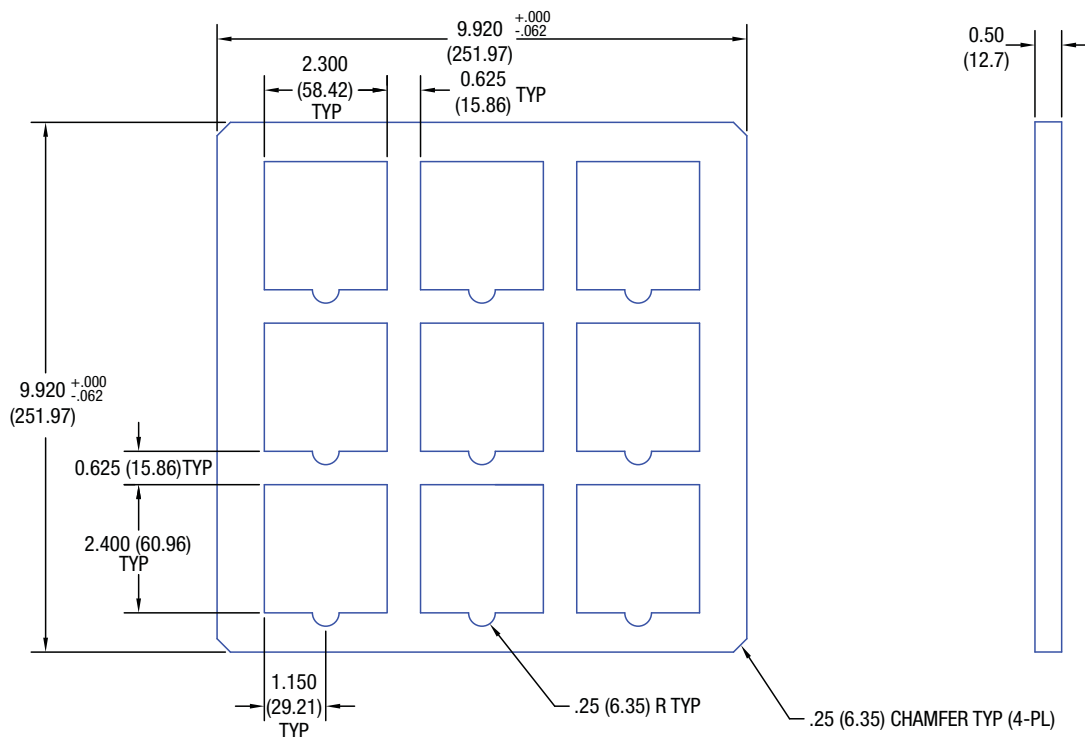
## BASEPLATE WITH STANDARD M3-12.7 THREADED RIVET



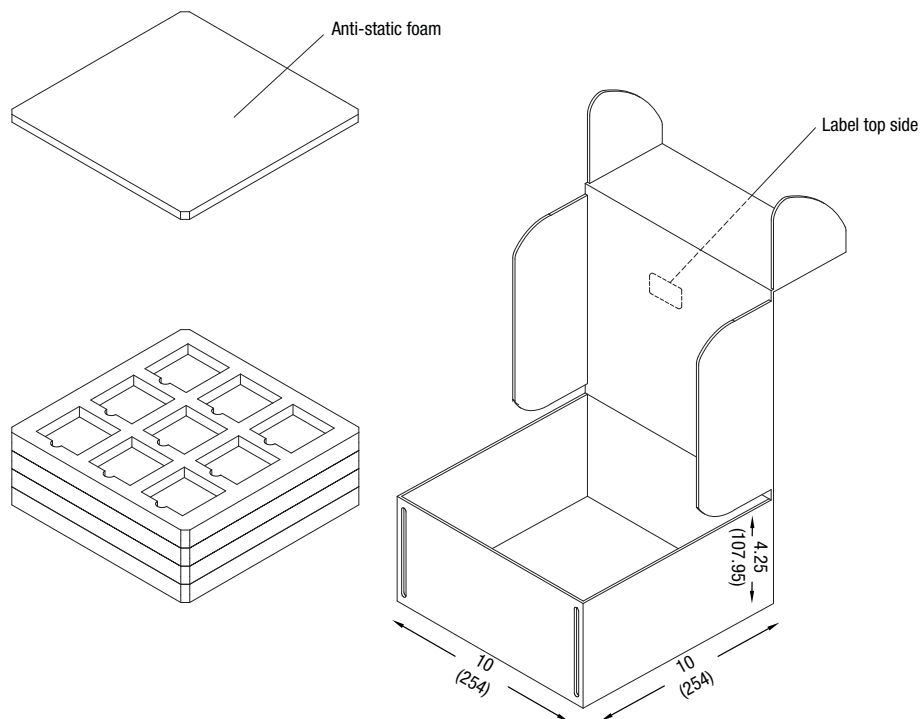
## BASEPLATE WITH UNTHREADED INSERT



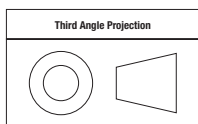
## SHIPPING TRAYS: LOW DENSITY CLOSED CELL POLYETHYLENE STATIC DISSIPATIVE FOAM



## SHIPPING BOXES



Dimensions are in inches (mm shown for ref. only).



Tolerances (unless otherwise specified):

.XX  $\pm 0.02$  (0.5)

.XXX  $\pm 0.010$  (0.25)

Angles  $\pm 2^\circ$

Components are shown for reference only.

## TECHNICAL NOTES

### Input Fusing

Certain applications and/or safety agencies may require fuses at the inputs of power conversion components. Fuses should also be used when there is the possibility of sustained input voltage reversal which is not current-limited. For greatest safety, we recommend a fast blow fuse installed in the ungrounded input supply line.

The installer must observe all relevant safety standards and regulations. For safety agency approvals, install the converter in compliance with the end-user safety standard.

### Input Reverse-Polarity Protection

If the input voltage polarity is reversed, an internal diode will become forward biased and likely draw excessive current from the power source. If this source is not current-limited or the circuit appropriately fused, it could cause permanent damage to the converter.

### Input Under-Voltage Shutdown and Start-Up Threshold

Under normal start-up conditions, converters will not begin to regulate properly until the ramping-up input voltage exceeds and remains at the Start-Up Threshold Voltage (see Specifications). Once operating, converters will not turn off until the input voltage drops below the Under-Voltage Shutdown Limit. Subsequent restart will not occur until the input voltage rises again above the Start-Up Threshold. This built-in hysteresis prevents any unstable on/off operation at a single input voltage.

Users should be aware however of input sources near the Under-Voltage Shutdown whose voltage decays as input current is consumed (such as capacitor inputs), the converter shuts off and then restarts as the external capacitor recharges. Such situations could oscillate. To prevent this, make sure the operating input voltage is well above the UV Shutdown voltage AT ALL TIMES.

### Start-Up Time

Assuming that the output current is set at the rated maximum, the Vin to Vout Start-Up Time (see Specifications) is the time interval between the point when the ramping input voltage crosses the Start-Up Threshold and the fully loaded regulated output voltage enters and remains within its specified accuracy band. Actual measured times will vary with input source impedance, external input capacitance, input voltage slew rate and final value of the input voltage as it appears at the converter.

These converters include a soft start circuit to moderate the duty cycle of its PWM controller at power up, thereby limiting the input inrush current.

The On/Off Remote Control interval from On command to Vout regulated assumes that the converter already has its input voltage stabilized above the Start-Up Threshold before the On command. The interval is measured from the On command until the output enters and remains within its specified accuracy band. The specification assumes that the output is fully loaded at maximum rated current. Similar conditions apply to the On to Vout regulated specification such as external load capacitance and soft start circuitry.

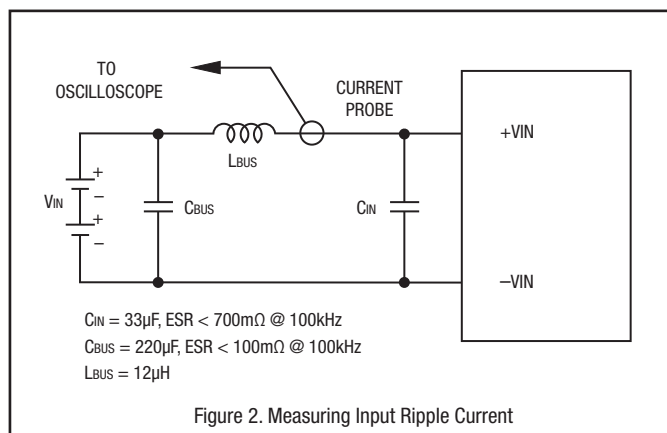
### Input Source Impedance

These converters will operate to specifications without external components, assuming that the source voltage has very low impedance and reasonable input voltage regulation. Since real-world voltage sources have finite impedance, performance is improved by adding external filter components. Sometimes only a small ceramic capacitor is sufficient. Since it is difficult to totally characterize all applications, some experimentation may be needed. Note that external input capacitors must accept high speed switching currents.

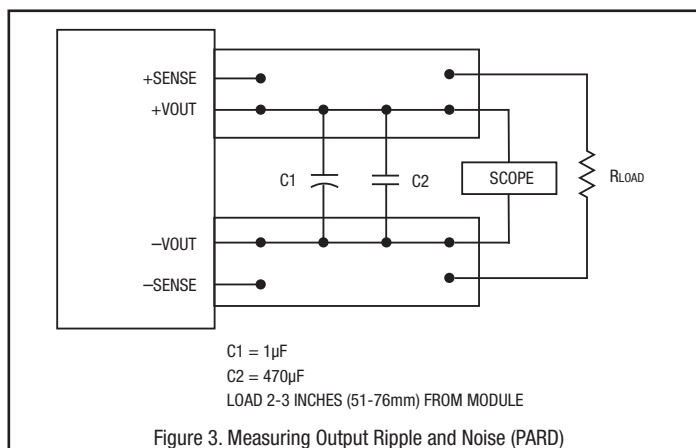
Because of the switching nature of DC-DC converters, the input of these converters must be driven from a source with both low AC impedance and adequate DC input regulation. Performance will degrade with increasing input inductance. Excessive input inductance may inhibit operation. The DC input regulation specifies that the input voltage, once operating, must never degrade below the Shut-Down Threshold under all load conditions. Be sure to use adequate trace sizes and mount components close to the converter.

### I/O Filtering, Input Ripple Current and Output Noise

All models in this converter series are tested and specified for input reflected ripple current and output noise using designated external input/output components, circuits and layout as shown in the figures below. External input capacitors (Cin in the figure) serve primarily as energy storage elements, minimizing line voltage variations caused by transient IR drops in the input conductors. Users should select input capacitors for bulk capacitance (at appropriate frequencies), low ESR and high RMS ripple current ratings. In the figure below, the Cbus and Lbus components simulate a typical DC voltage bus. Your specific system configuration may require additional considerations. Please note that the values of Cin, Lbus and Cbus will vary according to the specific converter model.



In critical applications, output ripple and noise (also referred to as periodic and random deviations or PARD) may be reduced by adding filter elements such as multiple external capacitors. Be sure to calculate component temperature rise from reflected AC current dissipated inside capacitor ESR. Our Application Engineers can recommend potential solutions.



### Floating Outputs

Since these are isolated DC-DC converters, their outputs are “floating” with respect to their input. The essential feature of such isolation is ideal ZERO CURRENT FLOW between input and output. Real-world converters however do exhibit tiny leakage currents between input and output (see Specifications). These leakages consist of both an AC stray capacitance coupling component and a DC leakage resistance. When using the isolation feature, do not allow the isolation voltage to exceed specifications. Otherwise the converter may be damaged. Designers will normally use the negative output (-Output) as the ground return of the load circuit. You can however use the positive output (+Output) as the ground return to effectively reverse the output polarity.

### Minimum Output Loading Requirements

These converters employ a synchronous rectifier design topology. All models regulate within specification and are stable under no load to full load conditions. Operation under no load might however slightly increase output ripple and noise.

### Thermal Shutdown

To prevent many over temperature problems and damage, these converters include thermal shutdown circuitry. If environmental conditions cause the temperature of the DC-DC's to rise above the Operating Temperature Range up to the shutdown temperature, an on-board electronic temperature sensor will power down the unit. When the temperature decreases below the turn-on threshold, the converter will automatically restart. There is a small amount of hysteresis to prevent rapid on/off cycling. The temperature sensor is typically located adjacent to the switching controller, approximately in the center of the unit. See the Performance and Functional Specifications.

**CAUTION:** If you operate too close to the thermal limits, the converter may shut down suddenly without warning. Be sure to thoroughly test your application to avoid unplanned thermal shutdown.

### Temperature Derating Curves

The graphs in this data sheet illustrate typical operation under a variety of conditions. The Derating curves show the maximum continuous ambient air temperature and decreasing maximum output current which is acceptable under increasing forced airflow measured in Linear Feet per Minute (“LFM”).

Note that these are AVERAGE measurements. The converter will accept brief increases in temperature and/or current or reduced airflow as long as the average is not exceeded.

Note that the temperatures are of the ambient airflow, not the converter itself which is obviously running at higher temperature than the outside air. Also note that very low flow rates (below about 25 LFM) are similar to “natural convection,” that is, not using fan-forced airflow.

MPS makes Characterization measurements in a closed cycle wind tunnel with calibrated airflow. We use both thermocouples and an infrared camera system to observe thermal performance. As a practical matter, it is quite difficult to insert an anemometer to precisely measure airflow in most applications. Sometimes it is possible to estimate the effective airflow if you thoroughly understand the enclosure geometry, entry/exit orifice areas and the fan flowrate specifications. If in doubt, contact MPS to discuss placement and measurement techniques of suggested temperature sensors.

**CAUTION:** If you routinely or accidentally exceed these Derating guidelines, the converter may have an unplanned Over Temperature shut down. Also, these graphs are all collected at slightly above Sea Level altitude. Be sure to reduce the derating for higher density altitude.

### Output Overvoltage Protection

This converter monitors its output voltage for an over-voltage condition using an on-board electronic comparator. The signal is optically coupled to the primary side PWM controller. If the output exceeds OVP limits, the sensing circuit will power down the unit, and the output voltage will decrease. After a time-out period, the PWM will automatically attempt to restart, causing the output voltage to ramp up to its rated value. It is not necessary to power down and reset the converter for this automatic OVP-recovery restart.

If the fault condition persists and the output voltage climbs to excessive levels, the OVP circuitry will initiate another shutdown cycle. This on/off cycling is referred to as “hiccup” mode. It safely tests full current rated output voltage without damaging the converter.

### Output Current Limiting

As soon as the output current increases to its maximum rated value, the DC-DC converter will enter a current-limiting mode. The output voltage will decrease proportionally with increases in output current, thereby maintaining a somewhat constant power output. This is commonly referred to as power limiting.

Current limiting inception is defined as the point at which full power falls below the rated tolerance. See the Performance/Functional Specifications. Note particularly that the output current may briefly rise above its rated value. This enhances reliability and continued operation of your application. If the output current is too high, the converter will enter the short circuit condition.

### Output Short Circuit Condition

When a converter is in current-limit mode, the output voltage will drop as the output current demand increases. If the output voltage drops too low, the magnetically coupled voltage used to develop primary side voltages will also drop, thereby shutting down the PWM controller. Following a time-out period, the PWM will restart, causing the output voltage to begin ramping up to its

appropriate value. If the short-circuit condition persists, another shutdown cycle will initiate. This on/off cycling is called "hiccup mode". The hiccup cycling reduces the average output current, thereby preventing excessive internal temperatures. A short circuit can be tolerated indefinitely.

## Remote Sense Input

Sense inputs compensate for output voltage inaccuracy delivered at the load. This is done by correcting voltage drops along the output wiring such as moderate IR drops and the current carrying capacity of PC board etch. Sense inputs also improve the stability of the converter and load system by optimizing the control loop phase margin.

Note: The Sense input and power Vout lines are internally connected through low value resistors to their respective polarities so that the converter can operate without external connection to the Sense. Nevertheless, if the Sense function is not used for remote regulation, the user should connect +Sense to +Vout and -Sense to -Vout at the converter pins.

The remote Sense lines carry very little current. They are also capacitively coupled to the output lines and therefore are in the feedback control loop to regulate and stabilize the output. As such, they are not low impedance inputs and must be treated with care in PC board layouts. Sense lines on the PCB should run adjacent to DC signals, preferably Ground. In cables and discrete wiring, use twisted pair, shielded tubing or similar techniques.

Please observe Sense inputs tolerance to avoid improper operation:

$$[V_{out}(+) - V_{out}(-)] - [Sense(+) - Sense(-)] \leq 10\% \text{ of } V_{out}$$

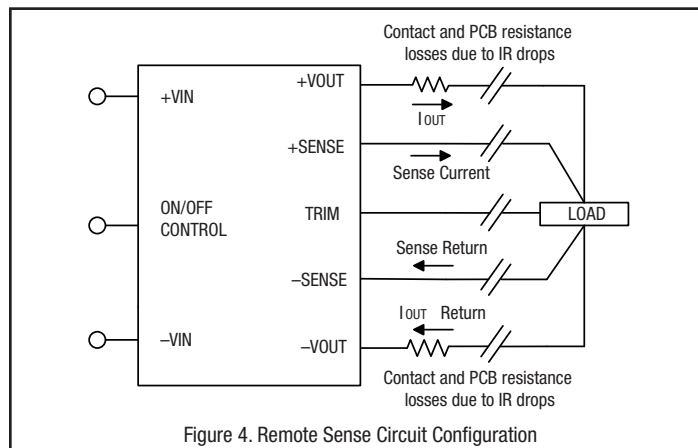


Figure 4. Remote Sense Circuit Configuration

Output overvoltage protection is monitored at the output voltage pin, not the Sense pin. Therefore excessive voltage differences between Vout and Sense together with trim adjustment of the output can cause the overvoltage protection circuit to activate and shut down the output.

Power derating of the converter is based on the combination of maximum output current and the highest output voltage. Therefore the designer must insure:

$$(V_{out} \text{ at pins}) \times (I_{out}) \leq (\text{Max. rated output power})$$

## Trimming the Output Voltage

The Trim input to the converter allows the user to adjust the output voltage over the rated trim range (please refer to the Specifications). In the trim equations and circuit diagrams that follow, trim adjustments use either a trimpot or

a single fixed resistor connected between the Trim input and either the +Sense or -Sense terminals. (On some converters, an external user-supplied precision DC voltage may also be used for trimming). Trimming resistors should have a low temperature coefficient ( $\pm 100$  ppm/deg.C or less) and be mounted close to the converter. Keep leads short. If the trim function is not used, leave the trim unconnected. With no trim, the converter will exhibit its specified output voltage accuracy.

There are two CAUTION's to be aware for the Trim input:

**CAUTION:** To avoid unplanned power down cycles, do not exceed EITHER the maximum output voltage OR the maximum output power when setting the trim. Be particularly careful with a trimpot. If the output voltage is excessive, the OVP circuit may inadvertently shut down the converter. If the maximum power is exceeded, the converter may enter current limiting. If the power is exceeded for an extended period, the converter may overheat and encounter overtemperature shut down.

**CAUTION:** Be careful of external electrical noise. The Trim input is a sensitive input to the converter's feedback control loop. Excessive electrical noise may cause instability or oscillation. Keep external connections short to the Trim input. Use shielding if needed.

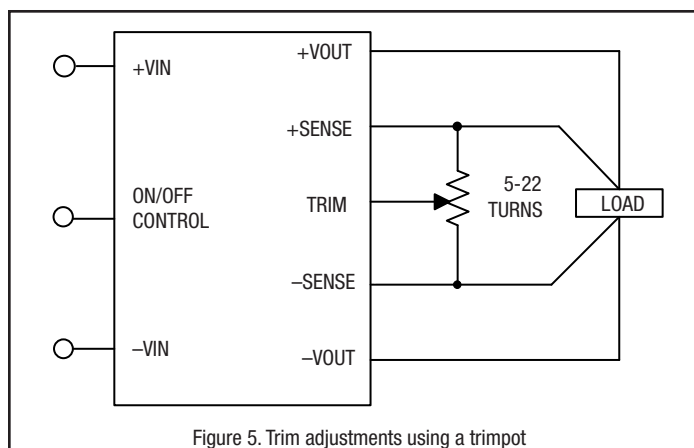


Figure 5. Trim adjustments using a trimpot

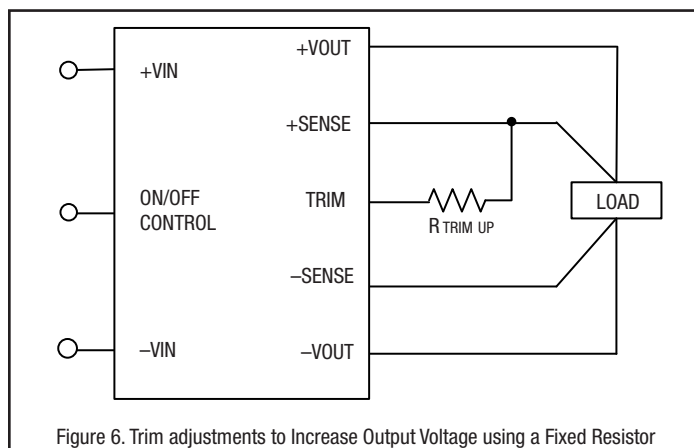


Figure 6. Trim adjustments to Increase Output Voltage using a Fixed Resistor

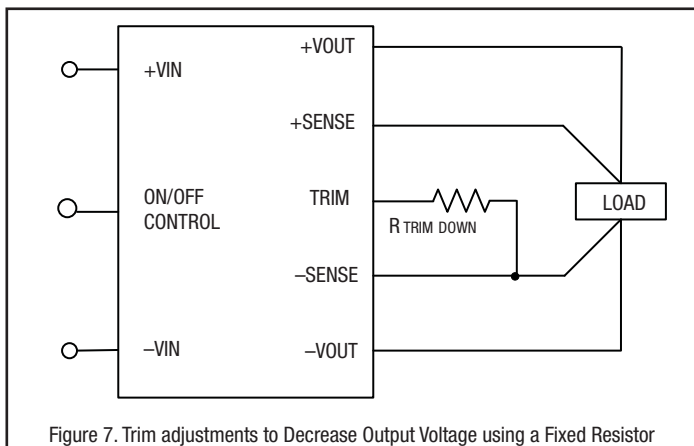


Figure 7. Trim adjustments to Decrease Output Voltage using a Fixed Resistor

## Trim Equations

$$R_{adj\_up} \text{ (in k}\Omega\text{)} = \frac{V_{nominal} \times (1 + \Delta)}{1.225 \times \Delta} - \frac{1}{\Delta} - 2$$

$$\text{where } \Delta = \frac{V_{out} - V_{nominal}}{V_{nominal}}$$

$$R_{adj\_down} \text{ (in k}\Omega\text{)} = \frac{1}{\Delta} - 2$$

$$\text{where } \Delta = \frac{V_{nominal} - V_{out}}{V_{nominal}}$$

Where Vref = +1.225 Volts and Δ is the desired output voltage change. Note that "Δ" is given as a small fraction, not a percentage.

A single resistor connected between Trim and +Sense will increase the output voltage. A resistor connected between Trim and -Sense will decrease the output.

## Remote On/Off Control

On the input side, a remote On/Off Control can be ordered with either logic type.

**Positive** models are enabled when the On/Off pin is left open or is pulled high to +15V with respect to -Vin. Some models will also turn on at lower intermediate voltages (see Specifications). Positive-logic devices are disabled when the On/Off is grounded or brought to within a low voltage (see Specifications) with respect to -Vin.

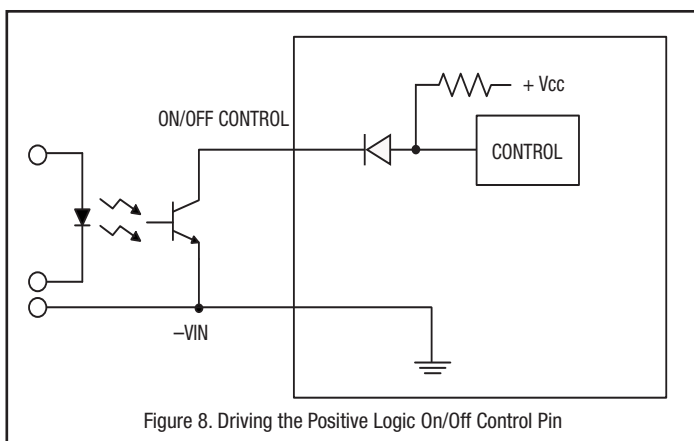


Figure 8. Driving the Positive Logic On/Off Control Pin

**Negative**-logic devices are on (enabled) when the On/Off is grounded or brought to within a low voltage (see Specifications) with respect to -Vin. The device is off (disabled) when the On/Off is pulled high to +15V with respect to -Vin.

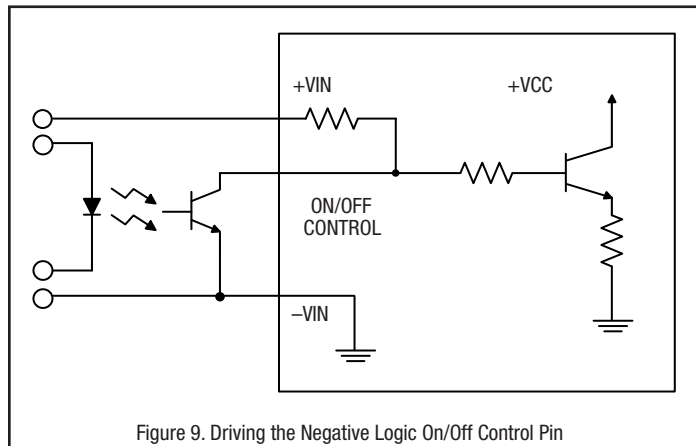


Figure 9. Driving the Negative Logic On/Off Control Pin

Dynamic control of the On/Off function should be able to sink appropriate signal current when brought low and withstand appropriate voltage when brought high. Be aware too that there is a finite time in milliseconds (see Specifications) between the time of On/Off Control activation and stable, regulated output. This time will vary slightly with output load type and current and input conditions.

There are two CAUTIONS for the On/Off Control:

**CAUTION:** While it is possible to control the On/Off with external logic if you carefully observe the voltage levels, the preferred circuit is either an open drain/open collector transistor or a relay (which can thereupon be controlled by logic).

**CAUTION:** Do not apply voltages to the On/Off pin when there is no input power voltage. Otherwise the converter may be permanently damaged.

## Soldering Guidelines

Murata Power Solutions recommends the specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Your production environment may differ; therefore please thoroughly review these guidelines with your process engineers.

Wave Solder Operations for through-hole mounted products (THMT)			
For Sn/Ag/Cu based solders:		For Sn/Pb based solders:	
Maximum Preheat Temperature	115° C.	Maximum Preheat Temperature	105° C.
Maximum Pot Temperature	270° C.	Maximum Pot Temperature	250° C.
Maximum Solder Dwell Time	7 seconds	Maximum Solder Dwell Time	6 seconds



## Emissions Performance

Murata Power Solutions measures its products for radio frequency emissions against the EN 55022 and CISPR 22 standards. Passive resistance loads are employed and the output is set to the maximum voltage. If you set up your own emissions testing, make sure the output load is rated at continuous power while doing the tests.

The recommended external input and output capacitors (if required) are included. Please refer to the fundamental switching frequency. All of this information is listed in the Product Specifications. An external discrete filter is installed and the circuit diagram is shown below.

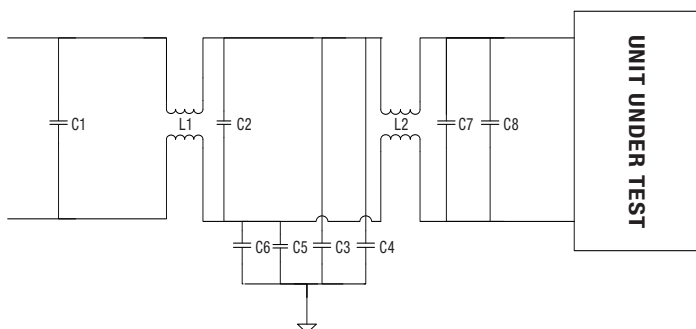


Figure 10. Conducted Emissions Test Circuit

## [1] Conducted Emissions Parts List

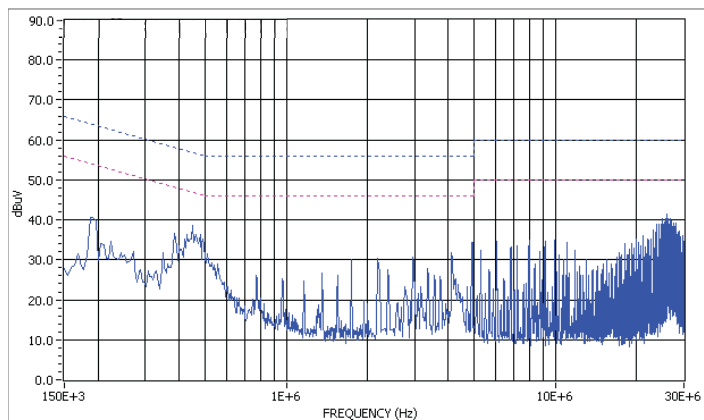
Reference	Part Number	Description	Vendor
C1, C2, C7	GRM32ER72A225KA35L	CAP SMT NON POL CERAMIC X7R 2.2 $\mu$ F 100V 20% 1210	Murata
L1, L2	LB22H1463	COMMON MODE-809 $\mu$ H- $\pm$ 25%-9.7A-R5K-28*26*12.7mm	Haiguang
C3, C4, C5, C6	GRM55DR72J224KW01L	SMD CERAMIC 630V-0.22 $\mu$ F- $\pm$ 10%-X7R-2220	Murata
C8	UVK2A330MPD	Aluminum 100V-33 $\mu$ F- $\pm$ 10%-long lead	Nichicon

## [2] Conducted Emissions Test Equipment Used

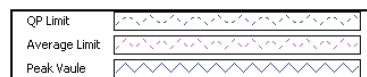
Hewlett Packard HP8594L Spectrum Analyzer – S/N 3827A00153

2Line V-networks LS1-15V 50 $\Omega$ /50U Line Impedance Stabilization Network

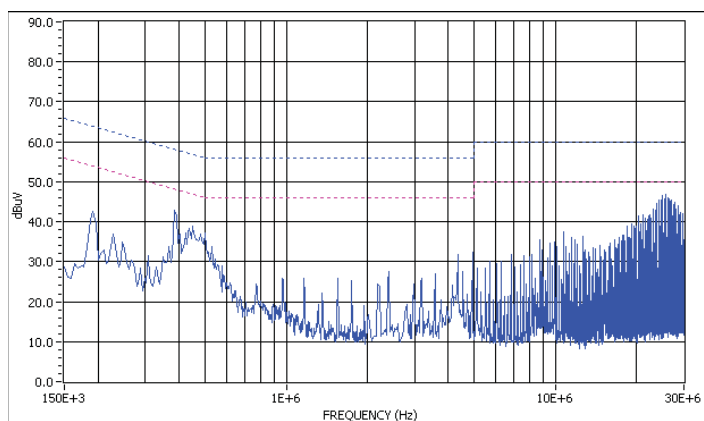
## [3] Conducted Emissions Test Results



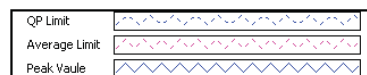
Peak Detection Value



Graph 1. Conducted emissions performance, Positive Line, CISPR 22, Class A, full load



Peak Detection Value



Graph 2. Conducted emissions performance, Negative Line, CISPR 22, Class A, full load

Most applications can use the filtering which is already installed inside the converter or with the addition of the recommended external capacitors. For greater emissions suppression, consider additional filter components and/or shielding. Emissions performance will depend on the user's PC board layout, the chassis shielding environment and choice of external components. Please refer to Application Note GEAN02 for further discussion.

Since many factors affect both the amplitude and spectra of emissions, we recommend using an engineer who is experienced at emissions suppression.

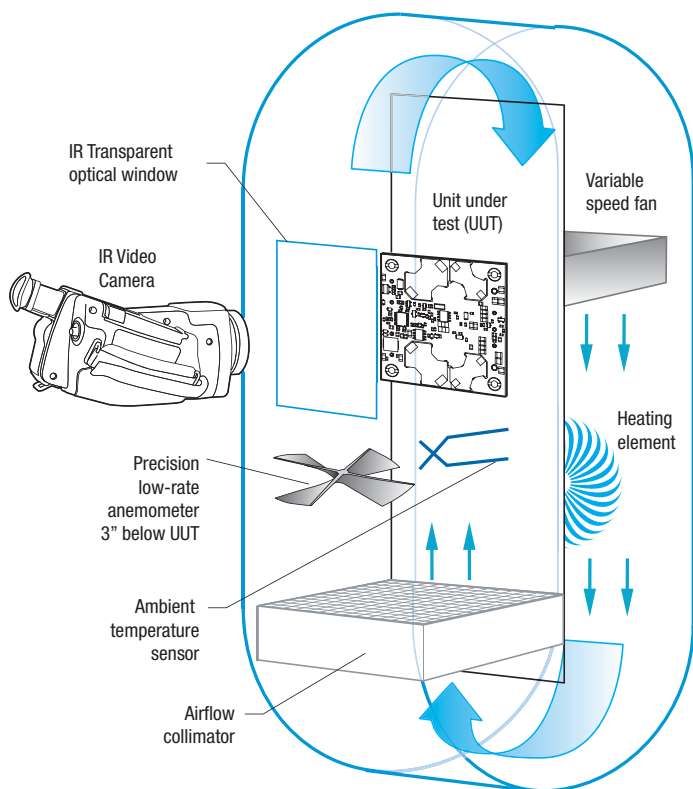


Figure 11. Vertical Wind Tunnel

## Vertical Wind Tunnel

Murata Power Solutions employs a computer controlled custom-designed closed loop vertical wind tunnel, infrared video camera system, and test instrumentation for accurate airflow and heat dissipation analysis of power products. The system includes a precision low flow-rate anemometer, variable speed fan, power supply input and load controls, temperature gauges, and adjustable heating element.

The IR camera monitors the thermal performance of the Unit Under Test (UUT) under static steady-state conditions. A special optical port is used which is transparent to infrared wavelengths.

Both through-hole and surface mount converters are soldered down to a 10" x 10" host carrier board for realistic heat absorption and spreading. Both longitudinal and transverse airflow studies are possible by rotation of this carrier board since there are often significant differences in the heat dissipation in the two airflow directions. The combination of adjustable airflow, adjustable ambient heat, and adjustable Input/Output currents and voltages mean that a very wide range of measurement conditions can be studied.

The collimator reduces the amount of turbulence adjacent to the UUT by minimizing airflow turbulence. Such turbulence influences the effective heat transfer characteristics and gives false readings. Excess turbulence removes more heat from some surfaces and less heat from others, possibly causing uneven overheating.

Both sides of the UUT are studied since there are different thermal gradients on each side. The adjustable heating element and fan, built-in temperature gauges, and no-contact IR camera mean that power supplies are tested in real-world conditions.

