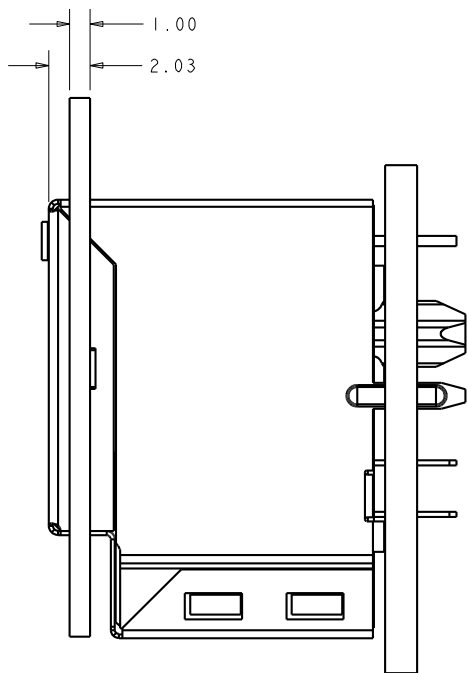
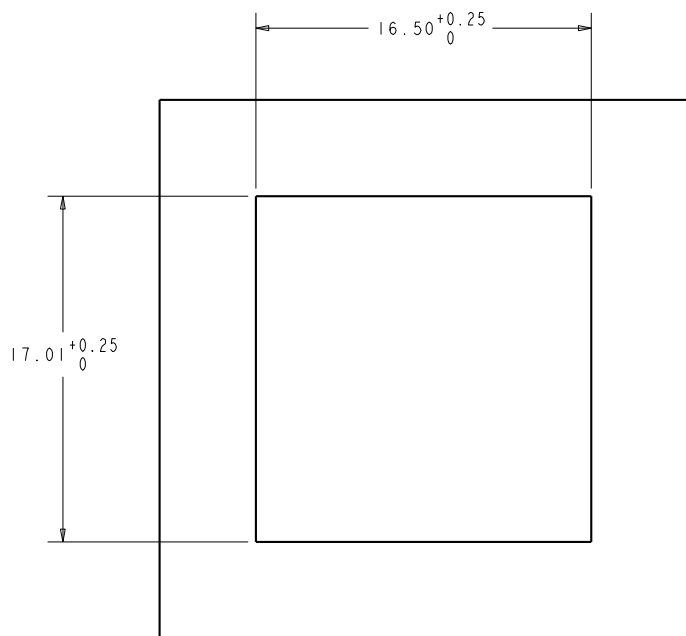
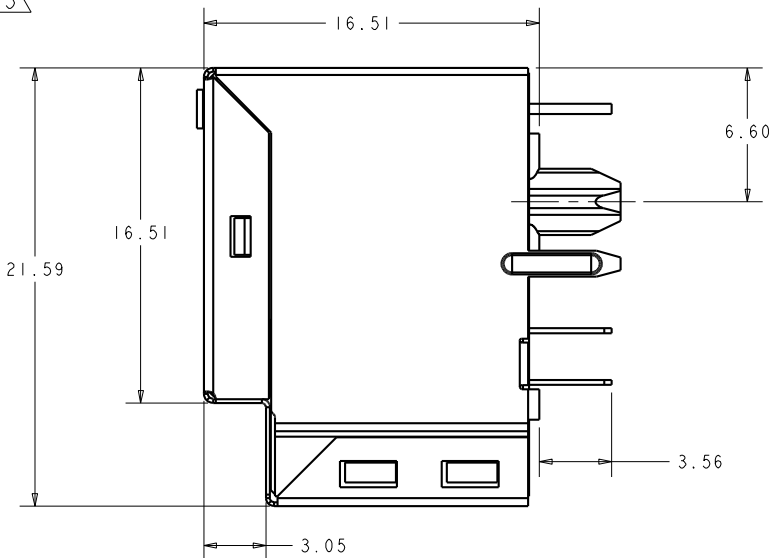
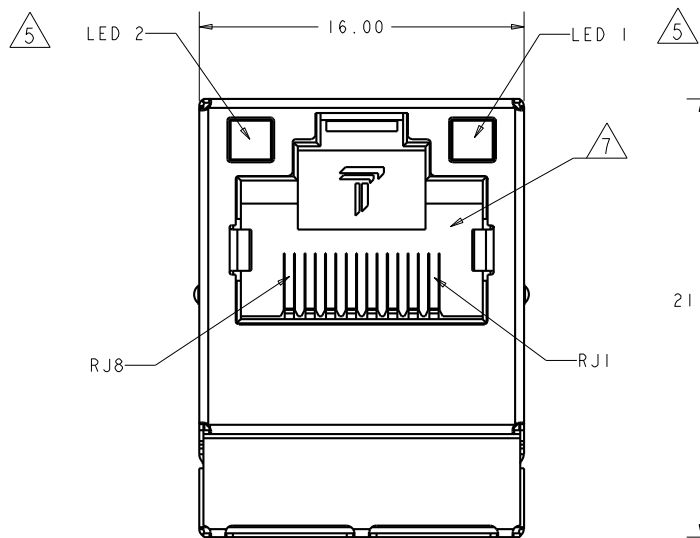
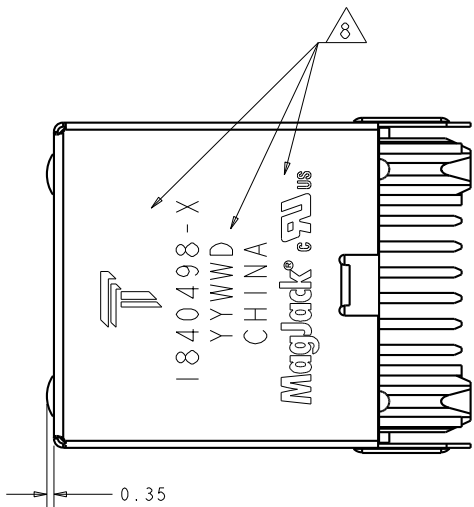
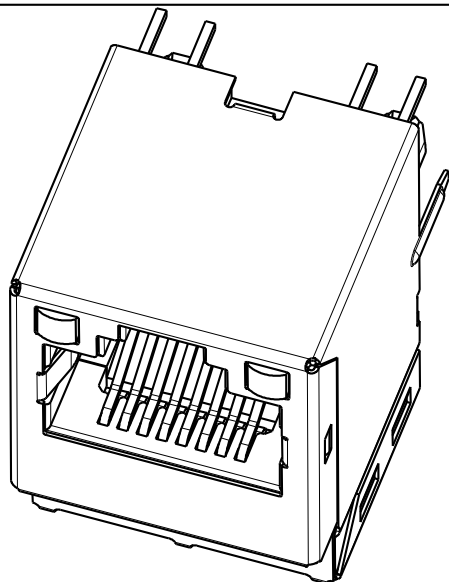


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PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS:
5736910 5939955 6425781 6428361 6554638 6840817 7123117
7429195 7717749 7808751 6217391 6149050 7924130

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	F	EC-1411035, LOGO CHANGE	19JAN2015	JC	TY



1. MATERIALS

HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATED UL 94V-0
SHIELD: BRASS, 0.20mm THICK,
PREPLATED WITH MIN 0.76um SEMI-BRIGHT NICKEL
POST DIPPED WITH 2.54um MIN SAC SOLDER AT GROUND PINS.
CONTACT: PHOSPHOR BRONZE,
WITH 1.27um MIN OVERALL NICKEL UNDERPLATE AND
SELECTIVE 1.27um MIN GOLD PLATING AT MATING INTERFACE
SOLDER TAIL: PHOSPHOR BRONZE,
WITH 1.27um MIN OVERALL NICKEL UNDERPLATE AND
PLATED WITH 2.54um MIN TIN
LED: DIFFUSED EPOXY LENS, 0.50mm SQUARE CARBON STEEL WIREFRAME LEADS
PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL
OVER 1.02um MIN COPPER UNDERPLATE POST PLATED WITH 2.54um MIN TIN

2

MAGNETICS

-APPLICATION: 10/100 BASE-T, EXTENDED TEMPERATURE
-IMPEDANCE: 100 OHMS
-TURNS RATIO (CHIP:CABLE): TX = 1:1, RX = 1:1
-OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
8mADC BIAS FROM -40°C TO +85°C, TX AND RX
-PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz
18-20LOG(f/30)dB MIN FROM 30.1MHz TO 60MHz
12dB MIN FROM 60.1MHz TO 80MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
-ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A
RISE TIME OF 500V/SEC)

3

THE MAGNETIC ARE SYMMETRICAL AND SUPPORT AUTO-MDIX OPERATION.

4. OPERATING TEMPERATURE: FROM -40°C TO +85°C

5

LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20 mA
LED COLOR: DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP @ IF=20 mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP @ IF=20 mA
DOMINANT WAVELENGTH (λD): YELLOW 588 nm TYP @ IF=20 mA
FORWARD VOLTAGE (VF): YELLOW 2.1V TYP @ IF=20 mA

6

ALL DIMENSIONS TO BE NOMINAL UNLESS OTHERWISE NOTED

7

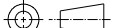
RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUB-PART F.

8

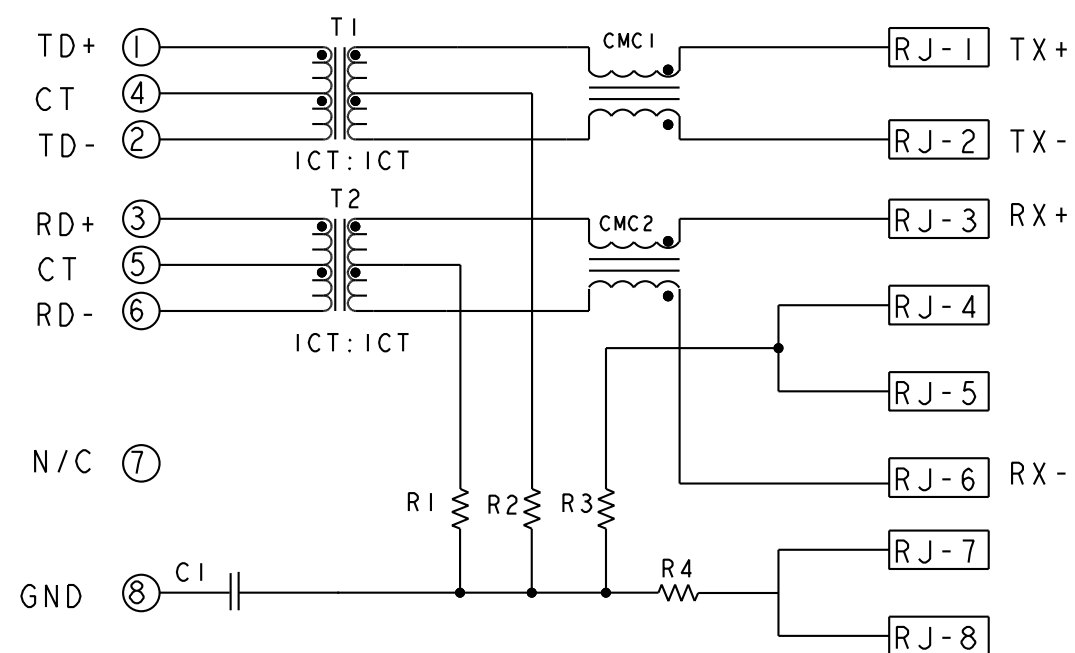
TRADEMARK, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN, AND
AGENCY APPROVAL MARKING IN APPROXIMATE LOCATION SHOW. DATE CODE: YY IS
YEAR, WW IS WORK WEEK, D IS DAY OF WEEK, WITH SUNDAY=1

9. THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS,
PEAK SOLDERING TEMPERATURE IS 260 °C MAX, 10 SECONDS MAX

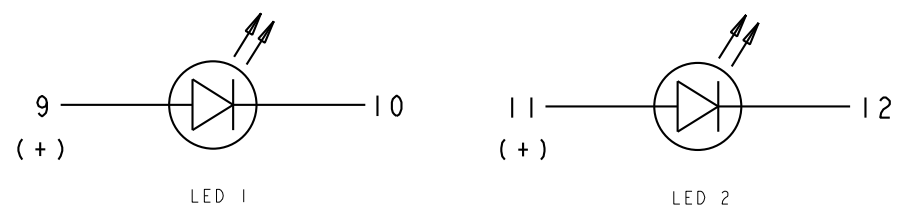
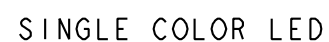
YES	YELLOW	GREEN	1-1840498-3
NO	GREEN/YELLOW	GREEN/YELLOW	1840498-6
NO	YELLOW	GREEN	1840498-1
CUT OUT	LED 1	LED 2	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN SUNNY HAO/JOHNSON 17AUG2010		 trp CONNECTOR a bel group		DONGGUAN CHINA	
DIMENSIONS: mm		CHK TOWER YU 17AUG2010					
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD KEITH ZHU 17AUG2010		DESC 1X1, MAGJACK, VERTICAL 426ETPI3, 10/100 GIGABIT CIRCUIT, W/ LEDS		RESTRICTED TO -	
		MODEL NAME MAGJACK SINGLE NON-POE					
PRODUCT SPEC 108-2100		APPLICATION SPEC -		SIZE A2		CAGE CODE C-1840498	
CUSTOMER DRAWING				SCALE 3:1		SHEET 1 OF 2	
				REV F			

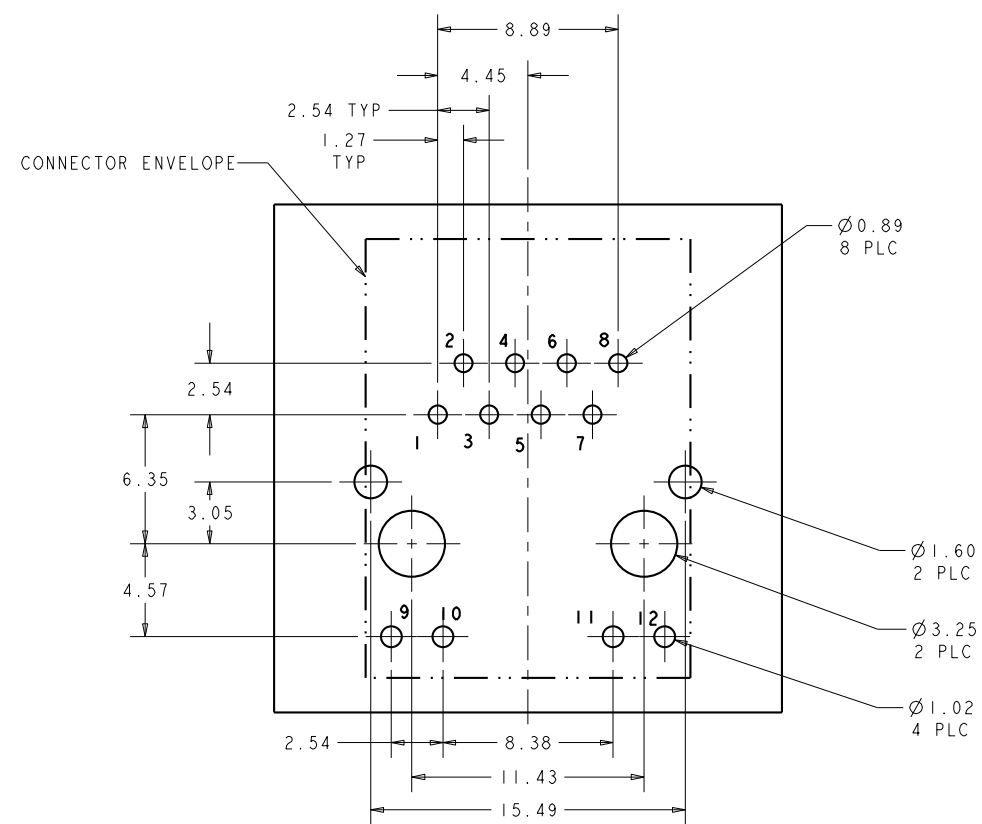
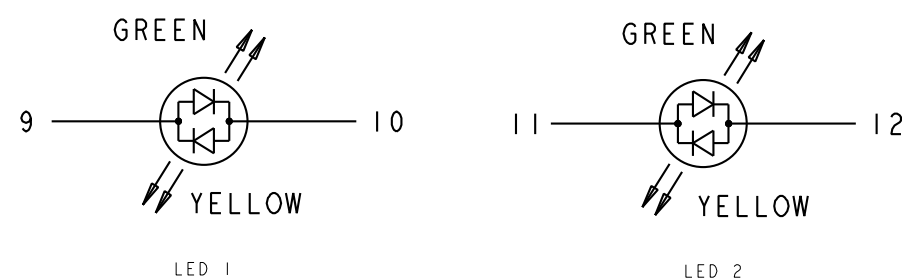
426ETP13 10/100 BASE-T CIRCUIT



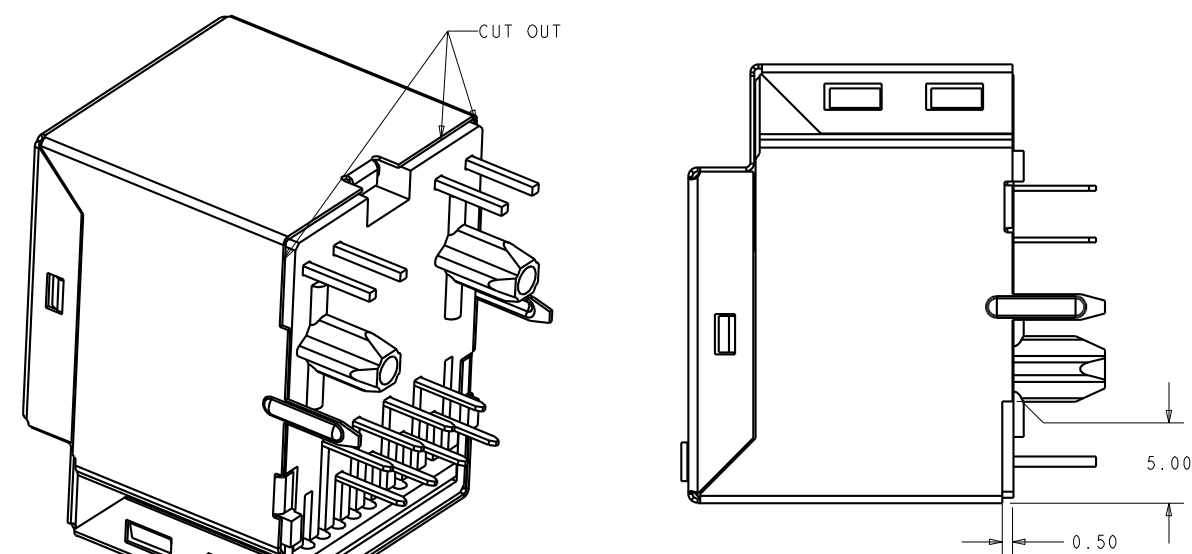
CI = 1000pF, 2KV, CAPACITOR
R1 - R4 = 75 ohms, RESISTOR



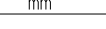
BI-COLOR LED



SUGGESTED PCB LAYOUT(TOL:±0.05)
(COMPONENT SIDE VIEW)



1 - 1840498 - 3

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN 17AUG2010 SUNNY HAO/JOHNSON		 trp CONNECTOR a bel group		DONGGUAN CHINA	
DIMENSIONS: mm		CHK 17AUG2010 TOWER YU APVD 17AUG2010 KEITH ZHU					
		TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ±0.25 4 PLC ± ANGLES ±		DESC 426ETPI3, 10/100 GIGABIT CIRCUIT, W/ LEDS		RESTRICTED TO	
PRODUCT SPEC 108-2100		APPLICATION SPEC -		SIZE A2 CAGE CODE DRAWING NO C=1840498		-	
CUSTOMER DRAWING				SCALE 3:1		SHEET 2 OF 2 REV F	