



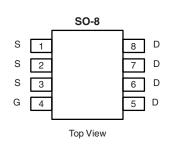
N-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
200	0.080 at V _{GS} = 10 V	4.0		
	0.090 at V _{GS} = 6.0 V	3.8		

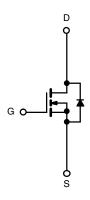
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4490DY-T1-E3 (Lead (Pb)-free) Si4490DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter	Symbol	10 s	Steady State	Unit		
Drain-Source Voltage		V _{DS}	200		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Drain Current /T 150 °C\a	T _A = 25 °C			2.85		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C	I _D	3.2	2.3	1	
Pulsed Drain Current		I _{DM}	40		Α	
Avalanch Current	L = 0.1 mH	I _{AS}	15			
Continuous Source Current (Diode Conduction) ^a		I _S	2.6	1.3		
	T _A = 25 °C	P _D	3.1	1.56	W	
Maximum Power Dissipation ^a	T _A = 70 °C	' D	2.0	1.0	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Manipulation to Applicate	t ≤ 10 s	R _{thJA}	33	40	
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	65	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	17	21	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

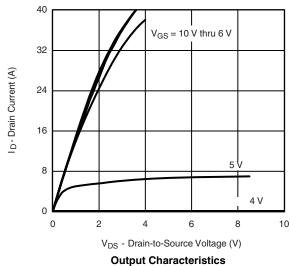
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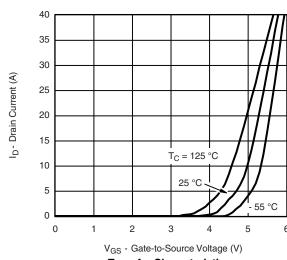


SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$ 2.0				V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zava Cata Valtaga Drain Current	1	V _{DS} = 160 V, V _{GS} = 0 V			1	4	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			Α	
	В	V _{GS} = 10 V, I _D = 4.0 A		0.065	0.080	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 6.0 \text{ V}, I_D = 4.0 \text{ A}$		0.070	0.090	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 5 A		19		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 2.8 A, V _{GS} = 0 V		0.75	1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			34	42	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 4.0 \text{ A}$		7.5			
Gate-Drain Charge	Q_{gd}			12.0			
Gate Resistance	R_{g}		0.2	0.85	1.3	Ω	
Turn-On Delay Time	t _{d(on)}			14	20		
Rise Time	t _r	$V_{DD} = 100 \text{ V}, R_{L} = 25 \Omega$		20	30		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 4.0 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		32	50	ns	
Fall Time	t _f			25	35		
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 2.8 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		70	100		

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





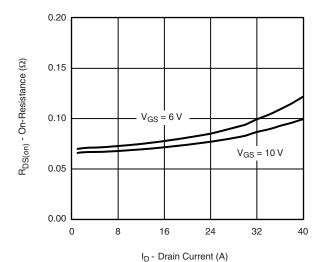
Notes: a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.



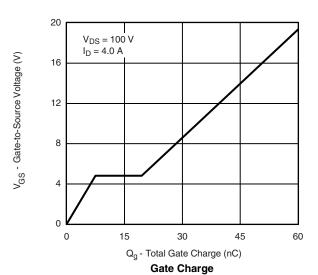


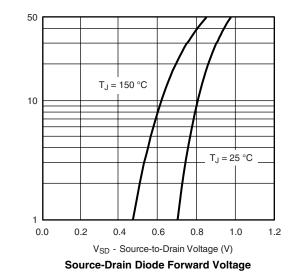


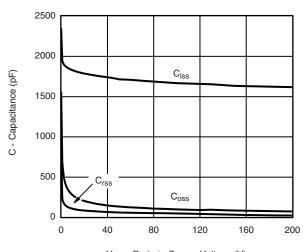
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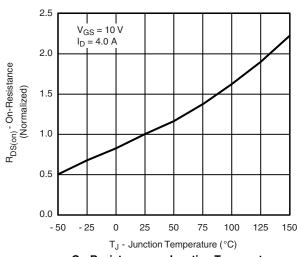
On-Resistance vs. Drain Current



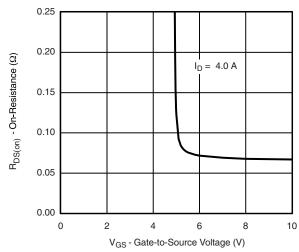




V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature



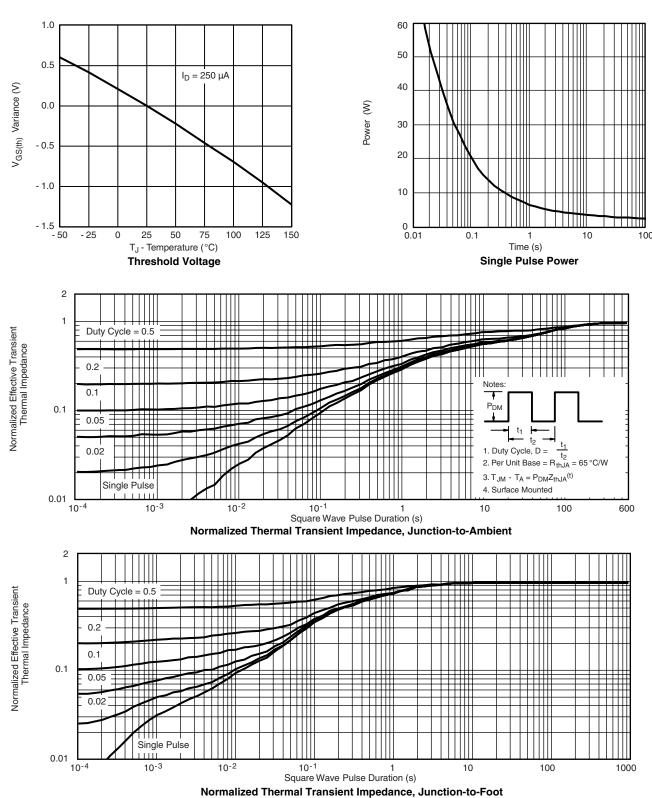
On-Resistance vs. Gate-to-Source Voltage

Is - Source Current (A)

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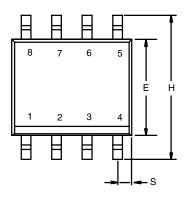
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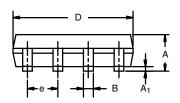


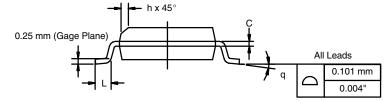
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







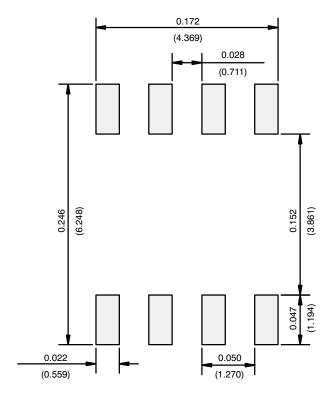
	MILLIM	IETERS	INC	HES	
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27	BSC	0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I. 11-Sep-06					

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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