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ADC1215S105F1 🚫 Obsolete

ADC1215S105F1 demo board; CMOS version; SPI, Regulators and CMOS buffer on board

IDT's ADC demonstration board is suitable for dynamic performance evaluations from low to high IF configuration. A Data acquisition board can be used to easily analyze the ADC performances in the design and prototyping phase.

Features

- Single power supply (on-board regulators)
- SMA connector for clock and analog inputs signal
- CMOS (buffered outputs)

Product Options

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ADC1215S105F1-DB	Obsolete	BD0	BOARD	0	I	No	Availability

Technical Documentation

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Demo Board Software ADC_1_r04	ZIP	2.33 MB	Jul 18, 2012

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ADC1215S105HN	Single 12-bit ADC 105 Msp/s with Input Buffer CMOS or LVDS DDR digital outputs

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Evaluation Board - No Image Available

