PERICOM®

## 16-to-8 Multiplexer/Demultiplexer Bus Switch

Features:
- Near-Zero propagation delay
- Low noise, 25-ohm version (PI5C32390)
- 5-ohm switches connect inputs to outputs (PI5C3390)
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2µA typical)
  – Ideally suited for notebook applications
- Packaging (Pb-free & Green available):
 —28-pin 150-mil wide plastic QSOP (Q)

Description:
Pericom Semiconductor's PI5C3390 and PI5C32390 are 16-to-8 multiplexer/demultiplexer bus switches with a low On-Resistance allowing inputs to be connected directly to outputs. The two enable inputs connect each of eight I/O to the common I/O pin. This multiplexer function can be used to select and route logic signals to form crossbar switches, isolate bus capacitance, or provide a zero delay switch connection. The bus switch creates no additional ground bounce noise or additional propagation delay. The PI5C32390 is designed with an internal 25-ohm resistor reducing noise reflection in high-speed applications.

Block Diagram
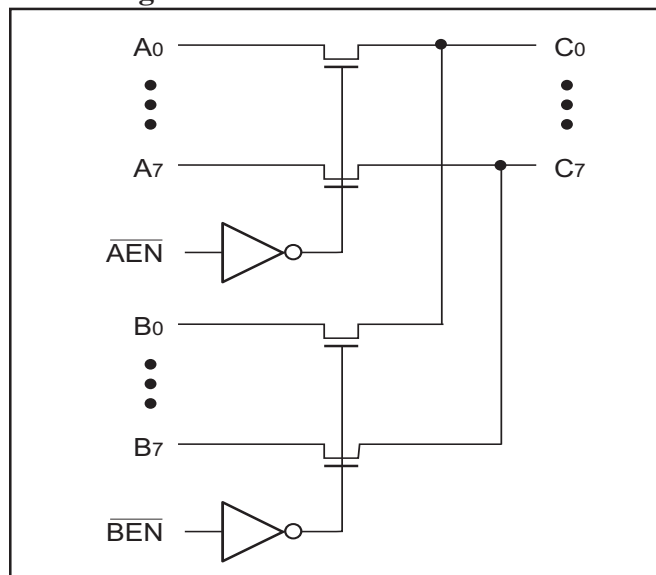
Pin Configuration
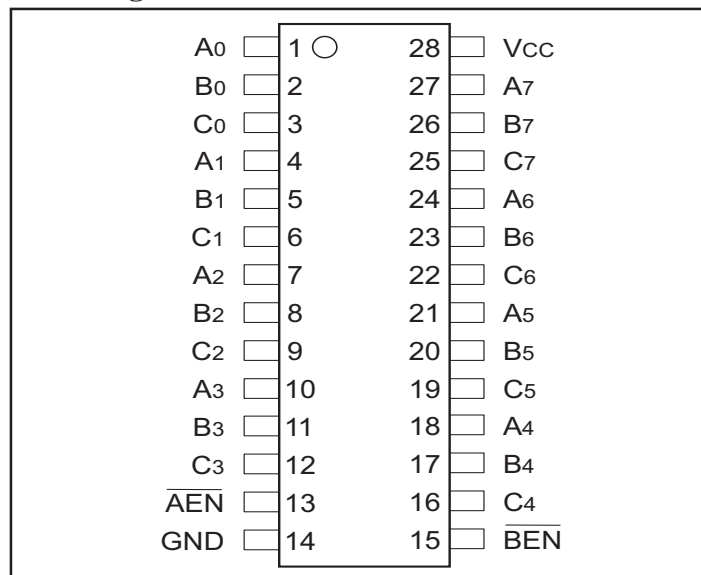
Truth Table[1]

$\overline{AEN}$	$\overline{BEN}$	A Sw	B Sw	Function
H	H	Off	Off	Disconnect
L	H	On	Off	A to C
H	L	Off	On	B to C
L	L	On	On	A, B to C

Note:
1. H = High Voltage Level
   L = Low Voltage Level

Pin Description

Pin Name	I/O	Description
$\overline{AEN}$, $\overline{BEN}$	I	Bus Output Enable (Active LOW)
A0-A7	I/O	Bus A
B0-B7	I/O	Bus B
C0-C7	I/O	Bus C

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature .... −65°C to +150°C	
Ambient Temperature with Power Applied .... −40°C to +85°C	
Supply Voltage to Ground Potential (Inputs & Vcc Only) .... −0.5V to +7.0V	
Supply Voltage to Ground Potential (Outputs & D/O Only) . −0.5V to +7.0V	
DC Input Voltage ....−0.5V to +7.0V	
DC Output Current .... 120 mA	
Power Dissipation 0.5W	

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = −40°C to +85°C, V_{CC} = 5V ±5%)

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level		−0.5		0.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}				±1	μA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND				±1	μA
I_{OZ}	High Impedance Output Current	$0 \le A, B, C \le V_{CC}$				±1	μA
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = −18 mA			−0.7	−1.2	V
I_{OS}	Short Circuit Current[3]	C = 0V; A, B = V_{CC}		100			mA
V_H	Input Hysteresis at Control Pins				150		mV
R_{ON}	Switch On Resistance[4]	V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48 mA	PI5C3390 PI5C32390	20	5 28	7 40	ohm
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15 mA	PI5C3390 PI5C32390	20	10 35	15 48	ohm

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at Vcc= 5.0V, T_A = 25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A, B, and C pins at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A,B,C) pins.

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[1]	Description	Test Conditions	Typ	Units
C_{IN}	Input Capacitance	V_{IN} = 0V	6	pF
C_{OFF}	A/B Capacitance, Switch Off	V_{IN} = 0V	6	pF
C_{ON}	A/B Capacitance, Switch On	V_{IN} = 0V	16	pF

Notes:
1. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ.[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	V_{IN} = GND or V_{CC}		0.2	30	µA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max.	V_{IN} = 3.4V[3]			3.5	mA
I_{CCD}	Supply Current per Input per MHz[4]	V_{CC} = Max., A, B, and C Pins Open $\overline{BE}$ = GND Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at Vcc = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); A, B, and C pins do not contribute to Icc.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A, B, and C inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

PI5C3390 Switching Characteristics over Operating Range

| Parameters	Description	Conditions[1]	PI5C3390			Unit
			Com			
			Min.	Typ.	Max.	
t_{PLH} t_{PHL}	Propagation Delay[2,3] A, B to/from C	C_L = 50 pF R_L = 500-ohm		0.25		ns
t_{PZH} t_{PZL}	Bus Enable Time $\overline{AEN}/\overline{BEN}$ to A, B, C		1.5		6.5	ns
t_{PHZ} t_{PLZ}	Bus Disable Time $\overline{AEN}/\overline{BEN}$ to A, B, C		1.5		5.5	ns

Notes:
1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

PI5C32390 Switching Characteristics over Operating Range

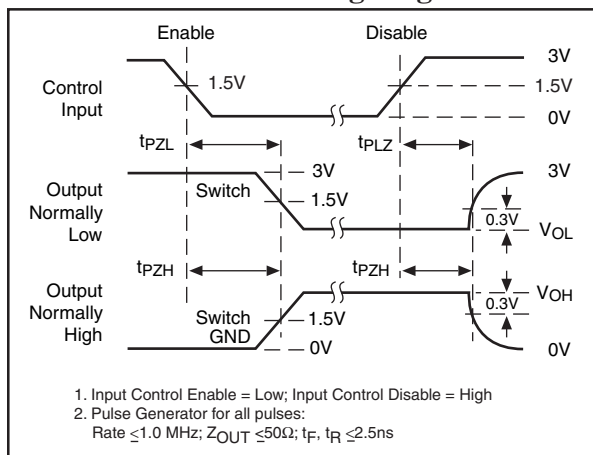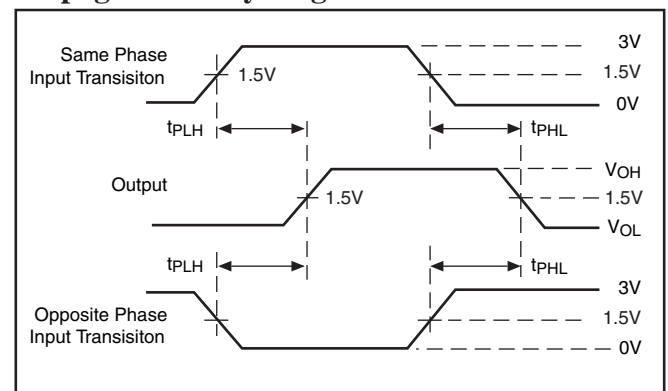| Parameters	Description	Conditions[1]	PI5C32390			Unit
			Com			
			Min.	Typ.	Max.	
t_{PLH} t_{PHL}	Propagation Delay[2,3] A, B to/from C	$C_L = 50$ pF $R_L = 500$-ohm		1.25		ns
t_{PZH} t_{PZL}	Bus Enable Time $\overline{AEN/BEN}$ to A, B, C		1.5		6.5	ns
t_{PHZ} t_{PLZ}	Bus Disable Time $\overline{AEN/BEN}$ to A, B, C		1.5		5.5	ns

Notes:
1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Test Circuit
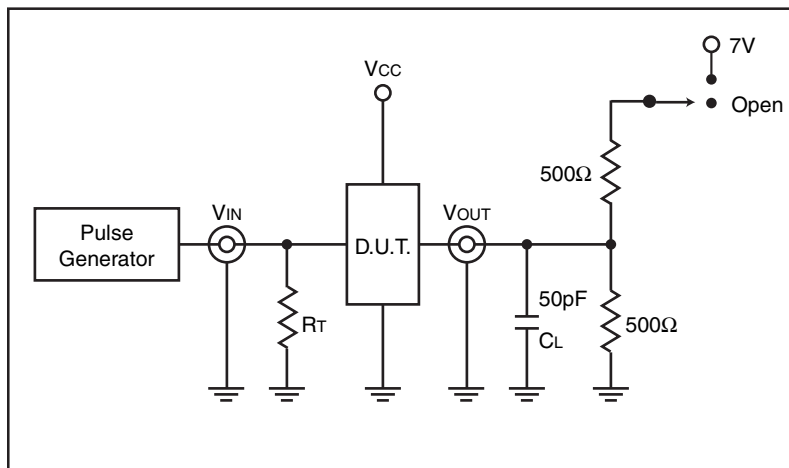
Switch Position

Test	Switch
Disable LOW	Closed
Enable LOW	Closed
t_{PD}	Open

Definitions:
C_L = Load capacitance (includes jig and probe capacitance)
R_T = Termination resistance (should be equal to Z_{OUT} of the pulse generator)

Enable and Disable Timing Diagram

1. Input Control Enable = Low; Input Control Disable = High
2. Pulse Generator for all pulses:
   Rate ≤ 1.0 MHz; $Z_{OUT} \leq 50\Omega$; t_F, $t_R \leq 2.5$ns

Propagation Delay Diagram

Packaging Mechanical: 28-pin QSOP (Q)

DOCUMENT CONTROL NO.
PD - 1204

REVISION: E
DATE: 03/09/05

$$\frac{0.150}{0.157} \quad \frac{3.81}{3.99}$$

$$\frac{.386}{.394} \quad \frac{9.804}{10.009}$$

$$\frac{.033}{0.84} \text{ REF}$$

$$\frac{1.35}{1.75} \quad \frac{.053}{.069}$$

SEATING
PLANE

$$\frac{.004}{.010} \quad \frac{0.101}{0.254}$$

.025
BSC
0.635

$$\frac{.008}{.012} \quad \frac{0.203}{0.305}$$

15°
MAX.

$$\frac{.008}{0.20} \text{ MIN.}$$

$$\frac{.008}{.013} \quad \frac{0.20}{0.33}$$

Guage Plane

$$\frac{.010}{0.254}$$

15°
MAX.

$$\frac{.016}{.035} \quad \frac{0.41}{0.89}$$

0°-6°

$$\frac{.041}{1.04} \text{ REF}$$

Detail A

.015 x 45°

Detail A

$$\frac{.007}{.010} \quad \frac{0.178}{0.254}$$

$$\frac{0.41}{1.27} \quad \frac{.016}{.050}$$

$$\frac{.228}{.244}$$

$$\frac{5.79}{6.19}$$

$$\boxed{\begin{matrix} X.XX \\ X.XX \end{matrix}} \begin{matrix} \text{DENOTES DIMENSIONS} \\ \text{IN MILLIMETERS} \end{matrix}$$

Note:
1) Controlling dimensions in inches.
2) Ref: JEDEC MO-137B/AF

Pericom Semiconductor Corporation
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1-800-435-2335 • www.pericom.com

DESCRIPTION: 28-Pin, 150-Mil Wide, QSOP

PACKAGE CODE: Q

Ordering Information

Ordering Code	Package Code	Package Description
PI5C3390Q	Q	28-pin 150-mil wide plastic QSOP
PI5C3390QE	Q	Pb-free & Green, 28-pin 150-mil wide plastic QSOP
PI5C32390Q	Q	28-pin 150-mil wide plastic QSOP
PI5C32390QE	Q	Pb-free & Green, 28-pin 150-mil wide plastic QSOP

Notes:
- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel