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Status: **Obsolete**

ADC1412D065F2

ADC1412D065F2 demo board; LVDS version; SPI, Regulators and LVDS outputs on board

IDT's dual channel ADC demonstration board is suitable for dynamic performances evaluation from low to high IF configuration with LVDS DDR outputs. A Data acquisition board can be used to easily analyze the ADC performances in the design and prototyping phase.



Product Quick Links

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Features

- SMA connector for clock and analog inputs signal
- LVDS/DDR Outputs

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Sample & Buy
ADC1412D065F2-DB	Obsolete	BD0	BOARD	0	I	No	Check Availability

Technical Documentation

	Title	Type	Format	File Size	Date
User Guides & Manuals					
	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

Related Products

Product ID	Title
ADC1412D065HN	Dual 14-bit ADC 65 Msp/s CMOS or LVDS DDR digital outputs

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