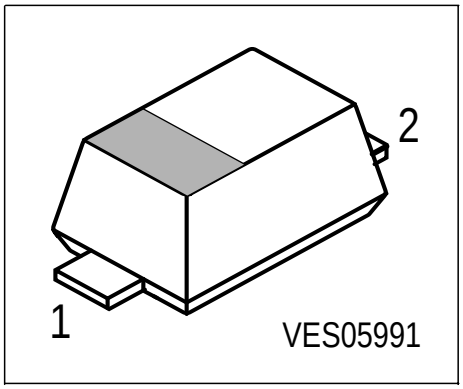
Silicon Tuning Diode

Preliminary data

- For SAT - Indor - units
- High capacitance ratio
- Low series inductance
- Excellent uniformity and matching due to "in-line" matching assembly procedure



Type	Marking	Pin Configuration			Package
BB867-02V	YY	1 = C	2 = A	-	SC79

Maximum Ratings

Parameter	Symbol	Value	Unit
Diode reverse voltage	V_R	30	V
Peak reverse voltage-	V_{RM}	35	
Forward current	I_F	20	mA
Operating temperature range	T_{op}	-55 ... 150	°C
Storage temperature	T_{stg}	-55 ... 150	

Electrical Characteristics at T_A = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Reverse current	I_R				nA
V_R = 30 V		-	-	10	
V_R = 30 V, T_A = 85 °C		-	-	200	
AC Characteristics					
Diode capacitance-	C_T				pF
V_R = 1 V, f = 1 MHz		8	8.7	9.4	
V_R = 25 V, f = 1 MHz		0.5	0.55	0.6	
V_R = 28 V, f = 1 MHz		0.45	0.52	-	
Capacitance ratio V_R = 1 V, V_R = 25 V, f = 1 MHz	C_{T1}/C_{T25}	14	15.8	-	-
Capacitance ratio V_R = 1 V, V_R = 28 V, f = 1 MHz	C_{T1}/C_{T28}	-	16.7	-	
Capacitance matching- V_R = 1 V, V_R = 28 V, f = 1 MHz	$\Delta C_T/C_T$	-	-	5	%
Series resistance V_R = 5 V, f = 470 MHz	r_S	-	2.8	-	Ω
Series inductance	L_S	-	0.6	-	nH

Diode capacitance $C_T = f(V_R)$

f = 1MHz