

*RoHS COMPLIANT
VERSIONS
AVAILABLE



BOURNS®

Features

- For use with Globespan ADSL chipset
- Excellent Total Harmonic Distortion (THD) Performance
- Exceeds standards for ANSI and ETSI
- Isolation voltages up to 2000 Vrms
- Designed to meet UL1950 and EN60950 supplementary insulation requirements for operating voltages up to 250 Vrms

- Operating temperature -40 °C to +85 °C
- Lead free version available (see How to Order)
- Lead free versions are RoHS compliant*

Applications

- ADSL - Globespan

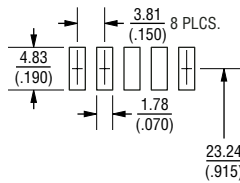
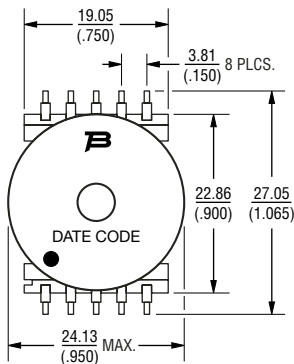
PT542-1 to SM547-1 ADSL & RADSL Transformers

Electrical Specifications @ 25 °C

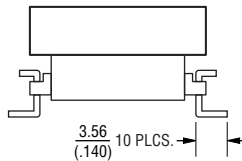
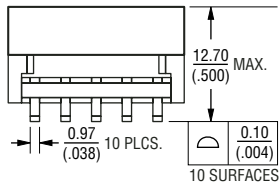
Part Number	Turns Ratio Chip-Line (±2 %)	OCL Primary (1-4) with (2-3) shorted (μH -10 %)	Leakage Inductance (μH max.)	DCR @ (1-3), (2-4), (10-8), (9-7) (Ω max.)	Isolation Voltage (Vrms)	Insertion Loss (dB max.)	Longitudinal Balance (dB typ.)	Package
PT542-1	1.4:1.4:1:1	940	10	1.2	1500	3	62	THT
PT543-1	1:1:1:1	410	10	0.8	1500	3	62	THT
PT544-1	1.4:1.4:1:1	940	10	1.2	3000	3	62	THT
PT545-1	1:1:1:1	410	10	0.8	3000	3	62	THT
SM546-1	1.4:1.4:1:1	940	10	1.2	3000	3	62	SMT
SM547-1	1:1:1:1	410	10	0.8	3000	3	62	SMT

Product Dimensions

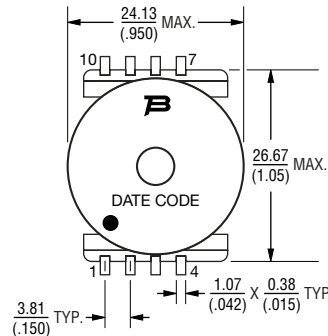
SMT



Recommended PAD layout

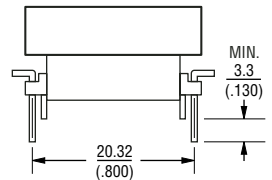
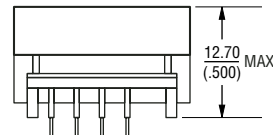


THT



DIMENSIONS ARE: $\frac{\text{MM}}{(\text{INCHES})}$

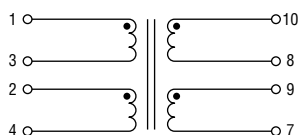
TOLERANCE: $\pm \frac{0.25}{(.010)}$



Packaging Specifications

Tube35 pcs./tube

Electrical Schematic



How to Order

PT or SM5xx-1

Model _____
Termination _____
Blank = Tin-lead
L = Tin only (lead free)

03/05

*RoHS Directive 2002/95/EC Jan 27 2003 including Annex
Specifications are subject to change without notice.
Customers should verify actual device performance in their specific applications.