

HA13481S

Three Phase Motor Driver with Speed Discriminator

Description

The HA13481S is hall sensorless three-phase brushless DC motor driver for HDD and, have following functions and features.

Functions

- Back-EMF detector
- Start up circuit
- Running commutation
- Output amp. (Driving capability; 2.0 A/phase)
- Zerocross detector
- Programmable frequency divider
- OSC
- Speed discriminator
- Charge pump
- Control amp.
- Current limiter
- Start up monitor
- Ready signal
- Chip enable
- Motor on/off control
- Internal protector (OTSD, LVI)

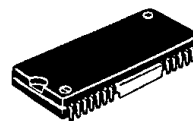
Features

- Hall sensorless
- Low saturation voltage; 2.0 V max.
(@ $I_O = 1.0$ A)
- Applicable for both 4 MHz and 5 MHz clock
- Small surface mount package ($\theta_{j-c} \leq 7^\circ\text{C/W}$)

Ordering Information

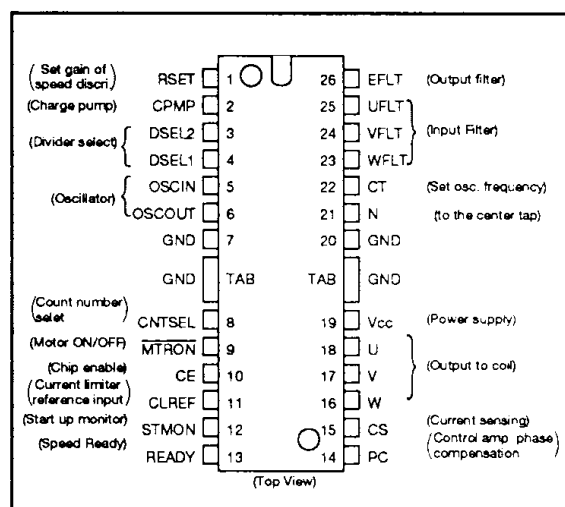
Type No.	Package
HA13481S	MP-26DT

HA13481S

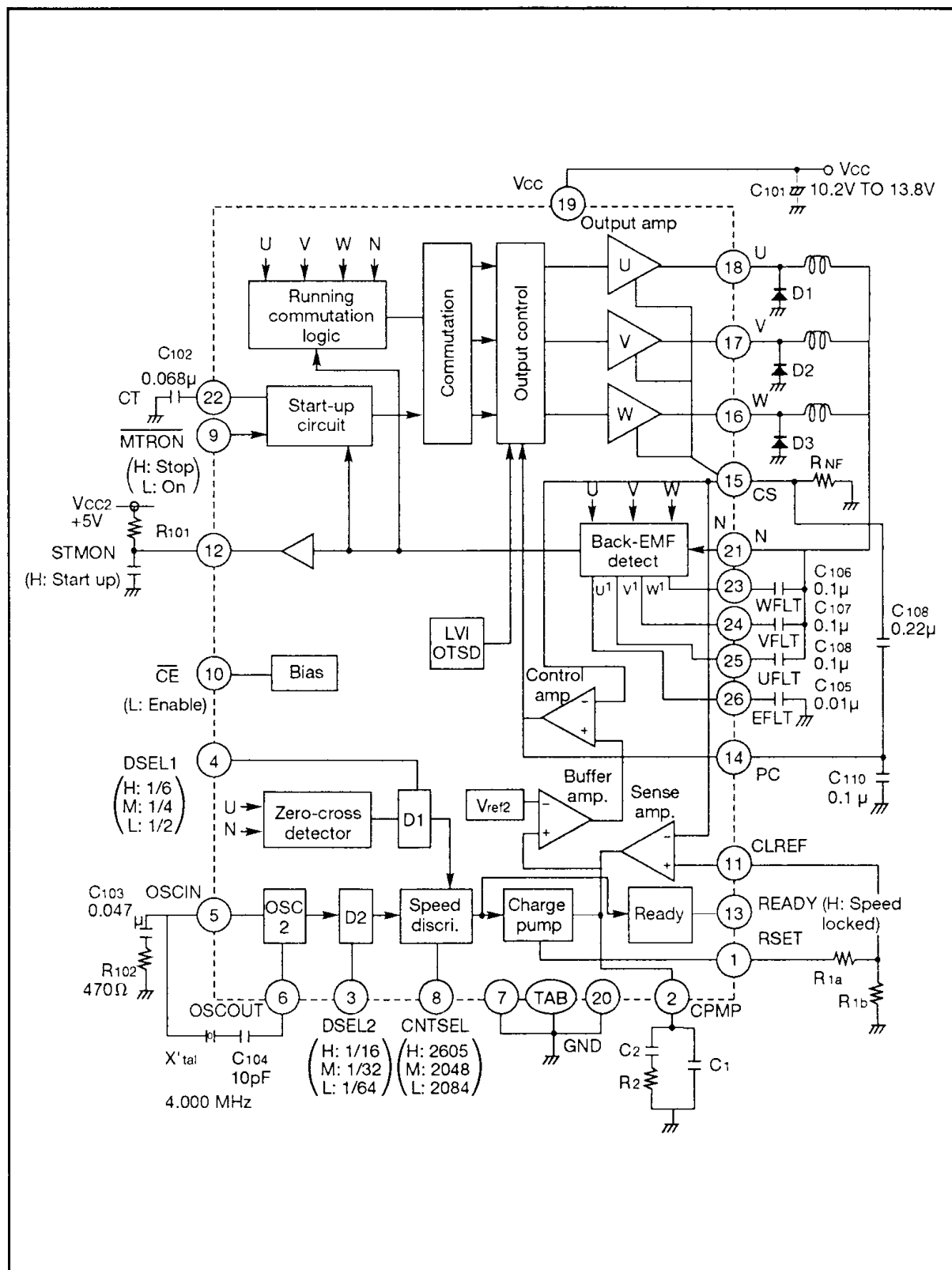


(MP-26DT)

Pin Arrangement



Block Diagram



External Components

Parts No.	Recommended Value	Purpose	Notes
R ₁₀₁	47 kΩ ≤ R ₁₀₁ ≤ 1 MΩ	Pull up	1
R ₁₀₂	470 kΩ	Oscillation stability	2
R _{1a}	3.9 kΩ	V/I converter	3
R _{1b}	1.3 kΩ	V/I converter	
R ₂	560 kΩ	Integral constant	
C ₁₀₁	10 μF and 0.1 μF	Power supply by-passing	
C ₁₀₂	0.068 μF	Oscillation	1
C ₁₀₃	0.047 μF	Oscillation stability	2
C ₁₀₄	10 μF ± 1%	AC-coupling	
C ₁₀₅	0.01 μF	Output filter	4
C ₁₀₆ , C ₁₀₇ , C ₁₀₈	0.1 μF	Input filter	5
C ₁₀₉	0.22 μF	Control amp phase compensation	
C ₁₁₀	0.1 μF		
C ₁₁₁	—	Time constant	1
C ₁	0.022 μF	Integral constants	3
C ₂	0.22 μF		
D ₁ , D ₂ , D ₃	—	Output clamp	6
Xtal	—	Oscillation	7
R _{NF}	—	Current sensing	8

Notes: 1. By adding the C₁₁₁ on pin 12, the excitation frequency of the motor transit from high frequency to low frequency. By this operation, the back motion of the motor at starting condition can be minimized. The oscillation frequency f_{osc1} , the C₁₀₂ and the time constant C₁₁₁ • R₁₀₁ are determined by following equations.

$$2 \times 10^{-3} \times \frac{N_o \cdot P}{V_E} < f_{osc1} < 0.25 \sqrt{\frac{I_o \cdot K_T \cdot P}{J}} \quad [\text{Hz}] \dots\dots\dots(1)$$

$$C_{102} = \frac{1}{f_{osc1} \cdot (V_{th1} - V_{th2})} \cdot \frac{I_{ts} \cdot I_{tf}}{I_{ts} + I_{tf}} = 0.52 / f_{osc1} \quad [\mu\text{F}] \dots\dots\dots(2)$$

$$C_{111} \cdot R_{101} > \frac{1}{I_n [V_{CC2} / (V_{CC2} - 2.5)] \cdot f_{osc1}} \quad [\text{S}] \dots\dots\dots(3)$$

Where,

- N_o : Standard rotation speed (rpm)
- P : Number of pole
- V_I : Back EMF at running (Vp-p)
- I_o : Starting up current (A)
- K_T : Torque constant (kg • cm/A)
- J : Moment of inertia (kg • cm • S²)

- 2. Unnecessary below 4 MHz oscillation frequency.
- 3. The integral constant can be designed as

$$\omega_o \leq \frac{2\pi}{10 \cdot m} \times \frac{N_o}{60} \times \frac{P}{2} \dots\dots\dots(4)$$

$$\frac{R_2}{R_{1a} + R_{1b}} = \frac{4}{9.55} \times \frac{R_{NF} \cdot J \cdot \omega_o \cdot N_o}{V_{ref}^2 \cdot K_T \cdot G_{CTL}} \dots\dots\dots(5)$$

$$R_{1a} + R_{1b} \leq 25 \text{ k}\Omega \dots\dots\dots(6)$$

$$C_1 = 1 / (\sqrt{10 \cdot \omega_o \cdot R_2}) \quad [\text{F}] \dots\dots\dots(7)$$

$$C_2 = 10 \cdot C_1 \quad [\text{F}] \dots\dots\dots(8)$$



where

V_{ref2} : output voltage at pin 1 (= 1.0 V) [V]

K_T : torque constant [kg • cm/A]

G_{CTL} : gain from pin 2 to pin 15 (= 0.5)

4. When the C_{102} is less than 0.047 μ F, the capacitor C_{105} is necessary.

5. Select the value of C_{106} , C_{107} and C_{108} as follows.

4 pole motor $\rightarrow$ 0.22 (μ F)

8 pole motor $\rightarrow$ 0.1 (μ F)

12 pole motor $\rightarrow$ 0.068 (μ F)

6. Some motors require these components.

7. Osc. frequency f_{osc2} is determined by the following equation:

$$f_{osc2} = \frac{N_o}{60} \times \frac{P}{2} \times \frac{n}{m} \times (1 - 0.5) \dots\dots\dots(9)$$

where,

N_o : Standard rotation speed (rpm)

P : Number of pole

l : Count number

$l = 2084$ (@ Pin 8 = Low)

$= 2048$ (@ Pin 8 = Open)

$= 2605$ (@ Pin 8 = High)

m : Dividing ratio on divider 1

$m = 2$ (@ Pin 4 = Low)

$= 4$ (@ Pin 4 = Open)

$= 6$ (@ Pin 4 = High)

n : Dividing ratio on divider 2

$n = 64$ (@ Pin 3 = Low)

$= 32$ (@ Pin 3 = Open)

$= 16$ (@ Pin 3 = High)

In order to minimize the error of rotation speed, select the dividing ratio m as follows:

at 4 pole motor $\rightarrow m = 2$

at 8 pole motor $\rightarrow m = 4$

at 12 pole motor $\rightarrow m = 6$

example (for $N_o = 3,600$ rpm)

Pin 8	Pin 3	f_{osc} [MHz]		
		Pin 4 = Low (for 4 pole)	Pin 4 = Open (for 8 pole)	Pin 4 = High (for 12 pole)
Low	Low	8.000	8.000	8.000
Low	Open	4.000	4.000	4.000
Low	High	2.000	2.000	2.000
Open	Low	7.862	7.862	7.862
Open	Open	3.931	3.931	3.931
Open	High	1.966	1.966	1.966
High	Low	—	—	—
High	Open	5.000	5.000	5.000
High	High	2.500	2.500	2.500

8. Output maximum current is determined by the following equation: $I_{omax} = V_{CLREF}/R_{NF} \dots\dots\dots(10)$

9. Fig. 1 shows the snubber circuit to reduce the spike noise at phase commutation.

10. Fig. 2 shows the application to use external clock with TTL level.

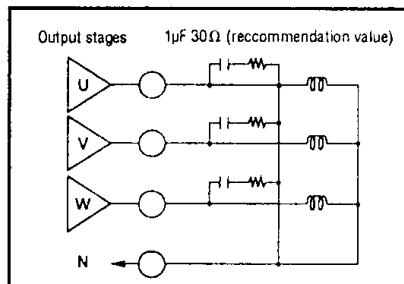


Figure 1 Snubber circuit

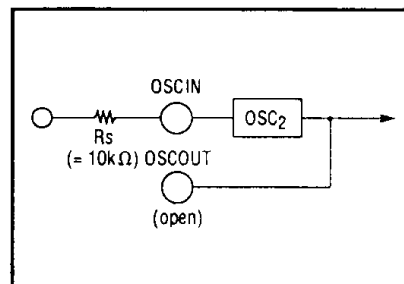
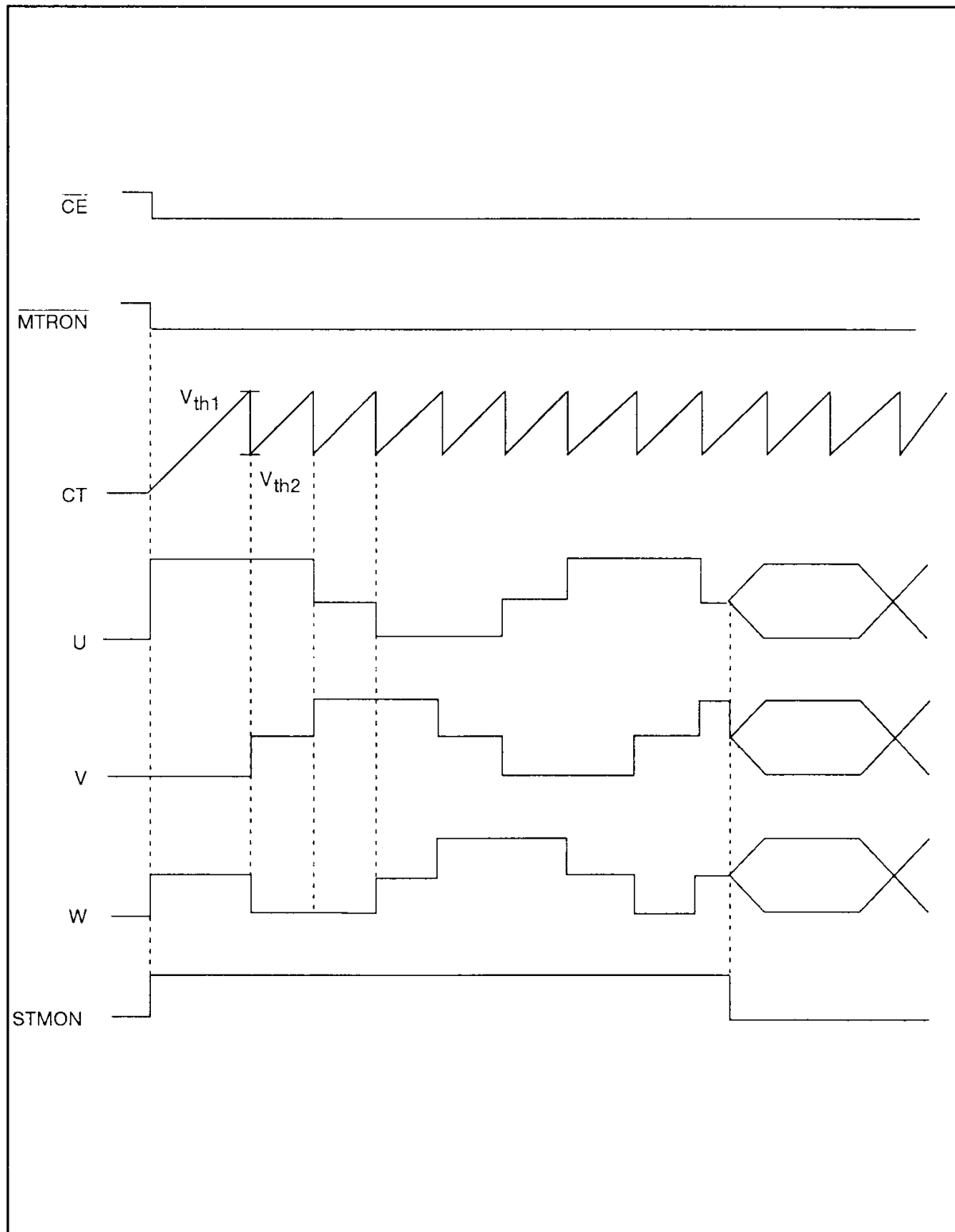


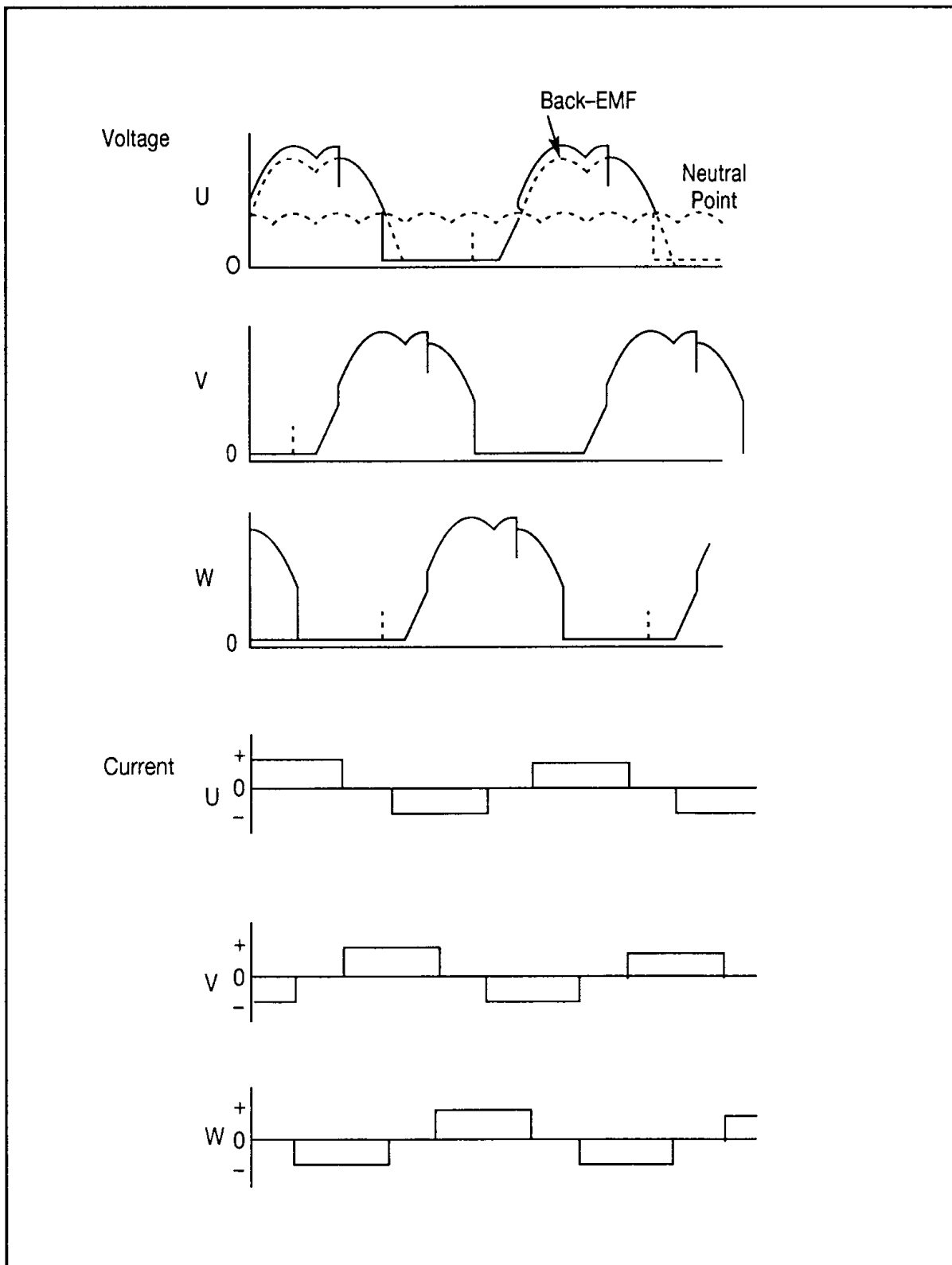
Figure 2 Application for external CLK

Timing Chart

Start-up



Running



Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Rating	Unit	Notes
Power supply voltage	V_{CC}	+15	V	1
Input voltage	V_{IN}	V_{CC}	V	2
Output current	I_O	+2	A	3
Power dissipation	P_T	5	W	4
Junction temperature	T_j	+150	$^\circ\text{C}$	5
Storage temperature	T_{stg}	-55 to +125	$^\circ\text{C}$	

The absolute maximum ratings are limiting values, to be applied individually, beyond which the device may be permanently damaged. Functional operation under any of these conditions is not guaranteed. Exposing a circuit to its absolute maximum rating for extended periods of time may affect the device's reliability.

- Notes: 1. Operating voltage range is 10.2 V to 13.8 V.
 2. Applied to CE, MTRON, CNTSEL, DSEL 1 and DSEL2 inputs.
 3. ASO. of upper and lower power transistors are shown below.
 Operating locus must be within the ASO.

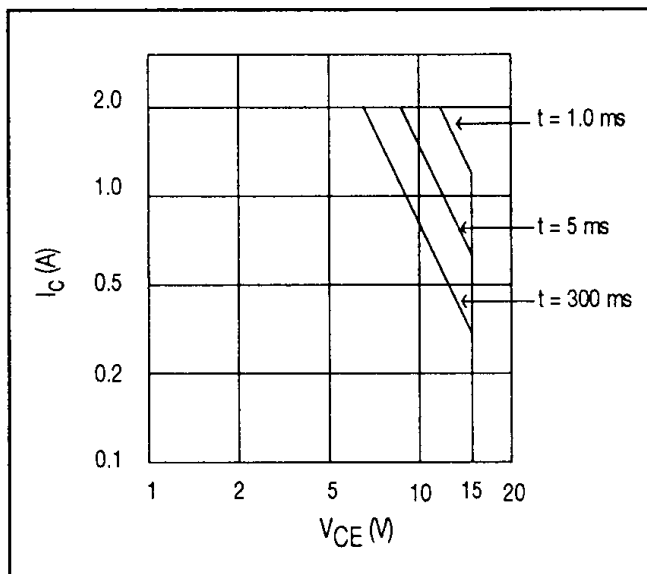


Figure 3 ASO. of Output Stages

4. Value at $T_C = 115^\circ\text{C}$
 Thermal resistance is shown below.
 $\theta_{j-c} \leq 7^\circ\text{C/W}$, $\theta_{j-a} \leq 15^\circ\text{C/W}$ (Using Fe board), $\theta_{j-a} \leq 62^\circ\text{C/W}$ (Using glass epoxide board)
 5. Operating junction temperature is $T_{jop} = 0^\circ\text{C}$ to $+125^\circ\text{C}$

HA13481S

Electrical Characteristics (Ta = 25°C, V_{CC} = 12 V)

Block	Item	Symbol	Min	Typ	Max	Unit	Test conditions	Appli- cable Terminal	Notes	
Total	Quiescent current	I _{CC1}	—	25	38	mA	Pin 10 = 0.0 V	19		
		I _{CC2}	—	—	4.0		Pin 10 = 5.0 V			
CE	Input low voltage	V _{IL1}	—	—	0.8	V	Enable	10		
	Input high voltage	V _{IH1}	2.0	—	—		Disable			
	Input low current	I _{IL1}	—	—	±100	μA	Pin 10 = 0.0 V			
	Input high current	I _{IH1}	—	—	±100		Pin 10 = 5.5 V			
Motor on	Input low voltage	V _{IL2}	—	—	0.8	V	Motor on	9		
	Input high voltage	V _{IH2}	2.0	—	—		Motor off			
	Input low current	I _{IL1}	—	—	±100	μA	Pin 9 = 0.0 V			
	Input high current	I _{IH1}	—	—	±100		Pin 9 = 5.5 V			
Output amp.	Leak current	I _{CER1}	—	—	2.0	mA	V _{CE} = 15 V	16,17,18		
	Saturation voltage	V _{sat1}	—	—	1.2	V	I _O = 0.3 A		1	
		V _{sat2}	—	—	2.0		I _O = 1.0 A			
		V _{sat3}	—	—	3.0		I _O = 1.5 A			
	Current limiter	Ref. voltage	V _{OCL}	V _{CLREF} -25	V _{CLREF} ±0	V _{CLREF} +25	mV	V _{CLREF} = 250 mV, R _{NF} = 1.0 Ω	14,15	2
	Input current	I _{ICL}	—	—	±5	μA				
B-EMF detector	Min. input sensitivity	V _{min1}	70	100	130	mV _{P-P}	Pin 21 = 4.2 V	21,23,24, 25,26		
Start-up circuit	OSC	Threshold voltage	V _{th1}	3.3	3.5	3.7	V		22	3
		Threshold voltage	V _{th2}	0.3	0.5	0.7				
		Sink current	I _{ts}	40	55	80	μA	Pin 22 = 2.0 V Pin 12 = 5.0 V		
		Source current	I _{tf}	0.9	1.6	2.8				
	Start-up monitor	Leak current	I _{CER2}	—	—	±100	μA	V _{CE} = 15 V	12	4
	Low voltage	V _{OL2}	—	—	0.4	V	I _O = 1.0 mA			
Running communication	Min. input sensitivity	V _{min2}	20	—	—	mV _{P-P}	Pin 21 = 2.8 V	16,17, 18,21	6	
Control amp.	Gain	G _{ctl}	-8	-6	-4	dB		2,15		
	Internal reference	V _{ref1}	0.9	1.0	1.1	V				
Oscillator	Frequency error	f _{err}	—	—	±0.1	%	X'tal	5,6	7	
	Operating frequency	f _{osc2}	—	—	8	MHz				
Zero-cross detector	Min. input sensitivity	V _{min3}	500	—	—	mV _{p-p}	Pin 21 = 4.2V	18,21		
Divider select 1	Input low voltage	V _{IL3}	—	—	1.0	V	DIV. rate = 1/2	4		
	Input middle voltage	V _{IM3}	2.0	—	3.1		DIV. rate = 1/4			
	Input high voltage	V _{IH3}	3.9	—	—		DIV. rate = 1/6			
	Input low current	I _{IL3}	-150	-105	-75	μA	Pin 4 = 0.0 V	4		
	Input high current	I _{IH3}	95	135	190		Pin 4 = 5.5 V			



Electrical Characteristics (Ta = 25°C, V_{CC} = 12 V)

Block	Item	Symbol	Min	Typ	Max	Unit	Test conditions	Appli- cable Terminal	Note
Divider select 2	Input low voltage	V _{IL4}	—	—	1.0	V	DIV. rate = 1/643	3	7
	Input middle voltage	V _{IM4}	2.0	—	3.1		DIV. rate = 1/32		
	Input high voltage	V _{IH4}	3.9	—	—		DIV. rate = 1/16		
	Input low current	I _{IL4}	-150	-105	-75	μA	Pin 3 = 0.0 V		
	Input high current	I _{IH4}	95	135	190		Pin 3 = 5.5 V		
Count number select	Input low voltage	V _{IL5}	—	—	1.0	V	Discri. count number	8	7
	Input middle voltage	V _{IM5}	2.0	—	3.1		2084		
	Input high voltage	V _{IH5}	3.9	—	—		2084 2605		
	Input low current	I _{IL5}	-150	-105	-75	μA	Pin 8 = 0.0 V		
	Input high current	I _{IH5}	95	135	190		Pin 8 = 5.5 V		
Charge pump	R1 set-up voltage	V _{ref2}	0.95	1.05	1.15	V	R _{1a} + R _{1b}	1	
	Charge current	I _{CH}	44	52	61	μA	= 5.0 kΩ	2	
	Discharge current	I _{DIS}	44	52	61		Pin 2 = 1.0 V		
	Leak current	I _{CER4}	—	—	±50	nA	Pin 2 = 1.0 V		
	Current ratio	I _{rat}	0.9	1.0	1.1	—	I _{rat} = I _{CH} /I _{DIS}		
Ready	Output high voltage	V _{OH1}	3.8	4.2	4.6	V	I _O = 1.0 mA	11	5
	Output low voltage	V _{OL1}	—	—	0.4		I _O = 1.0 mA		
LVI	Recovery voltage	V _{LVI}	—	—	8.0	V			
OSTSD	Operating temperature	T _{TSD}	125	150	—	°C			6
	Hysteresis temperature	T _{hys}	—	25	—				

- Notes: 1. Sum of upper and lower saturation voltage.
 2. Symbol "V_{CLREF}" denotes the applied voltage at pin 11, and "V_{OCL}" is measured from TAB (GND) to pin 15.
 3. See timing chart.
 4. This terminal is open collector output.
 5. See figure 4.
 6. Design guide only
 7. The maximum frequency, selecting the combination DSELZ = H and CNTSEL = H, must be less than 4 MHz.

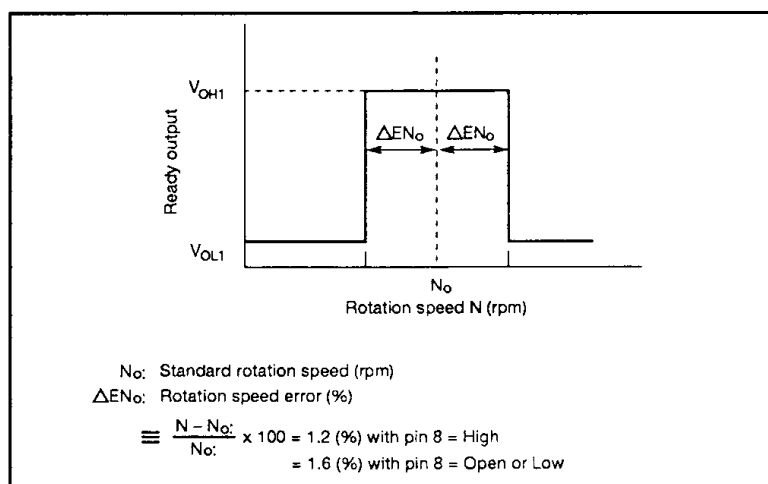


Figure 4 Operation of Ready Circuit

References

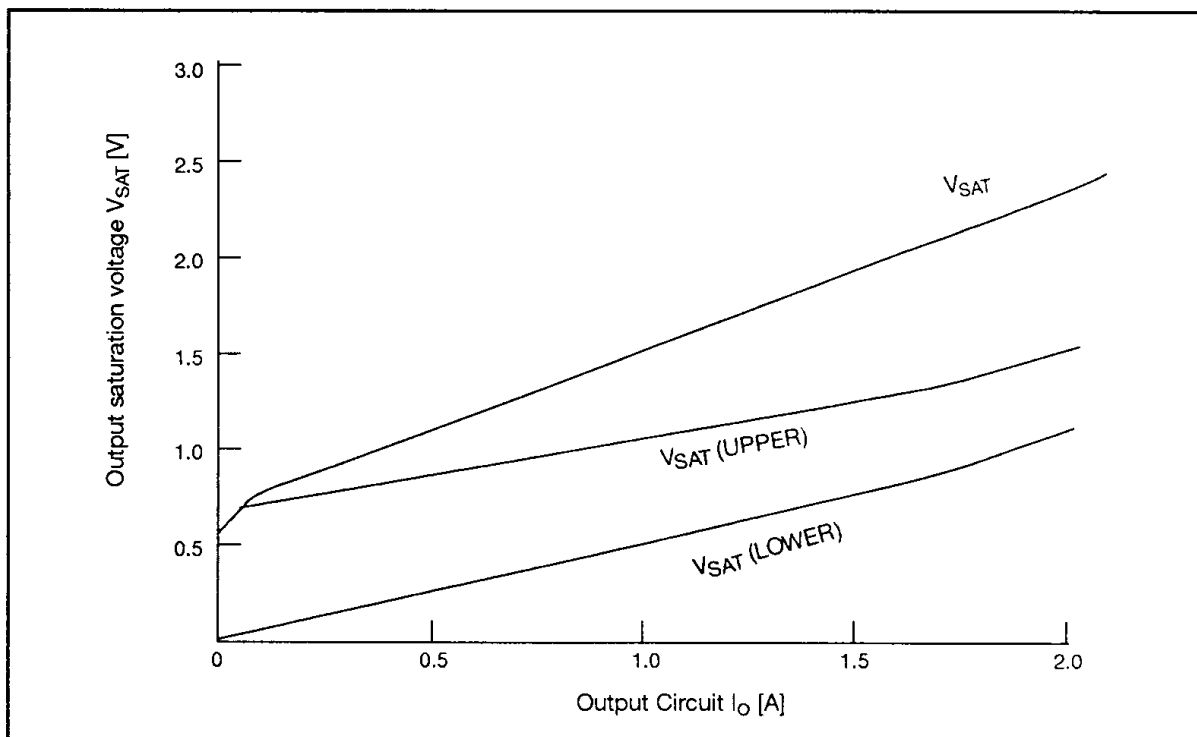


Figure 5 V_{sat} vs I_O

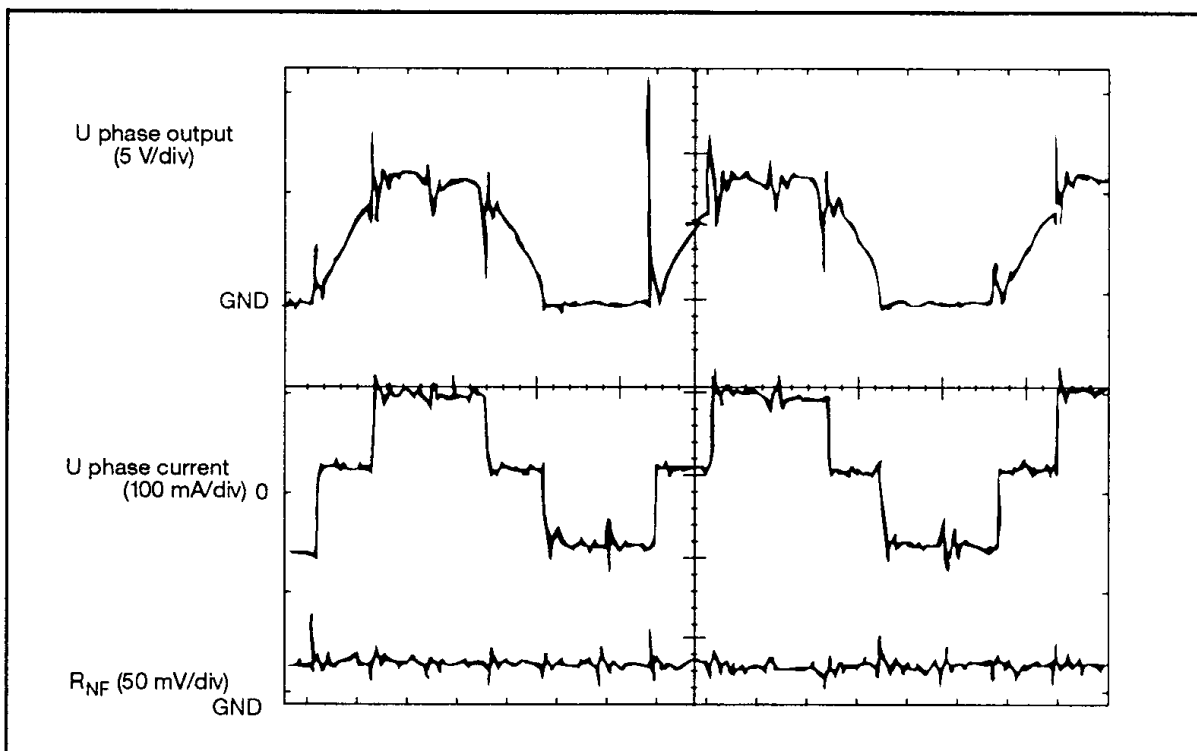


Figure 6 Waveform on Actual Application (3.5" 8 poles motor)