

P-Channel 1.8 V (G-S) MOSFET

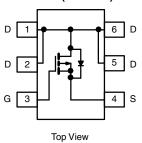
| PRODUCT SUMMARY | | | | |
|---------------------|------------------------------------|--------------------|--|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) | | |
| | 0.195 at V _{GS} = - 4.5 V | - 0.84 | | |
| - 20 | 0.260 at V _{GS} = - 2.5 V | - 0.73 | | |
| | 0.350 at V _{GS} = - 1.8 V | - 0.64 | | |

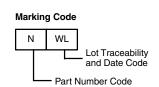
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Low Threshold
- Smallest LITTLE FOOT[®] Package: 1.6 mm x 1.6 mm
- · Low 0.6 mm Profile
- Compliant to RoHS Directive 2002/95/EC









Ordering Information: Si1037X-T1-GE3 (Lead (Pb)-free and Halogen-free)

APPLICATIONS

- Cell Phones and Pagers
 - Load Switch
- · Battery Operated Systems

| ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted) | | | | | |
|---|------------------------|-----------------------------------|-------------|--------------|------|
| Parameter | | Symbol | 5 s | Steady State | Unit |
| Drain-Source Voltage | | V _{DS} | - 20 | | V |
| Gate-Source Voltage | | V _{GS} | ± 8 | | |
| Continuous Drain Current /T 150 °C\8 | T _A = 25 °C | - I _D | - 0.84 | - 0.77 | A |
| Continuous Drain Current (T _J = 150 °C) ^a | T _A = 70 °C | | - 0.68 | - 0.62 | |
| Pulsed Drain Current | | I _{DM} | - 4 | | A |
| Continuous Diode Current (Diode Conduction) ^a | | I _S | - 0.18 | - 0.14 | |
| Maniana Banas Birahadian | T _A = 25 °C | P _D | 0.21 | 0.17 | - w |
| Maximum Power Dissipation ^a | T _A = 70 °C | | 0.13 | 0.10 | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | | °C |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|--------------|-------------------|---------|---------|------|
| Parameter | | Symbol | Typical | Maximum | Unit |
| Maniana da Ambianta | t ≤ 5 s | R _{thJA} | 500 | 600 | °C/W |
| Maximum Junction-to-Ambient ^a | Steady State | □thJA | 600 | 720 | |

Notes

a. Surface mounted on 1" x 1" FR4 board with minimum copper.

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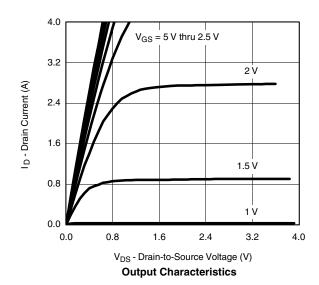
| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|---------------------|--|--------|-------------|-------|------|--|
| Static | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | - 0.45 | | | V | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$ | | | ± 100 | nA | |
| Zara Cata Maltana Duain Comunant | I _{DSS} | V _{DS} = - 20 V, V _{GS} = 0 V | | - 1 | | ^ | |
| Zero Gate Voltage Drain Current | | V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 70 °C | | | - 5 | μΑ | |
| On-State Drain Current ^a | I _{D(on)} | V _{DS} = - 5 V, V _{GS} = - 4.5 V | - 4 | | | Α | |
| | | V _{GS} = - 4.5 V, I _D = - 0.77 A | | 0.160 0.195 | | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V _{GS} = - 2.5 V, I _D = - 0.67 A | | 0.212 | 0.260 | Ω | |
| | | V _{GS} = - 1.8 V, I _D = - 0.2 A | | 0.290 | 0.350 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = - 10 V, I _D = - 0.77 A | | 3.1 | | S | |
| Diode Forward Voltage ^a | V_{SD} | I _S = - 0.14 A, V _{GS} = 0 V | | - 0.78 | - 1.2 | V | |
| Dynamic ^b | | | | | | | |
| Total Gate Charge | Q_g | | | 3.5 | 5.5 | | |
| Gate-Source Charge | Q_{gs} | $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -0.77 \text{ A}$ | | 0.65 | | nC | |
| Gate-Drain Charge | Q_{gd} | | | 0.60 | | | |
| Turn-On Delay Time | t _{d(on)} | | | 10 | 20 | | |
| Rise Time | t _r | V_{DD} = - 10 V, R_L = 20 Ω | | 15 | 30 | 1 | |
| Turn-Off Delay Time | t _{d(off)} | $I_{D} \cong -0.5 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_{G} = 6 \Omega$ | | 30 | 60 | ns | |
| Fall Time | t _f | | | 10 | 20 | | |
| Source-Drain Reverse Recovery Time | t _{rr} | I _F = - 0.14 A, dl/dt = 100 A/μs | | 20 | 40 | | |

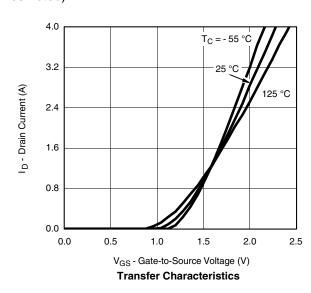
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

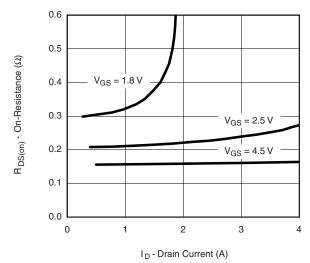




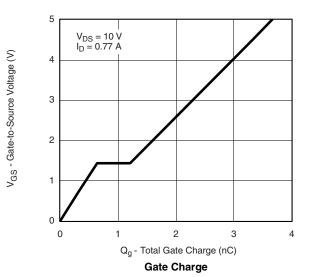


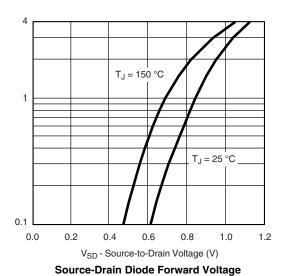


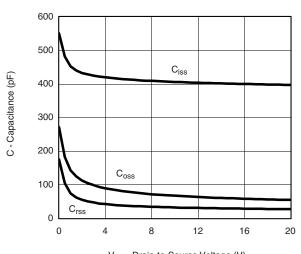
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



On-Resistance vs. Drain Current

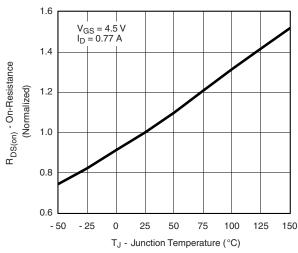




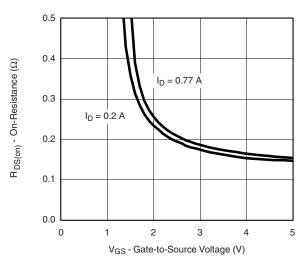


V_{DS} - Drain-to-Source Voltage (V)

Capacitance



On-Resistance vs. Junction Temperature



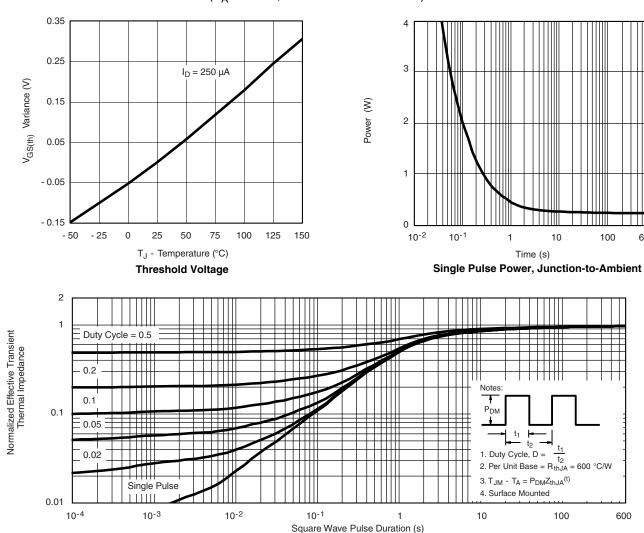
On-Resistance vs. Gate-to-Source Voltage

Is - Source Current (A)

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TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

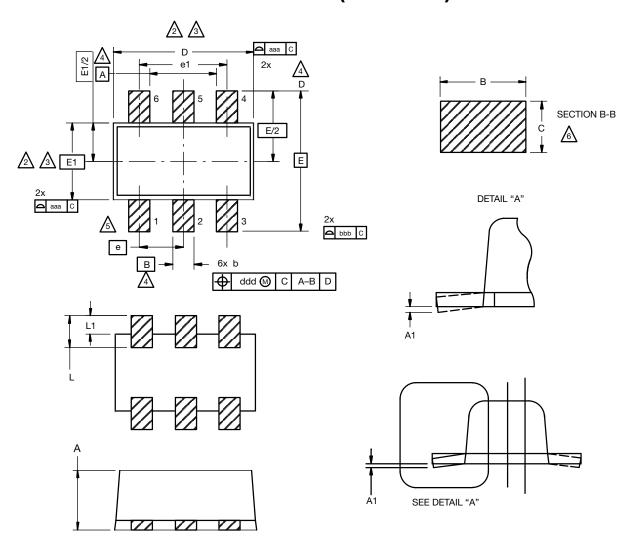


Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?70686.



SC-89 6-Leads (SOT-563F)



Notes

1. Dimensions in millimeters.

Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.

Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.

ADatums A, B and D to be determined 0.10 mm from the lead tip.

 Δ Terminal numbers are shown for reference only.

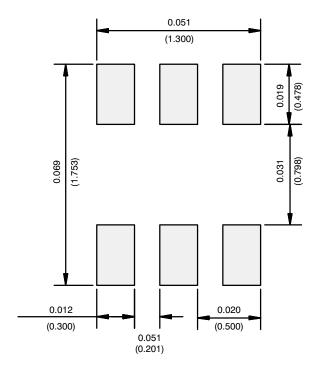
These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

| DIM. | MILLIMETERS | | | | | |
|---|-------------|------|------|--|--|--|
| | MIN. | NOM. | MAX. | | | |
| Α | 0.56 | 0.58 | 0.60 | | | |
| A1 | 0 | 0.02 | 0.10 | | | |
| b | 0.15 | 0.22 | 0.30 | | | |
| С | 0.10 | 0.14 | 0.18 | | | |
| D | 1.50 | 1.60 | 1.70 | | | |
| E | 1.50 | 1.60 | 1.70 | | | |
| E1 | 1.15 | 1.20 | 1.25 | | | |
| е | 0.45 | 0.50 | 0.55 | | | |
| e1 | 0.95 | 1.00 | 1.05 | | | |
| L | 0.25 | 0.35 | 0.50 | | | |
| L1 | 0.10 | 0.20 | 0.30 | | | |
| C14-0439-Rev. C, 11-Aug-14 DWG: 5880 | | | | | | |

Revision: 11-Aug-14 1 Document Number: 71612



RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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