

REV. Status

PRELIMINARY
02/23/00 MP

HDSL transformer designed for use with Level One HDSL IC chips SK70706/SK70721 at 784, 528 and 400Kbps rates.

A. Electrical Specifications (@ 25°C)

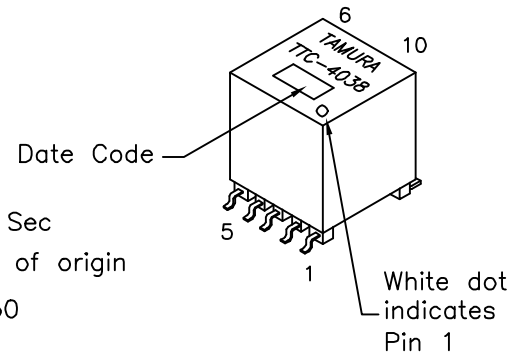
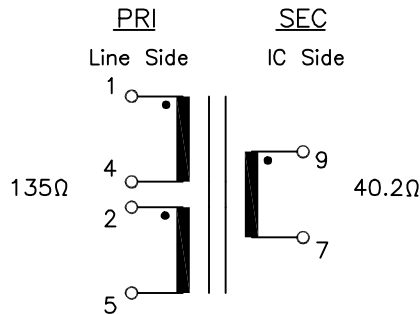
1. Primary (Line side) Impedance; 135Ω
2. Secondary (IC side) Impedance; 40.2Ω
3. Longitudinal Balance; 50dBm MIN @ 40KHz TO 300KHz, 0dBm
(Per FCC part 68.310, pin 7 grounded)
4. Insertion Loss; 0.75dB MAX @ 100KHz, 0dBm
5. Total Harmonic Distortion; -80dB TYP @ 5KHz, 5Vp-p
6. Primary Open Circuit Inductance; 2.92mH ±5% @ 10KHz, 1.0Vrms
Measured (1-5) with 2 & 4 shorted
7. DC Current; 160mA
8. DC Resistance; (1-4)=2.11Ω ±10%
(2-5)=2.11Ω ±10%
(7-9)=2.63Ω ±10%
9. Turns Ratio; (1-4):(2-5)=1:1.00±2%
(7-9):(1-5)=1:1.80±2%
2 & 4 shorted

10. Dielectric Strength; 1875Vrms 1 second @ Pri to Sec

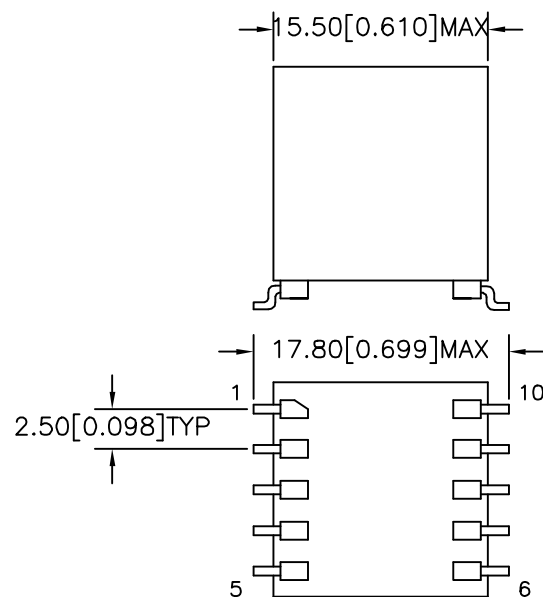
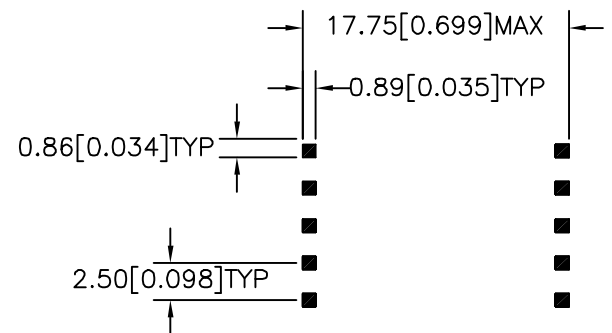
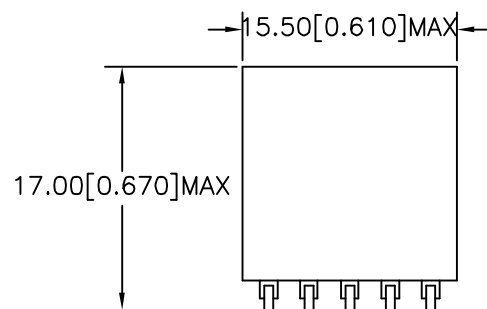
B. Marking; TTC-4038, TAMURA, date code and country of origin

C. Safety; Complies with UL1950 3rd Edition and IEC950

D. Schematic;



E. Mechanical Specifications;



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M. Pitchai

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO.

P-A1-12364

ACAD\TTC\A1123641.DWG

REV

PRE

HDSL

TRANSFORMER

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(909) 699-1270 FAX 9096769482**TTC-4038**

MODEL SPECIFICATION

DIM: mm(in) SCL: 2/1 SH: 1 OF 1

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