



*SANYO Semiconductors*

**DATA SHEET**

An ON Semiconductor Company

**LV4991TH** — **Bi-CMOS IC**  
**For Portable Audio Equipment**  
**Monaural BTL Power Amplifier**

## Overview

LV4991TH incorporates the power amplifier circuit operable at low voltage (2.7V or more) and has additionally the standby function to reduce the current drain. This is the best LSI for speaker drive for the battery-driven portable equipment, such as portable DVD player, Notebook PC, etc. The LV4991TH is a HMSOP8 (150mil) package, and The LV4991TT of MSOP8 (150mil) package is available.

## Function and Feature

- Monaural BTL power amplifier incorporated
  - Standard output power 1 = 450mW ( $V_{CC} = 3.6V$ ,  $R_L = 8\Omega$ , THD = 10%)
  - Standard output power 2 = 1000mW ( $V_{CC} = 5V$ ,  $R_L = 8\Omega$ , THD = 10%)
  - Output coupling capacitor not necessary because of differential output type
- Operation at low voltage possible
  - $V_{CC} = 2.7V$  or more
- Standby function incorporated
  - Standard current drain at standby =  $0.1\mu A$  ( $V_{CC} = 3.6V$ )
- Second amplifier stop control function incorporated : Reducing the pop sound at startup, simple MUTE
- Overheat protection circuit incorporated
- Gain setting possible
  - BTL voltage gain = 0 to 26dB
- Output phase compensation capacitor not necessary

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**Specifications****Maximum Ratings** at  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	$V_{CC\ max}$		6	V
Allowable power dissipation	$P_d\ max$	Substrate mounted*	1.1	V
Operating temperature	$T_{opr}$		-40 to +85	$^\circ\text{C}$
Storage temperature	$T_{stg}$		-40 to +150	$^\circ\text{C}$

\* Substrate mounted : with 60mm  $\times$  60mm  $\times$  1.6mm, glass epoxy substrate

**Operating Conditions** at  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	$V_{CC}$		3.6	V
Recommended load resistance	$R_L$		8 to 32	$\Omega$
Allowable operating supply voltage range	$V_{CC\ op}$		2.7 to 5.5	V

\* Determine the supply voltage to be used with due consideration of allowable power dissipation.

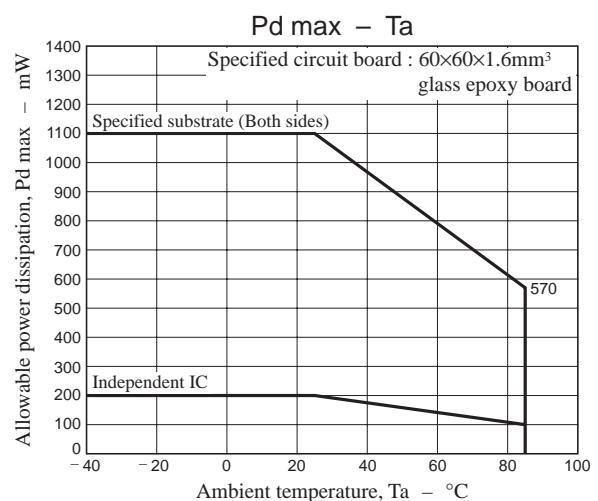
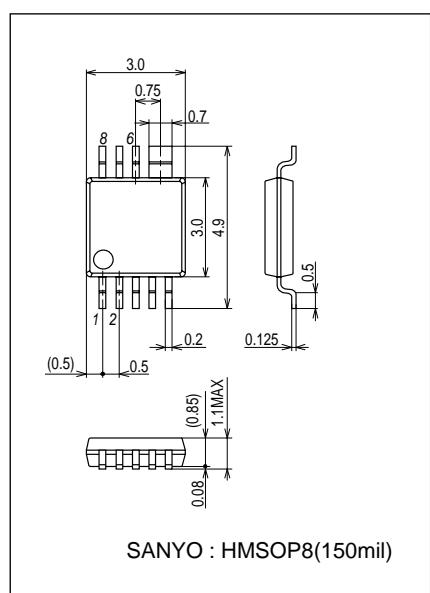
**Electrical Characteristics**  $T_a = 25^\circ\text{C}$ ,  $V_{CC} = 3.6\text{V}$ ,  $f_{in} = 1\text{kHz}$ ,  $R_L = 8\Omega$ 

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Quiescent current drain	$I_{CCOP}$	No signal, $R_L = \infty$		3.6	6	mA
Stand-by current drain	$I_{STBY}$	No signal, $R_L = \infty$ , $V3 = \text{LOW}$		0.1	10	$\mu\text{A}$
Maximum output power 1	$P_{OMX1}$	THD = 10%	300	450		mW
Maximum output power 2	$P_{OMX2}$	THD = 10%, $V_{CC} = 5\text{V}$	665	1000		mW
Voltage gain	$VG$	$V_{IN} = -30\text{dBV}$	4.5	6	7.5	dB
Voltage gain use range	$VGR$		0		26	dB
Total harmonic distortion ratio	THD	$V_{IN} = -30\text{dBV}$		0.3	1	%
Output noise voltage	$V_{NOUT}$	$R_g = 620\Omega$ , 20 to 20kHz		120	280	$\mu\text{Vrms}$
Ripple removal ratio	SVRR	$R_g = 620\Omega$ , $f_r = 100\text{Hz}$ , $V_r = -20\text{dBV}$		48		dB
Output offset voltage	$V_{OS}$	$R_g = 620\Omega$	-50		50	mV
Reference (pin 1) voltage	$V_{REF}$			1.81		V
Pin 3 control HIGH voltage	$V_{STBH}$	Power amplifier operation mode	1.9		$V_{CC}$	V
Pin 3 control LOW voltage	$V_{STBL}$	Power amplifier standby mode	0		0.3	V
Pin 2 control HIGH voltage	$V_{CNTH}$	Second power amplifier operation mode	1.6		$V_{CC}$	V
Pin 2 control LOW voltage	$V_{CNTL}$	Second power amplifier standby mode	0		0.3	V

## Package Dimensions

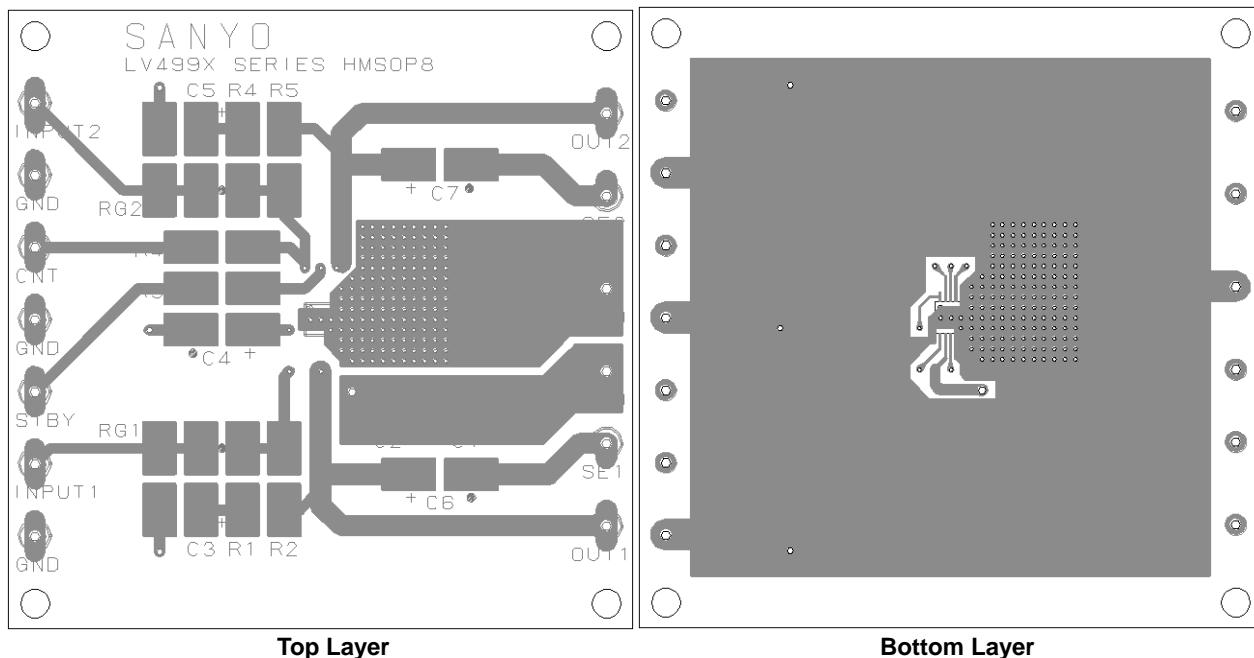
unit : mm (typ)

3339

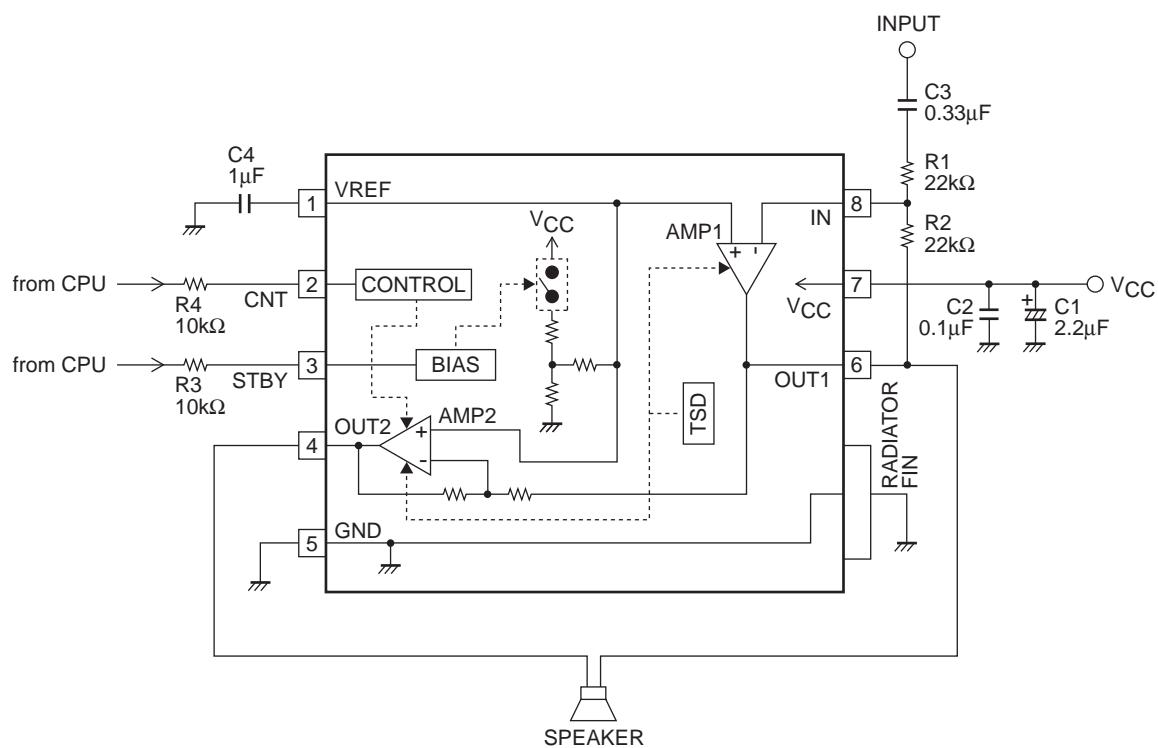


### Recommended substrate

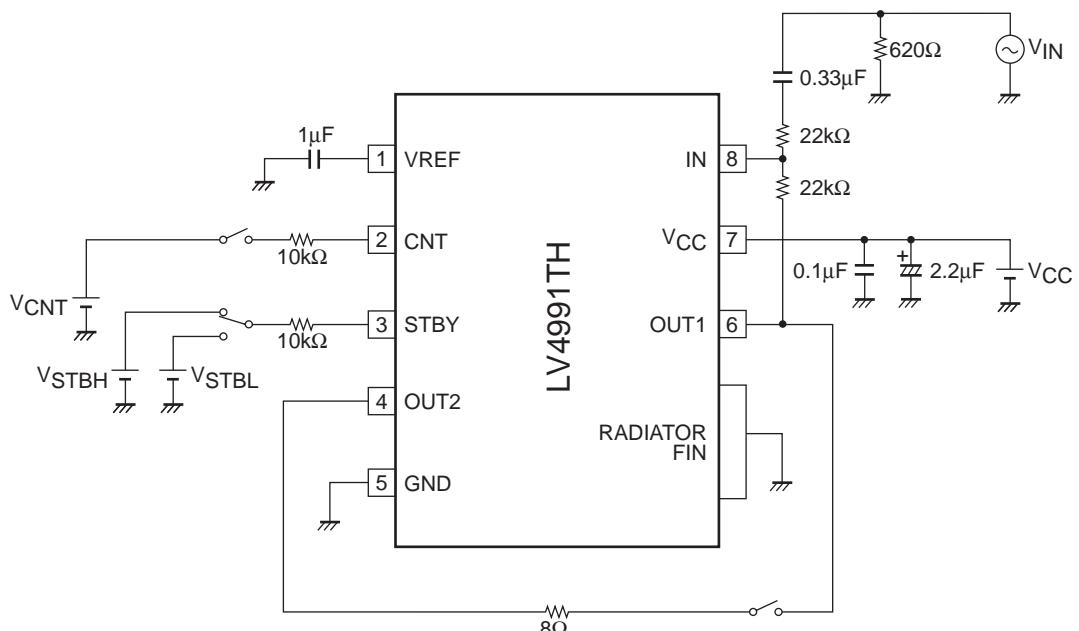
Size : 60mm×60mm×1.6mm



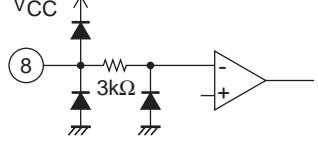
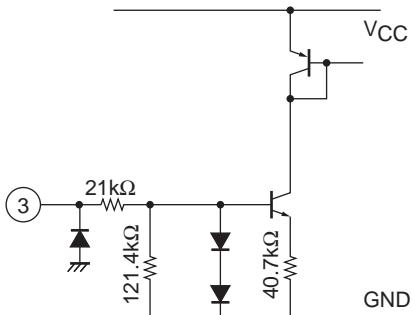
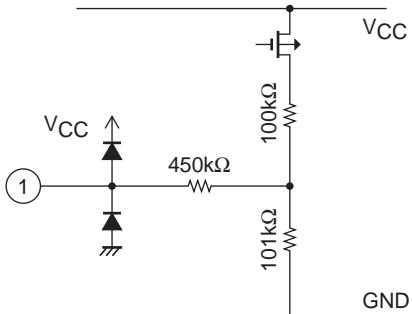
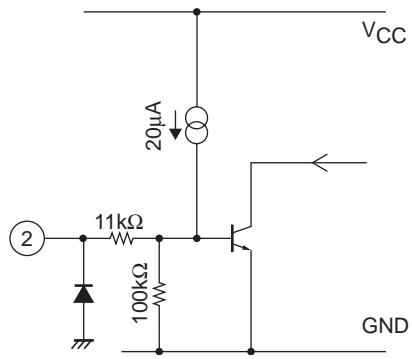
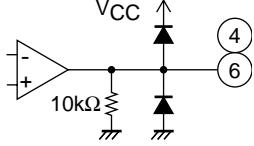
## Block Diagram and Sample Application Circuit



## Test Circuit



## Pin Description

Pin No.	Symbol	Pin voltage $V_{CC} = 3.6V$	Description	Equivalent circuit
8	IN	1.81	Input pin	
3	STBY	Voltage impression	Standby pin •Standby mode at 0 to 0.3V •Operation mode at 1.9 to VCC	
1	VREF	1.81	Ripple filter pin (For connection of capacitor for filter)	
2	CNT	Voltage impression	Second amplifier stop control pin •Second amplifier stopped at 0 to 0.3V	
4 6	OUT2 OUT1	1.81	Power amplifier output pin	
5	GND	0	Ground pin	
7	$V_{CC}$	Voltage impression (3.6)	Power pin	

## Cautions for use

### 1. Input coupling capacitor (C3)

The input coupling capacitor C3 and input resistor R1 make up the high-pass filter, attenuating the bass frequency. Therefore, the capacitance value must be selected with due consideration of the pass band. Note with care that this capacitance value affects the pop sound at startup.

Namely, the increased capacitance value will make the pop sound louder.

### 2. Pin 1 capacitor (C4)

This capacitor C4 is designed to reduce the power ripple. The ripple removal ratio increases when the capacitance is larger. Note however that this capacitor affects the pop sound at startup.

Design must therefore be made by taking into both features as above described.

### 3. Pin 2 control (second amplifier stop control function)

Pin 2 is a pin to turn ON/OFF the operation of second amplifier. By using this function, the pop sound at startup can be reduced. Note that pin 2 can be controlled by applying the voltage described below :

Second amplifier ON  $\Rightarrow$  V2 = 1.6 to VCCV or OPEN

Second amplifier OFF  $\Rightarrow$  V2 = 0 to 0.3V

When the pin-1 capacitor C4 is downsized, the pop sound becomes louder. The pop sound can be reduced by providing the time Tmu to stop the second amplifier (see Fig. 1) while utilizing this function of the microcomputer. The recommended mute time Tmu is as follows.

C4 [ $\mu$ F]	0.1	0.22	0.33
Tmu [ms]	$\geq 170$	$\geq 270$	$\geq 280$

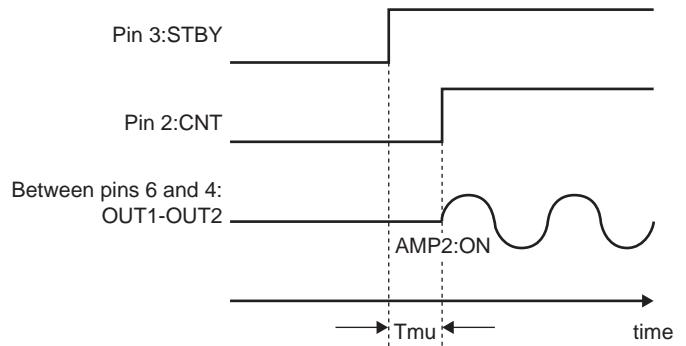


Fig. 1

### 4. Standby pin (pin 3)

By controlling the standby pin, the mode changeover can be made between standby and operation modes.

Standby mode  $\Rightarrow$  V3 = 0 to 0.3V

Operation mode  $\Rightarrow$  V3 = 1.9 to VCCV

When using the standby pin as interlocked with power supply as shown in Fig. 2, care should be taken because the current  $I_{STBY}$  as expressed by the following equation flows through the standby pin.

$$I_{STBY} = \frac{V_{CC} - 1.4V}{R3 + 21k\Omega}$$

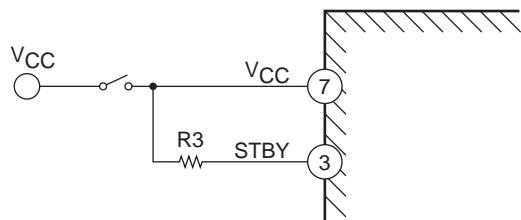


Fig. 2

5. Bypass capacitor (C2) of the power supply block

The bypass capacitor attached to the power pin (pin 7) must be arranged as near to this pin as possible.

6. Short-circuit between pins

When power is applied with pins left short-circuited, deterioration or damage may result.

Therefore, check before power application if pins are short-circuited with solder, etc. during mounting of IC to the substrate.

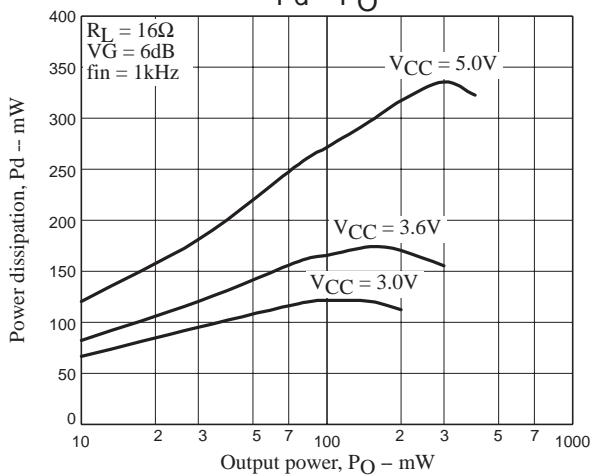
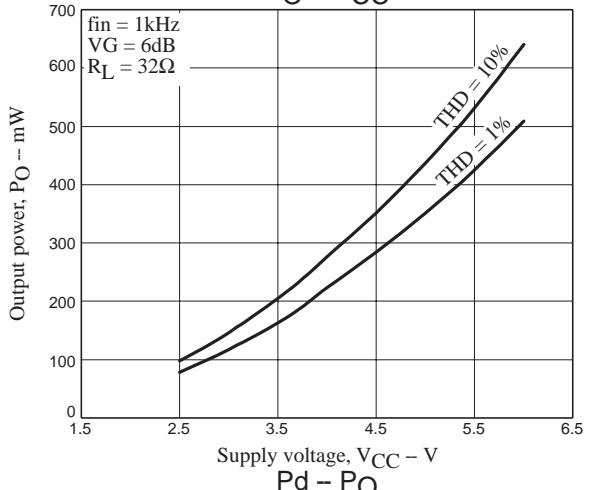
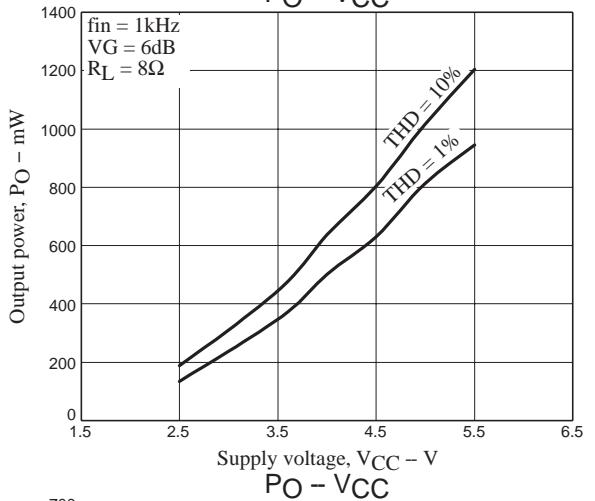
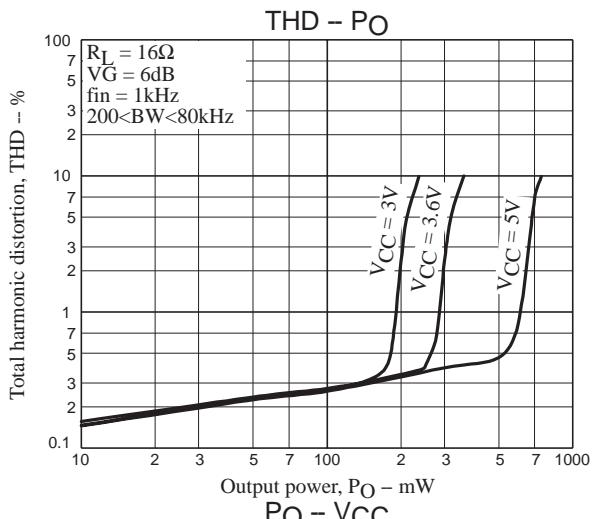
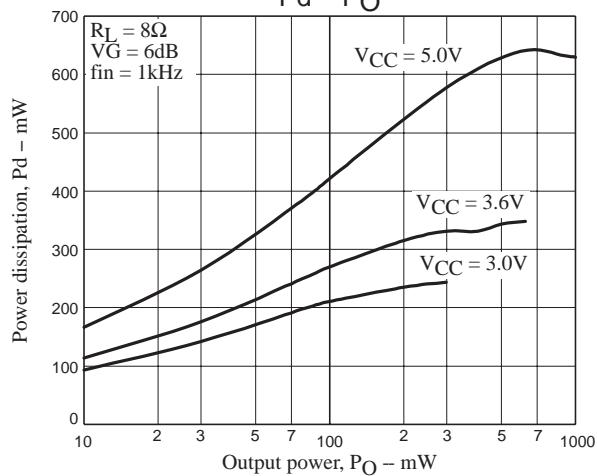
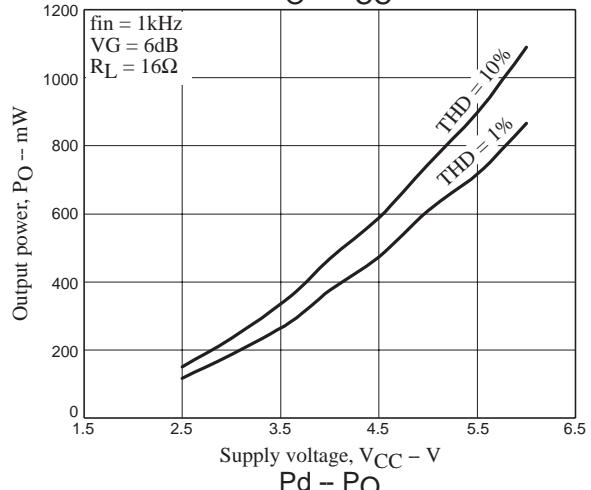
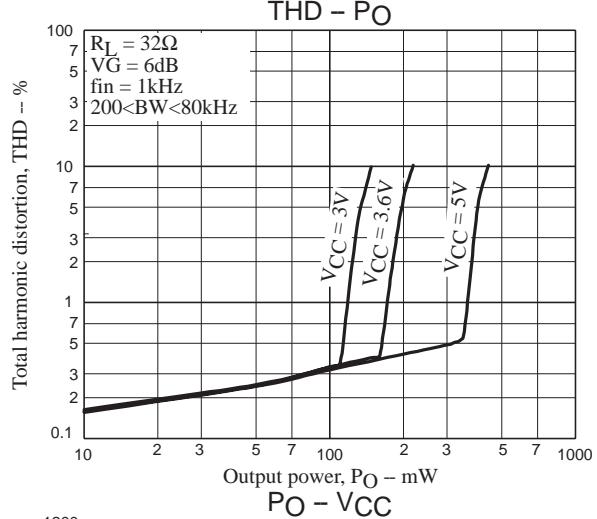
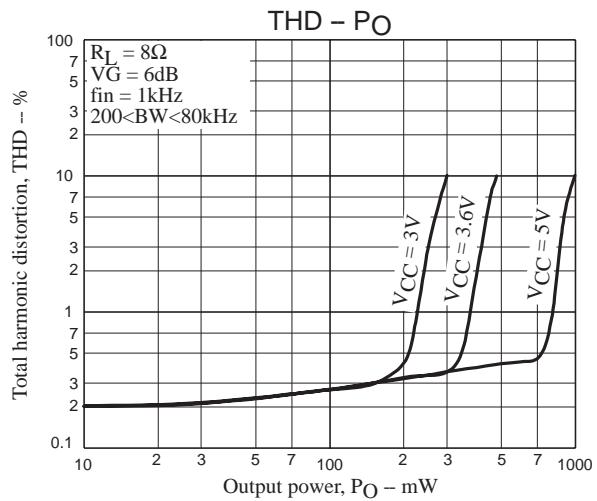
7. Short-circuit of load

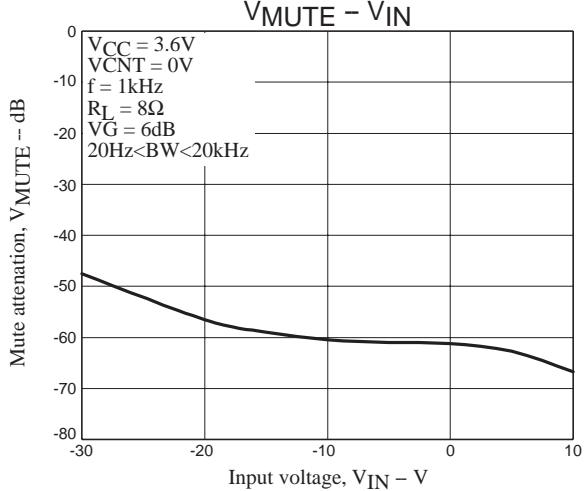
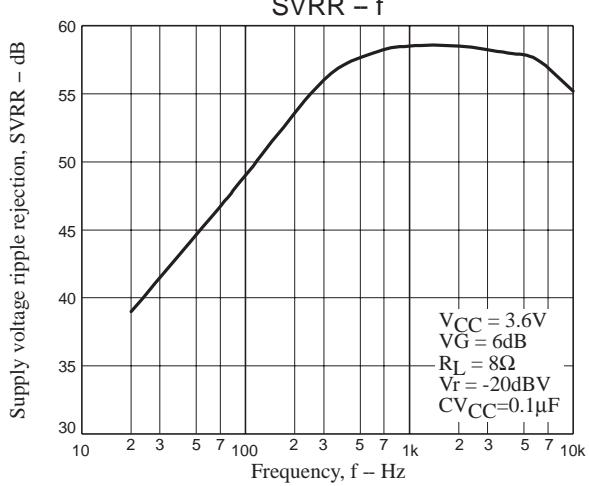
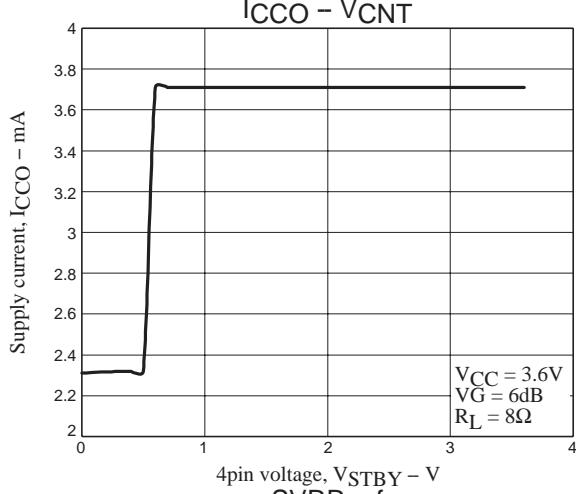
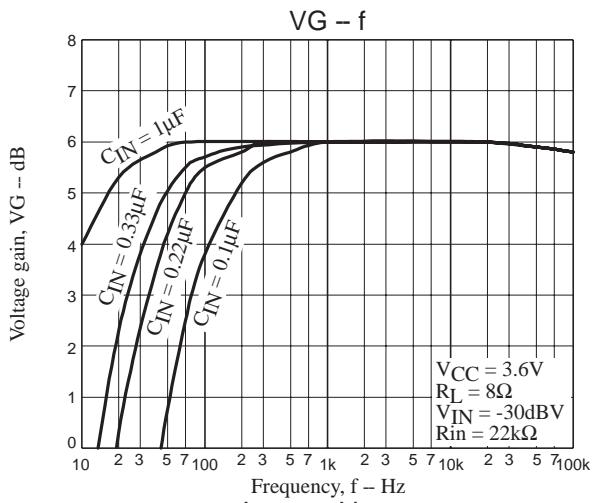
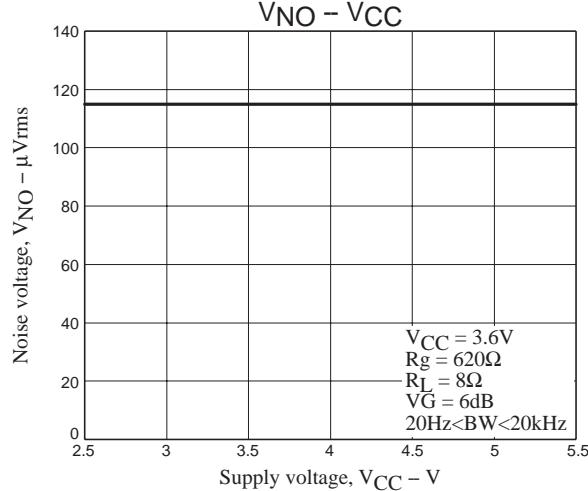
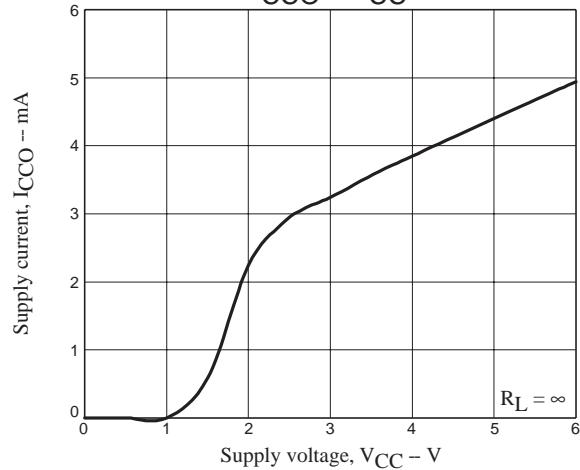
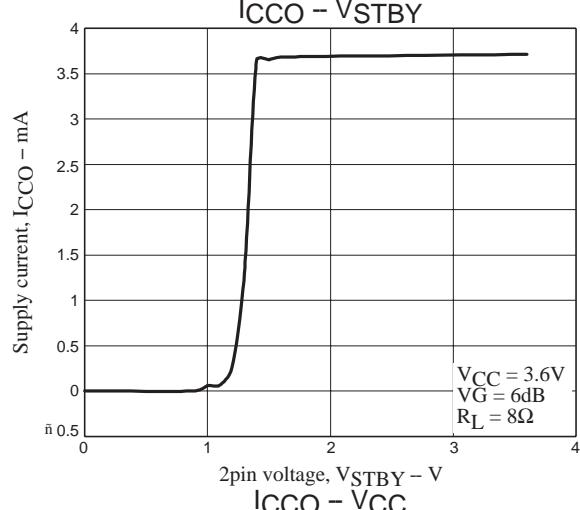
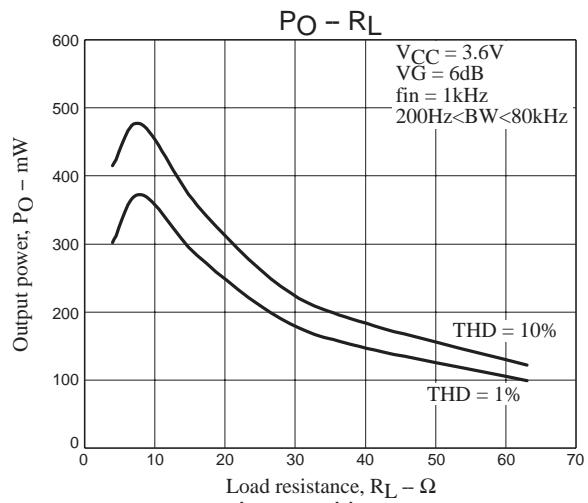
If the load is left short-circuited for a long period of time, deterioration or damage may occur.

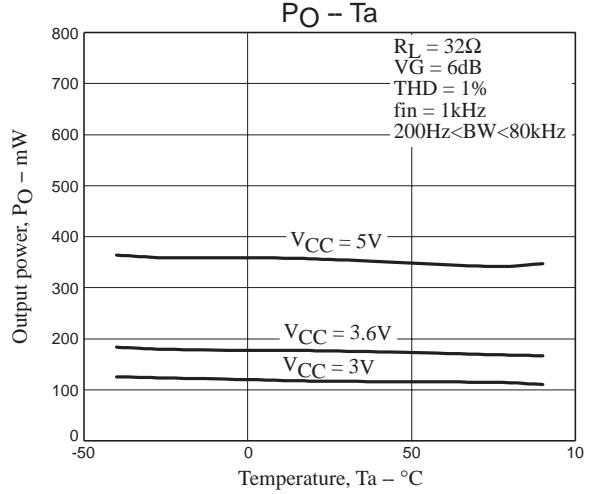
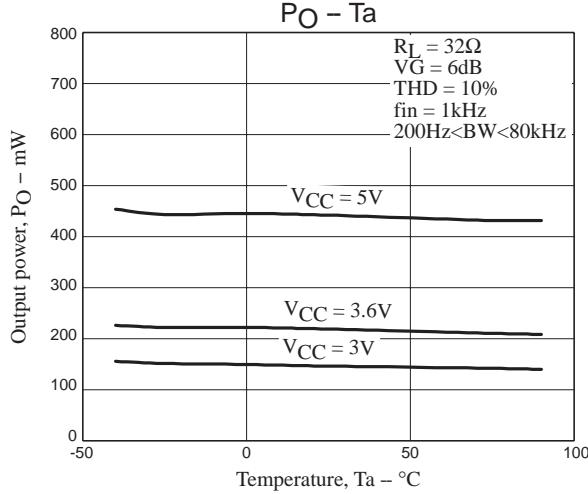
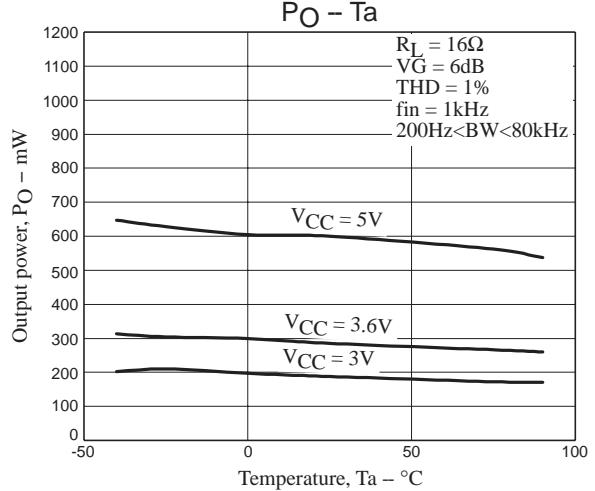
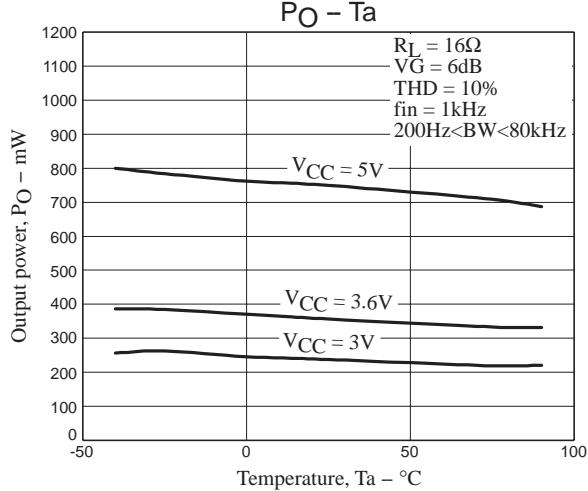
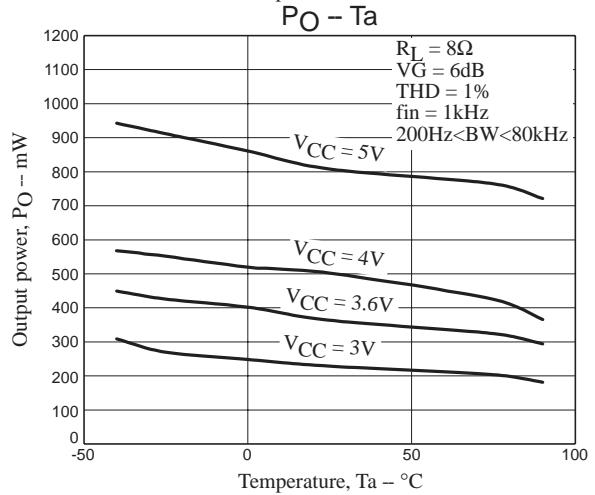
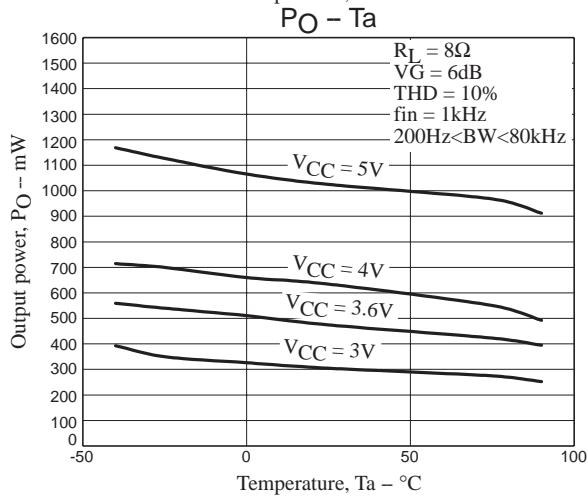
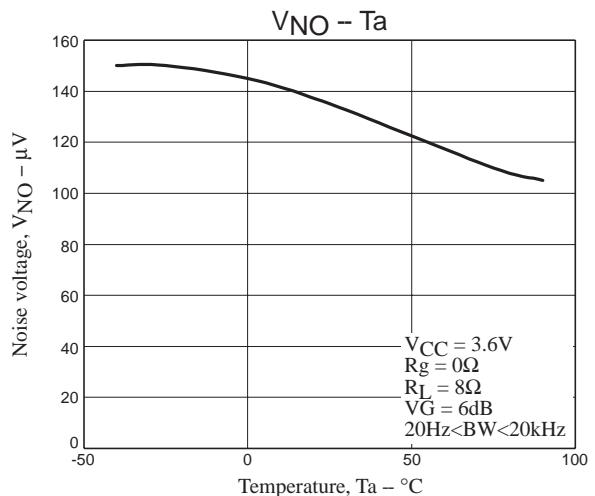
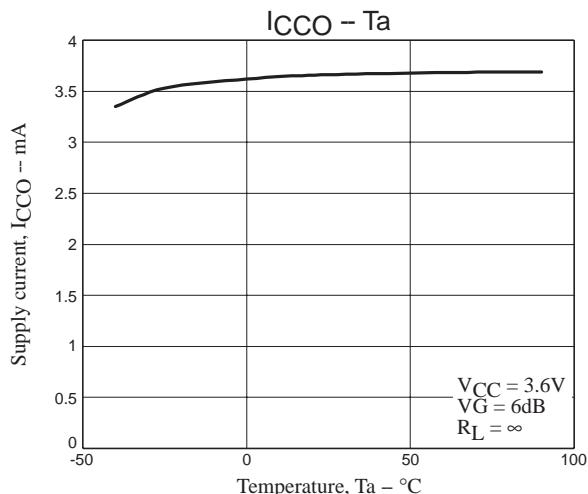
Never allow the load to short-circuit.

8. Maximum rating

When IC is used near the maximum rating, there is a possibility that the maximum rating may be exceeded even under the smallest change of conditions, resulting in failure. Take the sufficient margin for variation of supply voltage and use IC within a range where the maximum rating will never be exceeded.

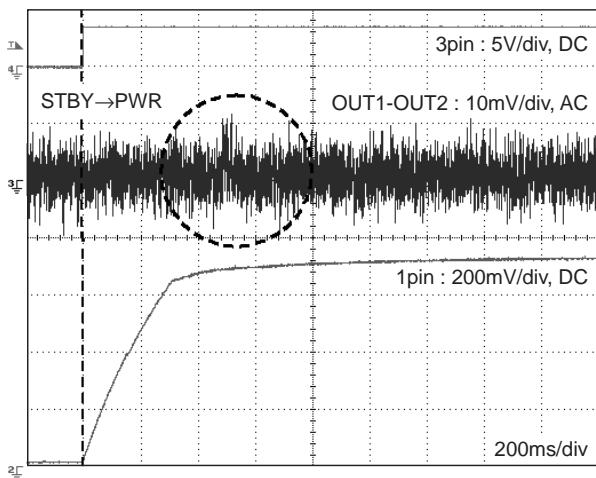




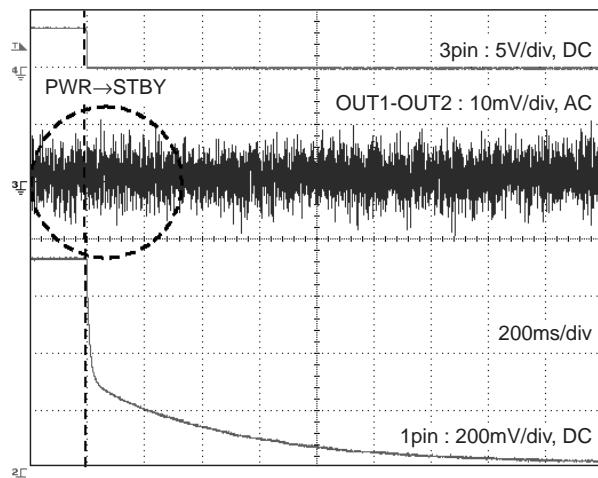


## Pop sound

## 1. Startup



## 2. Fall



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