

SLE 76CX482P

48 Kbyte EEPROM

196 Kbytes ROM, 7100 bytes RAM,

1100-Bit Advanced Crypto Engine

certified RSA 2048-bit library available

Dual Key Triple DES

8/16-Bit Security Controller with enhanced instruction set
for large memories in 0.22 μm CMOS Technology

Short Product Overview

May 2010

SLE 66CX482PE Short Product Overview		Ref.: Chip_Card_Product_Overview_11/09
Revision History: Current Version 05.10		
Previous Releases:		
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Important: Further information is confidential and on request. Please contact:
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Product name	SLE 66CX482PE Secure μSlim EEPROM
Product description	Security cryptocontroller
User-ROM	196 kByte
EEPROM	48 kByte
RAM	6,400 Byte + 700 Byte crypto
CPU	8-bit/16-bit
Crypto coprocessors	
Symmetrical Cryptography	3DES
Asymmetrical Cryptography	RSA up to 2048 bit, ECC up to 521 bit
Clock (int.)	1 - 33 MHz
Clock (ext.)	1 - 7.5 MHz
Operating voltage	1.62 V - 5.5 V
Max. supply current (at 5 MHz, 5 V)	10 mA
Max. sleep mode current (typical)	100 μ A
Ambient temperature	-25 to +85°
Write / erase time	2 ms (typ.)
EEPROM page programming	1 to 64 Byte
Security features	EEPROM Error Detection, Memory-, Bus- and SFR-Encryption, Active Shield, MMU, DPA/SPA, DEMA/SEMA Countermeasures, Security Sensors: V, f, Light, Temperature, Glitch, Tamperproof Design, Chip ID, True RNG (AIS31, FIPS-140)
Peripherals	CRC, PLL, UART DF 16
Delivery forms	Module M5.1, MFC5.x, DSO-8, VQFN-8, die
Typical applications	Payment, EMV DDA, ePurse, Loyalty, Access Control, Health / Social Security, Digital Signature, ID-Card, Pay-TV
Certifications	CC EAL5+ high, EMVCo

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