

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
- 30	0.085 at $V_{GS} = - 4.5$ V	- 4.3
	0.135 at $V_{GS} = - 2.5$ V	- 3.4

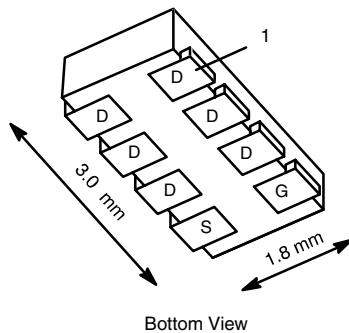
FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFETs: 2.5 V Rated

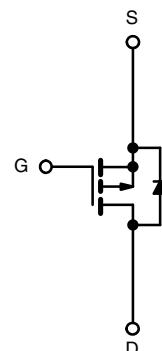


RoHS
COMPLIANT
HALOGEN
FREE
Available

1206-8 ChipFET®



Marking Code
BH XX
— Lot Traceability and Date Code
— Part # Code



Ordering Information: Si5449DC-T1-E3 (Lead (Pb)-free)
Si5449DC-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V_{DS}	- 30		V	
Gate-Source Voltage		V_{GS}	± 12			
Continuous Drain Current ($T_J = 150$ °C) ^a		I_D	- 4.3	- 3.1	A	
			- 3.1	- 2.2		
Pulsed Drain Current		I_{DM}	- 15			
Continuous Source Current ^a		I_{AS}	- 2.1	- 1.1		
Maximum Power Dissipation ^a		P_D	2.5	1.3	W	
			1.3	0.7		
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150			
Soldering Recommendations (Peak Temperature) ^{b, c}			260		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 5$ s	R_{thJA}	40	50	°C/W
	Steady State		80	95	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	15	20	

Notes:

- Surface Mounted on 1" x 1" FR4 board.
- See Reliability Manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

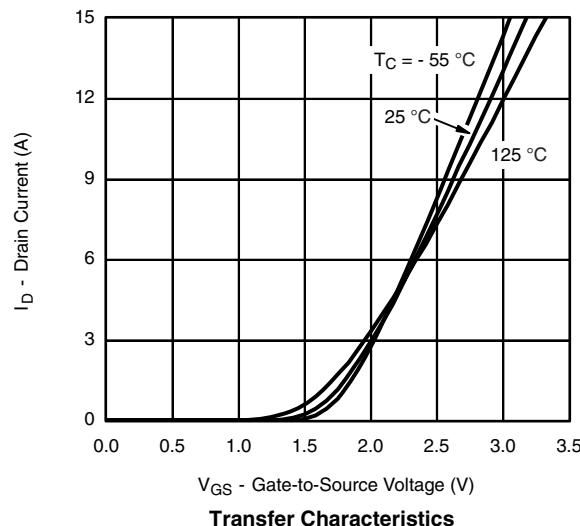
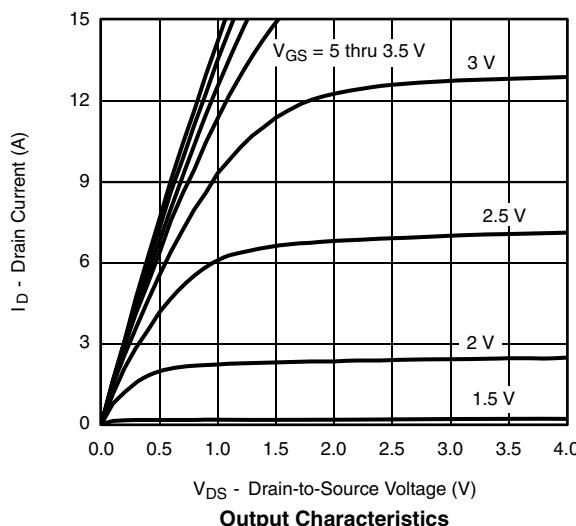
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = -250 \mu\text{A}$	- 0.6			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 12 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24 \text{ V}$, $V_{GS} = 0 \text{ V}$		- 1		μA
		$V_{DS} = -24 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_J = 85^\circ\text{C}$		- 5		
		$V_{DS} \leq -5 \text{ V}$, $V_{GS} = -4.5 \text{ V}$	- 15			
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{GS} = -4.5 \text{ V}$, $I_D = -3.1 \text{ A}$		0.071	0.085	Ω
		$V_{GS} = -2.5 \text{ V}$, $I_D = -1 \text{ A}$		0.112	0.135	
		$V_{DS} = -10 \text{ V}$, $I_D = -3.1 \text{ A}$		8.5		
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.1 \text{ A}$, $V_{GS} = 0 \text{ V}$		- 0.8	- 1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -15 \text{ V}$, $V_{GS} = -4.5 \text{ V}$, $I_D = -3.1 \text{ A}$		7.2	11	nC
Gate-Source Charge	Q_{gs}			2.2		
Gate-Drain Charge	Q_{gd}			1.7		
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -15 \text{ V}$, $R_L = 15 \Omega$ $I_D \equiv -1 \text{ A}$, $V_{GEN} = -4.5 \text{ V}$, $R_G = 6 \Omega$		13	25	μs
Rise Time	t_r			14	30	
Turn-Off Delay Time	$t_{d(\text{off})}$			35	70	
Fall Time	t_f			20	40	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = -1.1 \text{ A}$, $dI/dt = 100 \text{ A}/\mu\text{s}$		40	60	ns

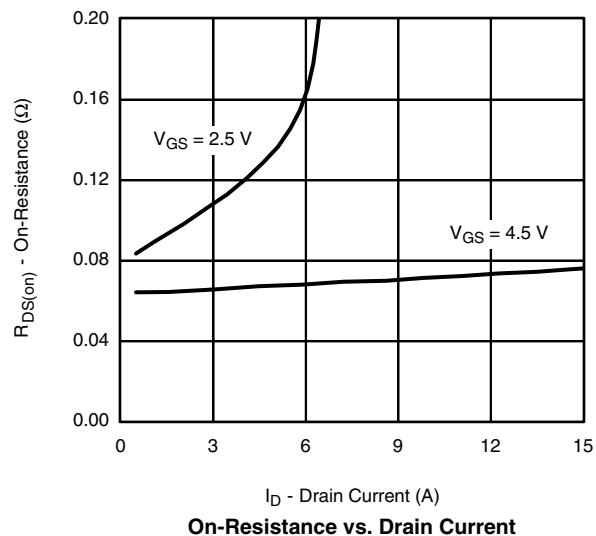
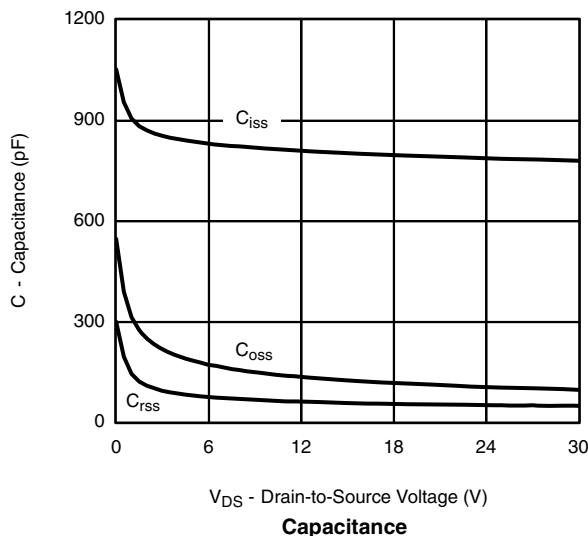
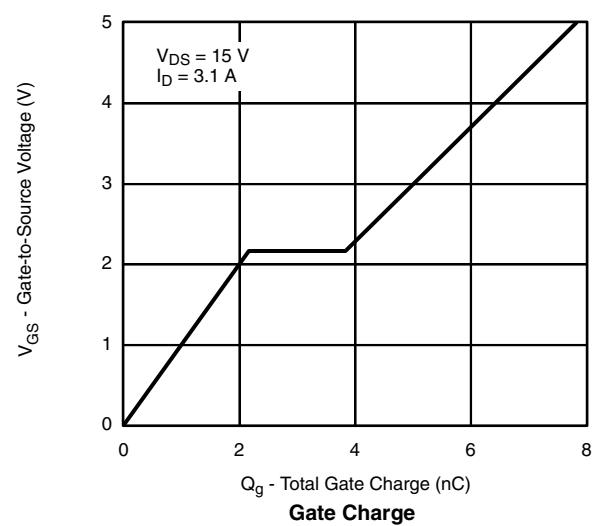
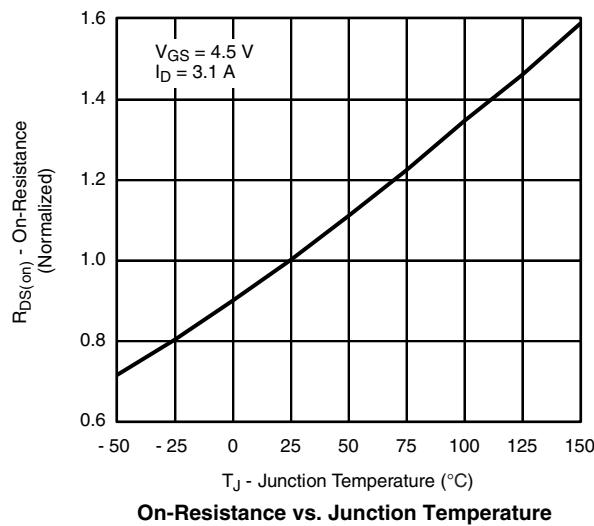
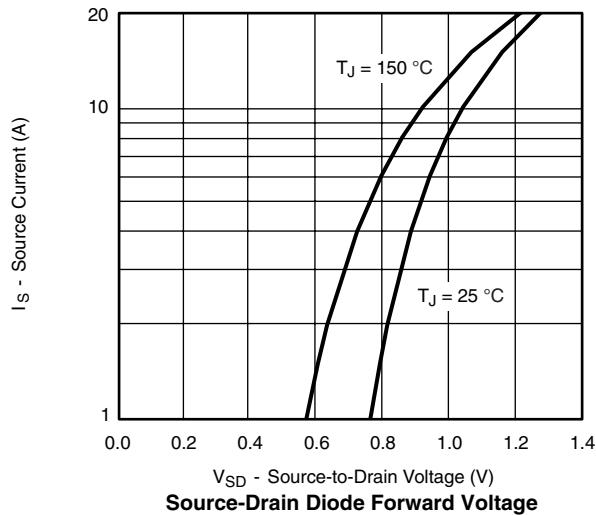
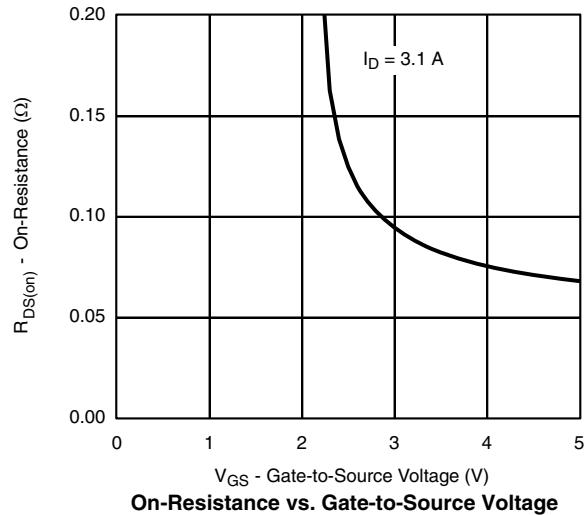
Notes:

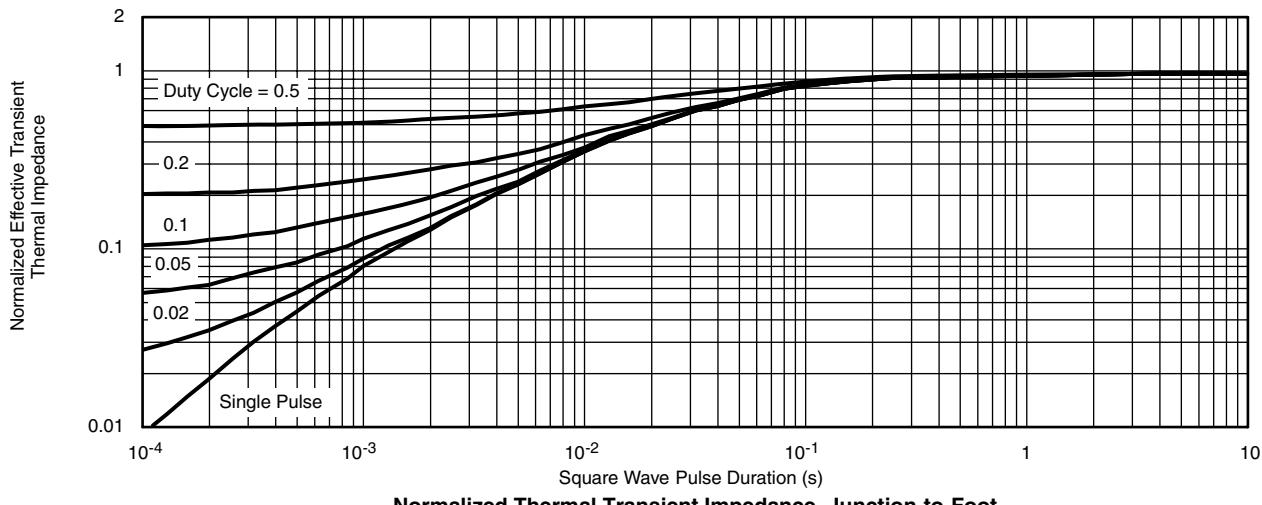
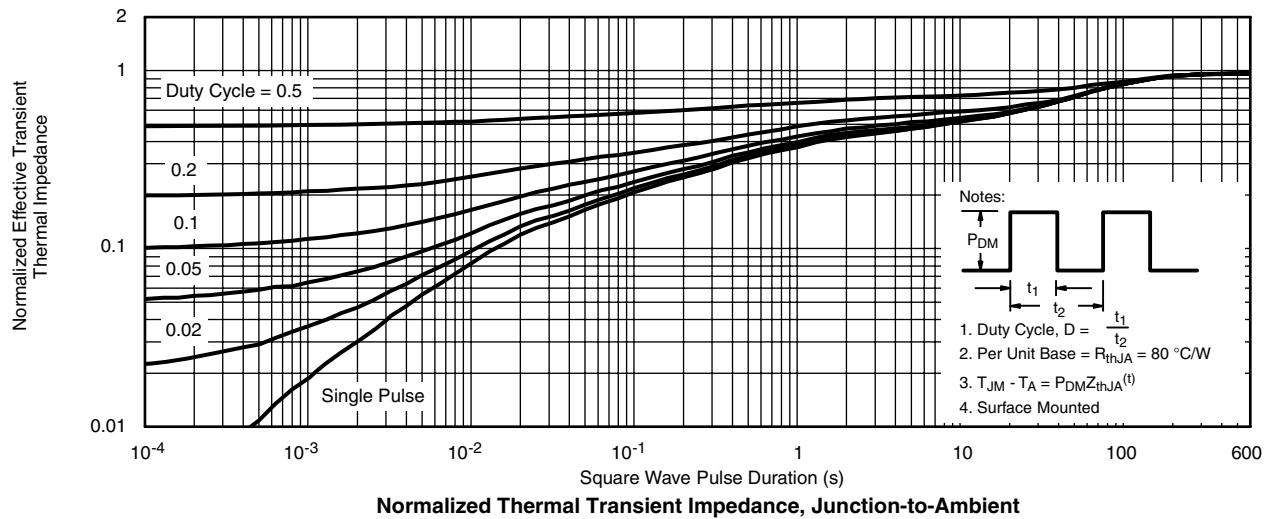
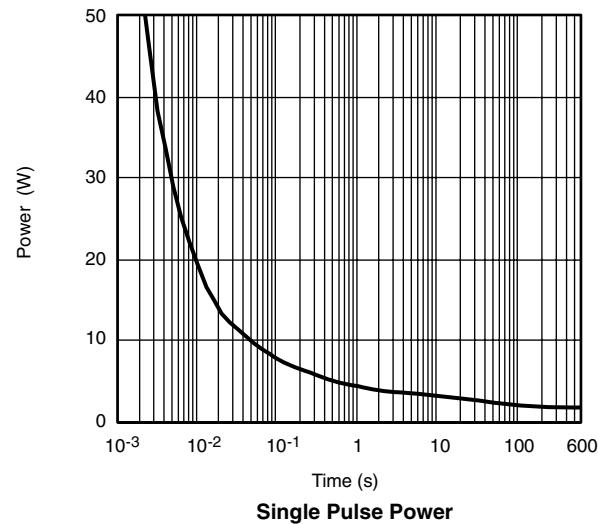
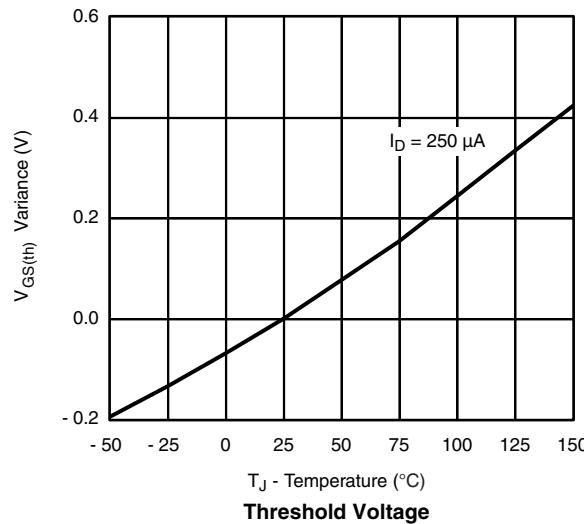
a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2 \%$.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25°C , unless otherwise noted

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On-Resistance vs. Drain Current

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


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