



BT236X series F and G

6 A Four-quadrant triacs

Rev. 3 — 3 November 2011

Product data sheet

1. Product profile

1.1 General description

Passivated triacs in a full pack, plastic package intended for use in applications requiring high bidirectional transient and blocking voltage capability and thermal cycling performance.

1.2 Features and benefits

- Isolated package
- High I_{TSM}

1.3 Applications

- Lamp dimmers
- Motor speed controllers
- High inrush resistive loads
- Heating and static switching

1.4 Quick reference data

- $V_{DRM} \leq 600$ V (BT236X-600_600F_600G)
- $V_{DRM} \leq 800$ V (BT236X-800_800G)
- $I_{TSM} \leq 65$ A ($t = 20$ ms)
- $I_{T(RMS)} \leq 6$ A
- $I_{GT} \leq 35$ mA (BT236X-600_800)
- $I_{GT} \leq 25$ mA (BT236X-600F)
- $I_{GT} \leq 50$ mA (BT236X-600G_800G)

2. Pinning information

Table 1. Pinning

Pin	Description	Simplified outline	Symbol
1	main terminal 1 (T1)		
2	main terminal 2 (T2)		
3	gate (G)		
mb	mounting base; isolated		

SOT186A (3-lead TO-220F)



3. Ordering information

Table 2. Ordering information

Type number	Package			Version
	Name	Description		
BT236X-600	3-lead	plastic single-ended package; isolated heatsink mounted; 1 mounting hole;		SOT186A
BT236X-600F	TO-220F	3 lead TO-220 'full pack'		
BT236X-600G				
BT236X-800				
BT236X-800G				

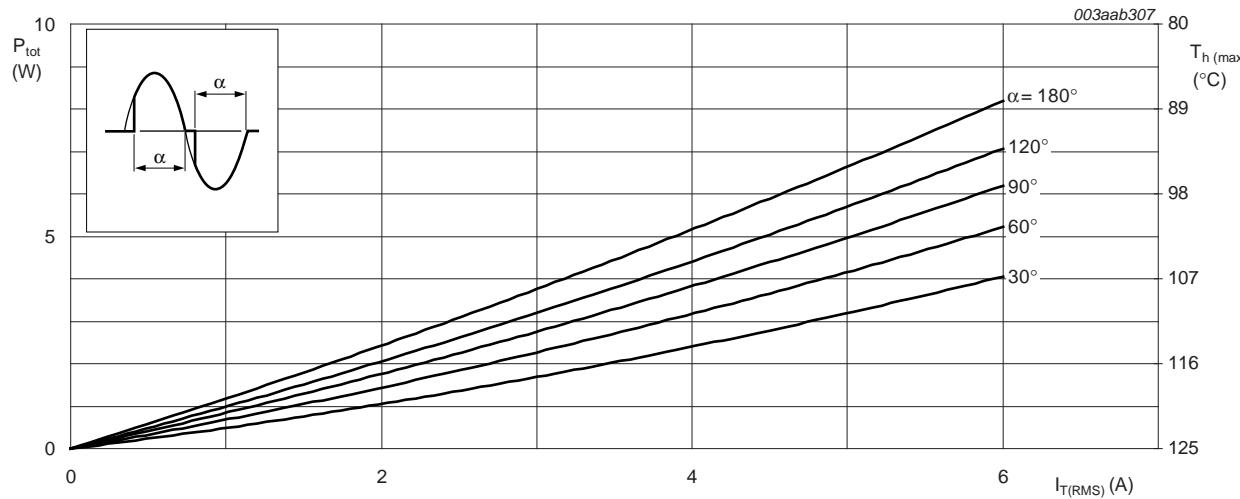
4. Limiting values

Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

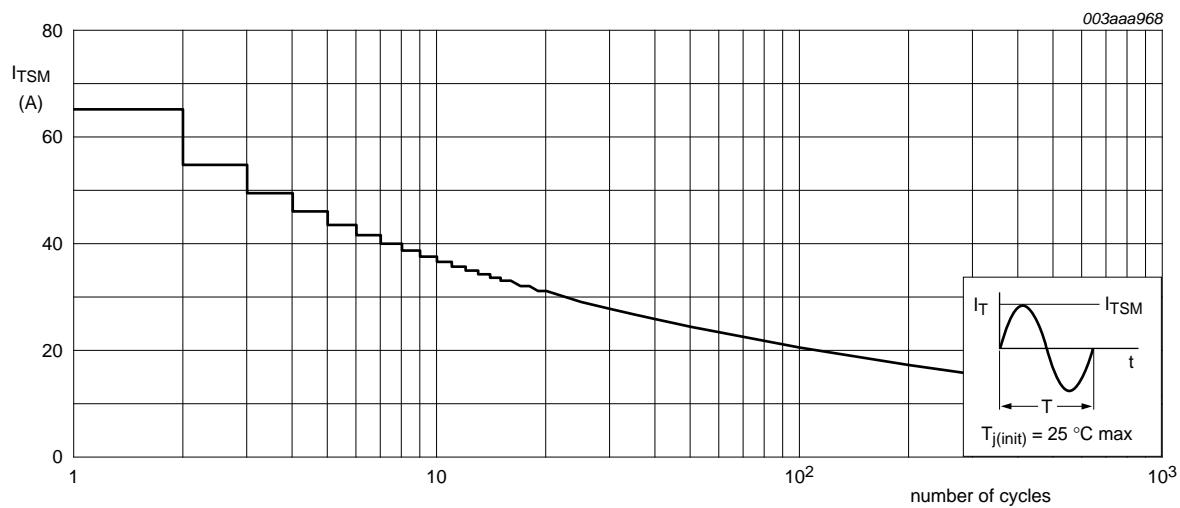
Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage				
	BT236X-600		[1]	-	V
	BT236X-600F		[1]	-	V
	BT236X-600G		[1]	-	V
	BT236X-800		-	800	V
	BT236X-800G		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_h \leq 88^\circ\text{C}$; see Figure 4 and 5	-	6	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_j = 25^\circ\text{C}$ prior to surge; see Figure 2 and 3			
		$t = 20\text{ ms}$	-	65	A
I^2t	I^2t for fusing	$t = 10\text{ ms}$	-	21	A^2s
dI_T/dt	rate of rise of on-state current	$I_{TM} = 12\text{ A}$; $I_G = 0.2\text{ A}$; $dI_G/dt = 0.2\text{ A}/\mu\text{s}$			
		$T2+ G+$	-	50	$\text{A}/\mu\text{s}$
		$T2+ G-$	-	50	$\text{A}/\mu\text{s}$
		$T2- G-$	-	50	$\text{A}/\mu\text{s}$
I_{GM}	peak gate current		-	2	A
V_{GM}	peak gate voltage		-	5	V
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T_{stg}	storage temperature		-40	+150	$^\circ\text{C}$
T_j	junction temperature		-	125	$^\circ\text{C}$

[1] Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/ μs .



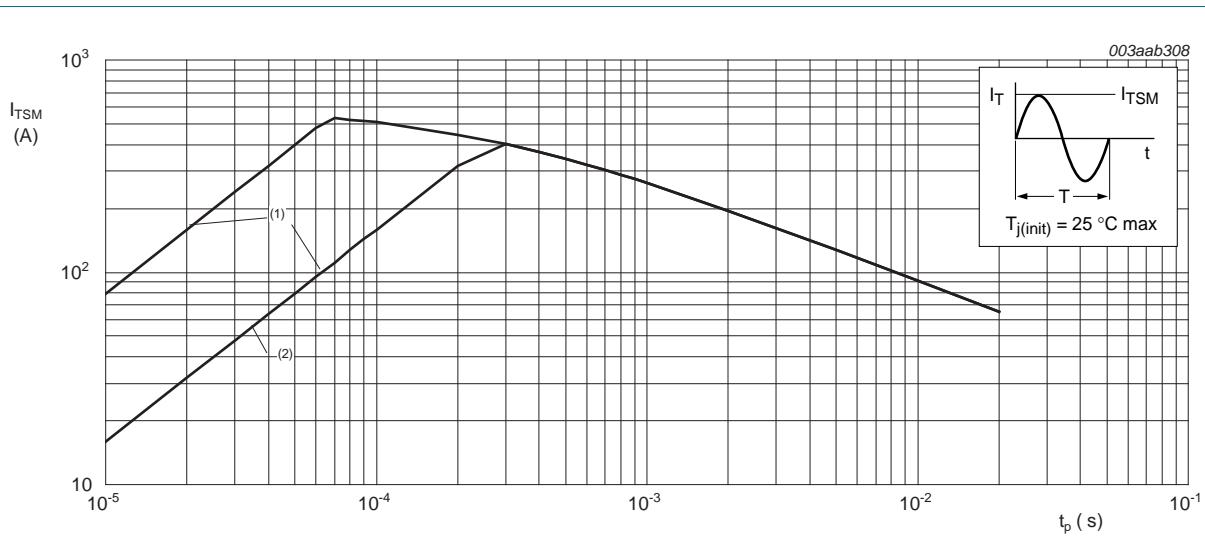
α = conduction angle

Fig 1. Total power dissipation as a function of RMS on-state current; maximum values



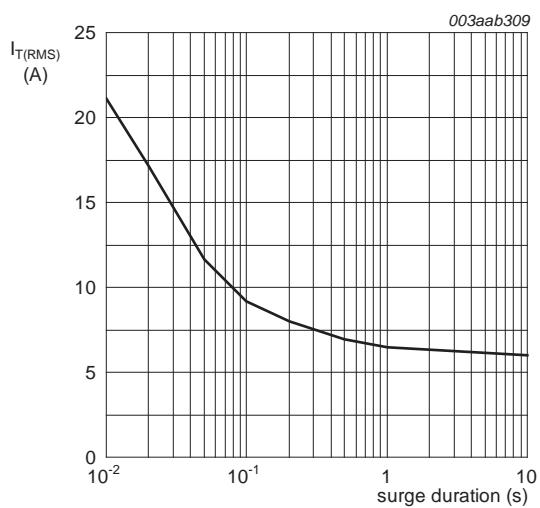
$f = 50$ Hz

Fig 2. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



$t_p \leq 20 \text{ ms}$
 (1) dl_T/dt limit
 (2) T2-G+ quadrant

Fig 3. Non-repetitive peak on-state current as a function of pulse duration; maximum values



$f = 50 \text{ Hz}; T_h \leq 88^\circ\text{C}$

Fig 4. RMS on-state current as a function of surge duration; maximum values

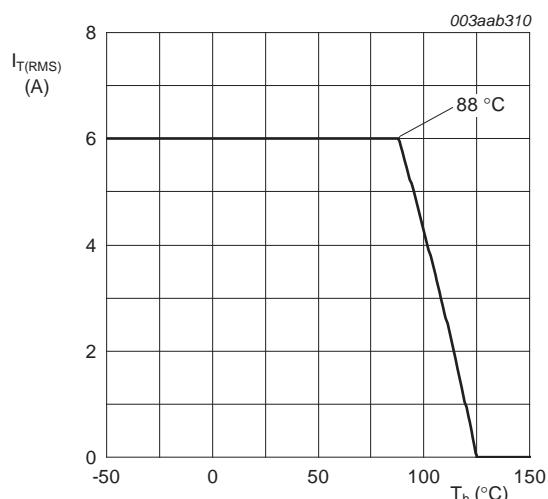


Fig 5. RMS on-state current as a function of heatsink temperature; maximum values

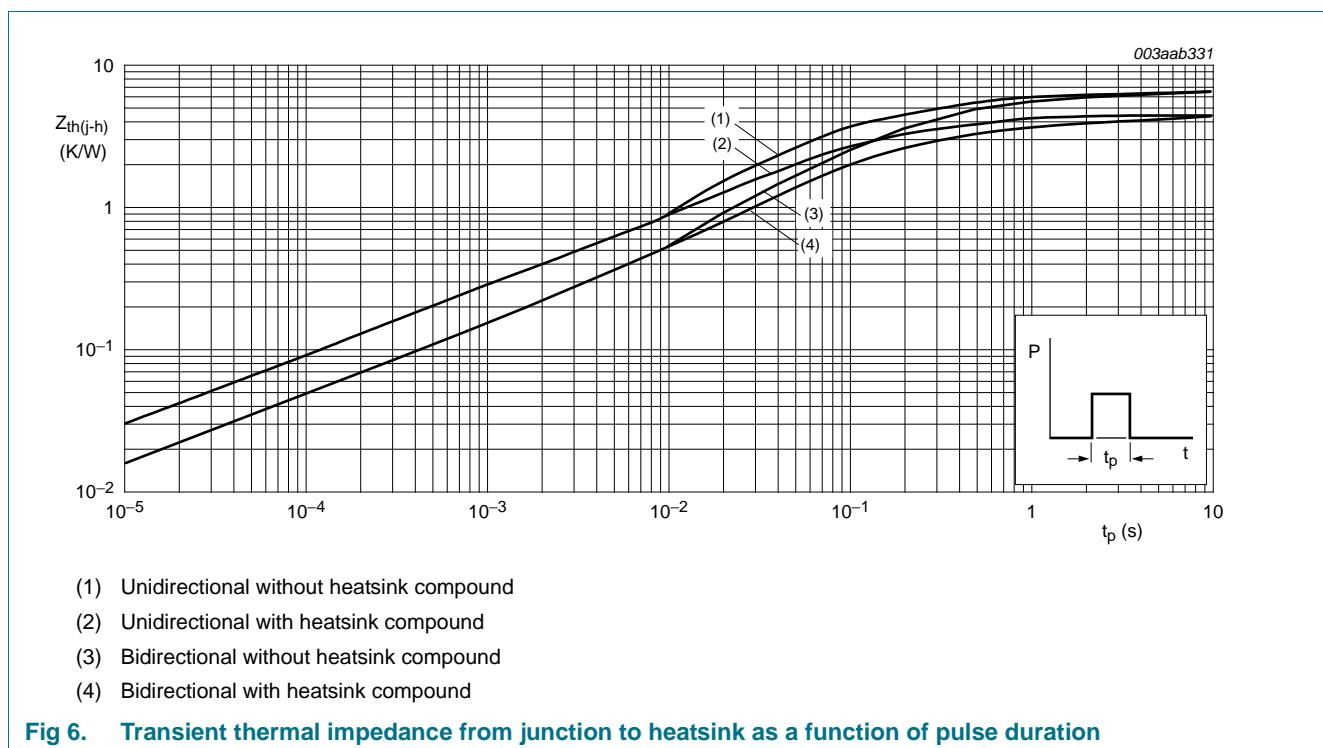
5. Thermal characteristics

Table 4. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-h)}$	thermal resistance from junction to heatsink	see Figure 6	[1]	-	-	K/W
		see Figure 6	[2]	-	-	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	55	-	K/W

[1] Full or half cycle with heatsink compound

[2] Full or half cycle without heatsink compound



6. Isolation characteristics

Table 5. Isolation limiting values and characteristics

$T_h = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{isol(rms)}$	RMS isolation voltage	from all three terminals to external heatsink; $f = 50$ Hz to 60 Hz; sinusoidal waveform; $RH \leq 65\%$; clean and dust free	-	-	2500	V
C_{isol}	isolation capacitance	from pin 2 to external heatsink; $f = 1$ MHz	-	10	-	pF

7. Static characteristics

Table 6. Static characteristics

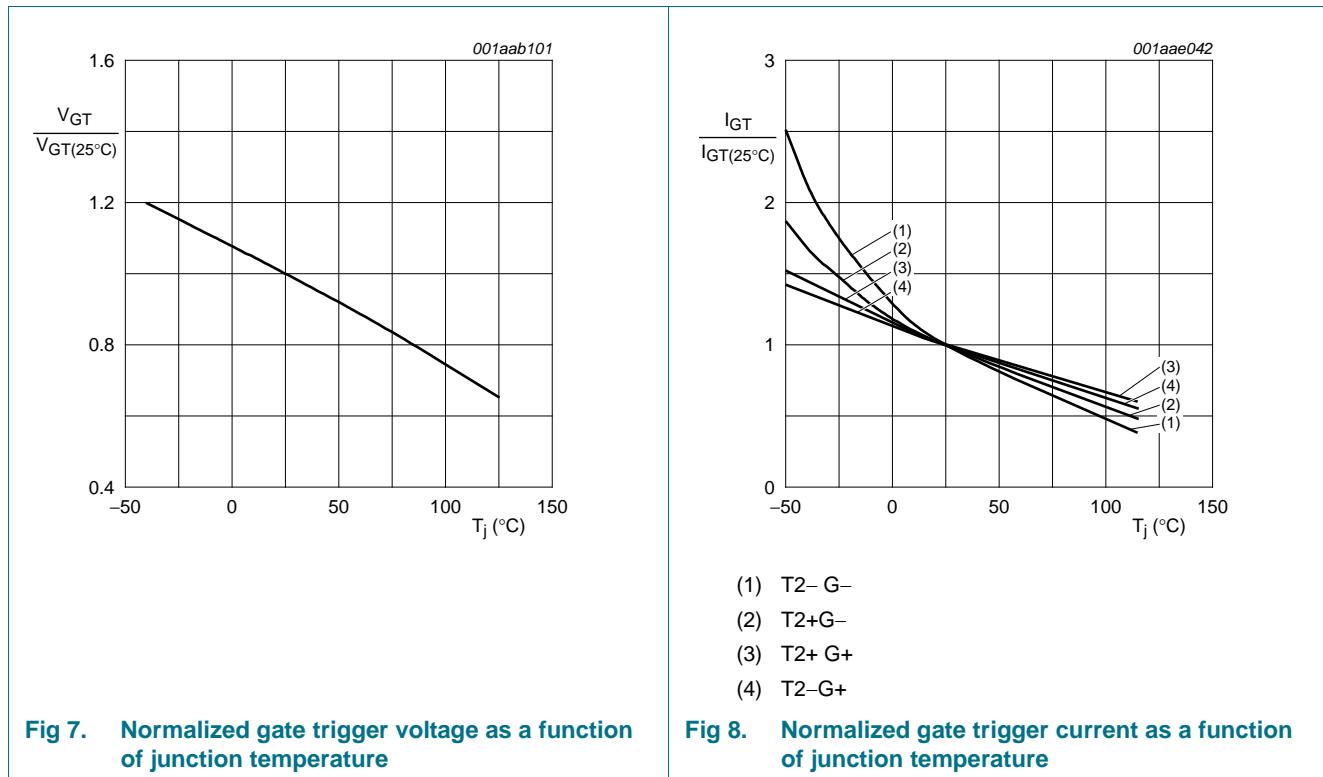
$T_j = 25^\circ\text{C}$ unless otherwise specified.

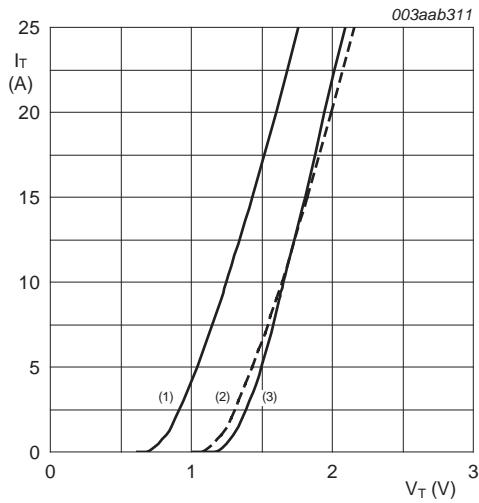
Symbol	Parameter	Conditions	BT236X-600 BT236X-800			BT236X-600F			BT236X-600G BT236X-800G			Unit
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; see Figure 8	T2+ G+	-	5	35	-	5	25	-	5	50 mA
			T2+ G-	-	8	35	-	8	25	-	8	50 mA
			T2- G-	-	11	35	-	11	25	-	11	50 mA
			T2- G+	-	30	70	-	30	70	-	30	100 mA
I_L	latching current	$V_D = 12\text{ V}$; $I_{GT} = 0.1\text{ A}$; see Figure 10	T2+ G+	-	7	30	-	7	30	-	7	45 mA
			T2+ G-	-	16	45	-	16	45	-	16	60 mA
			T2- G-	-	5	30	-	5	30	-	5	45 mA
			T2- G+	-	7	45	-	7	45	-	7	60 mA
I_H	holding current	$V_D = 12\text{ V}$; $I_{GT} = 0.1\text{ A}$; see Figure 11	-	5	20	-	5	20	-	5	40	mA
V_T	on-state voltage	$I_T = 10\text{ A}$; see Figure 9	-	1.3	1.65	-	1.3	1.65	-	1.3	1.65	V
V_{GT}	gate trigger voltage	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; see Figure 7	-	0.7	1.5	-	0.7	1.5	-	0.7	1.5	V
		$V_D = 400\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 125^\circ\text{C}$	0.25	0.4	-	0.25	0.4	-	0.25	0.4	-	V
I_D	off-state current	$V_D = V_{DRM(\text{max})}$; $T_j = 125^\circ\text{C}$	-	0.1	0.5	-	0.1	0.5	-	0.1	0.5	mA

8. Dynamic characteristics

Table 7. Dynamic characteristics

Symbol	Parameter	Conditions	BT236X-600			BT236X-600F			BT236X-600G			Unit
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 0.67V_{DRM(max)}$; $T_j = 125^\circ\text{C}$; exponential waveform; gate open circuit	100	250	-	50	250	-	200	250	-	$\text{V}/\mu\text{s}$
dV_{com}/dt	rate of change of commutating voltage	$V_{DM} = 400 \text{ V}$; $T_j = 95^\circ\text{C}$; $I_{T(RMS)} = 6 \text{ A}$; $dI_{com}/dt = 3.6 \text{ A/ms}$; gate open circuit; see Figure 12	-	20	-	-	20	-	10	20	-	$\text{V}/\mu\text{s}$
t_{gt}	gate-controlled turn-on time	$I_{TM} = 12 \text{ A}$; $V_D = V_{DRM(max)}$; $I_G = 0.1 \text{ A}$; $dI_G/dt = 5 \text{ A}/\mu\text{s}$	-	2	-	-	2	-	-	2	-	μs





$V_o = 1.26 \text{ V}$
 $R_s = 0.0378 \Omega$
(1) $T_j = 125 \text{ }^\circ\text{C}$; typical values
(2) $T_j = 125 \text{ }^\circ\text{C}$; maximum values
(3) $T_j = 25 \text{ }^\circ\text{C}$; maximum values

Fig 9. On-state current as a function of on-state voltage

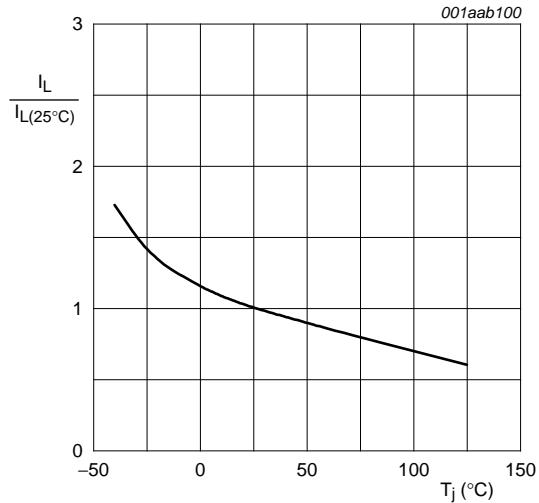


Fig 10. Normalized latching current as a function of junction temperature

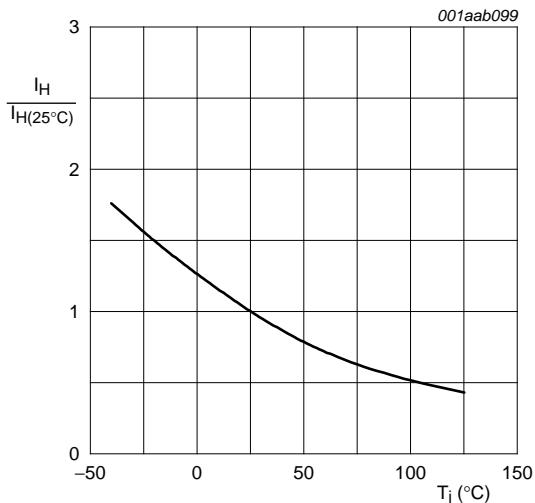
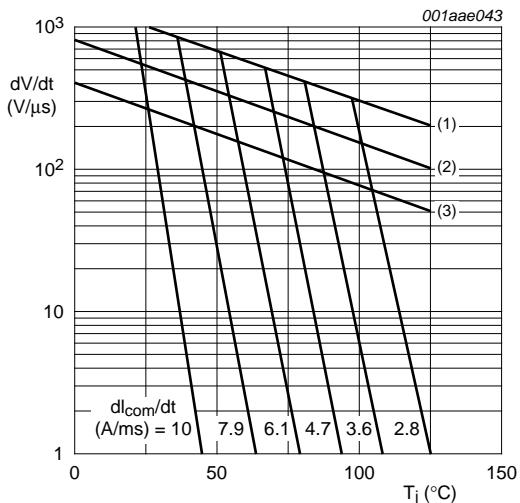


Fig 11. Normalized holding current as a function of junction temperature



The triac should commutate when the dV/dt is below the value on the appropriate curve for pre-commutation dI_T/dt .

- (1) Off-state dV/dt limit for BT236X-600G_800G
- (2) Off-state dV/dt limit for BT236X-600_800
- (3) Off-state dV/dt limit for BT236X-600F

Fig 12. Typical commutation dV/dt as a function of junction temperature

9. Package outline

Plastic single-ended package; isolated heatsink mounted;
1 mounting hole; 3-lead TO-220 'full pack'

SOT186A

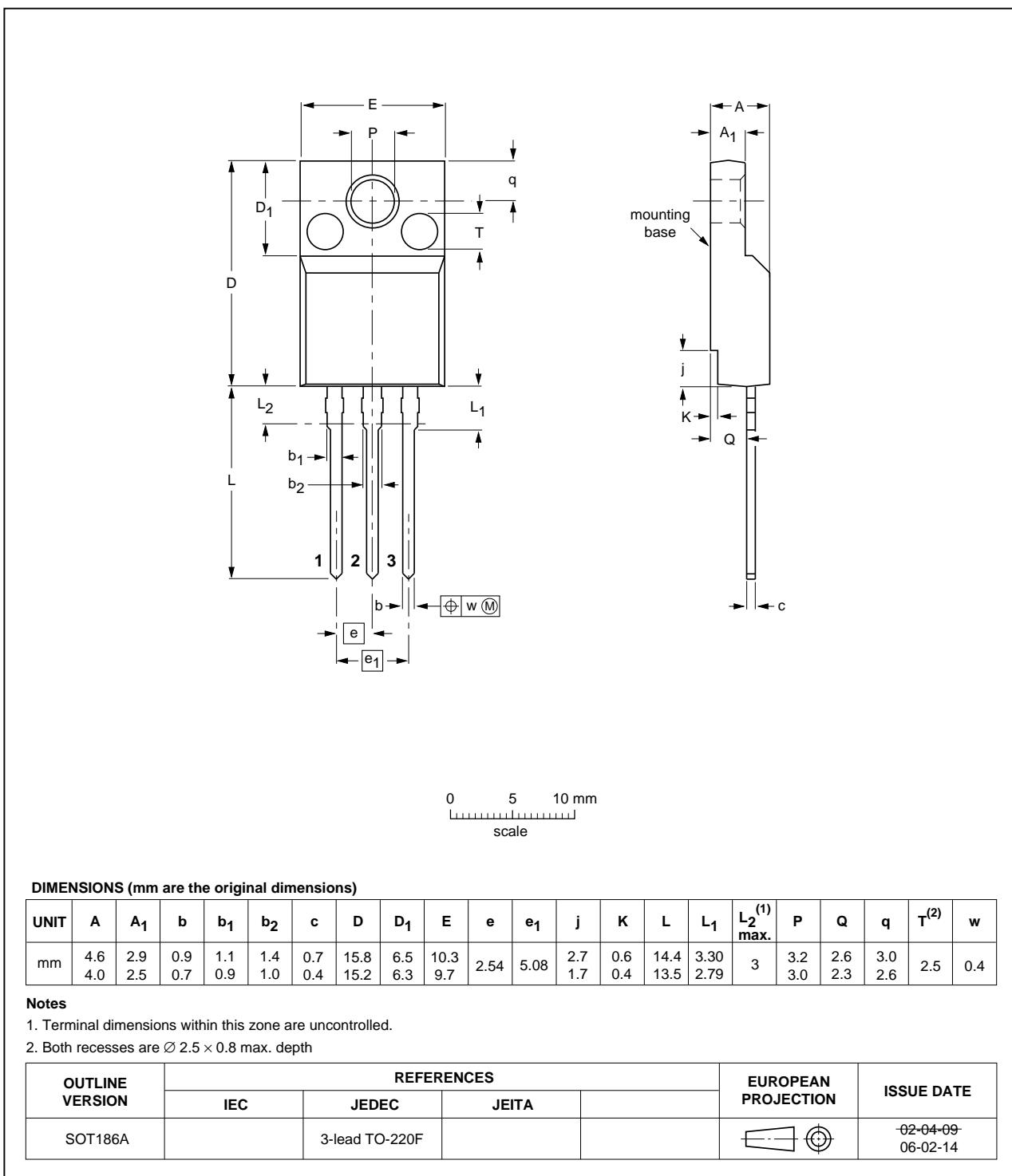


Fig 13. Package outline SOT186A (3-lead TO-220F)

10. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BT236X_SER_F_G v.3	20111103	Product data sheet	-	BT236X_SER_F_G v.2
Modifications:	<ul style="list-style-type: none">• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.• Legal texts have been adapted to the new company name where appropriate.			
BT236X_SER_F_G v.2	20060314	Product data sheet	-	BT236X_SER_F_G v.1
BT236X_SER_F_G v.1	20060209	Product data sheet	-	-

11. Legal information

11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

11.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

11.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

11.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

12. Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

13. Contents

1	Product profile	1
1.1	General description	1
1.2	Features and benefits	1
1.3	Applications	1
1.4	Quick reference data	1
2	Pinning information	1
3	Ordering information	2
4	Limiting values	2
5	Thermal characteristics	5
6	Isolation characteristics	5
7	Static characteristics	6
8	Dynamic characteristics	7
9	Package outline	9
10	Revision history	10
11	Legal information	11
11.1	Data sheet status	11
11.2	Definitions	11
11.3	Disclaimers	11
11.4	Trademarks	12
12	Contact information	12
13	Contents	13

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2011.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 3 November 2011

Document identifier: BT236X_SER_F_G