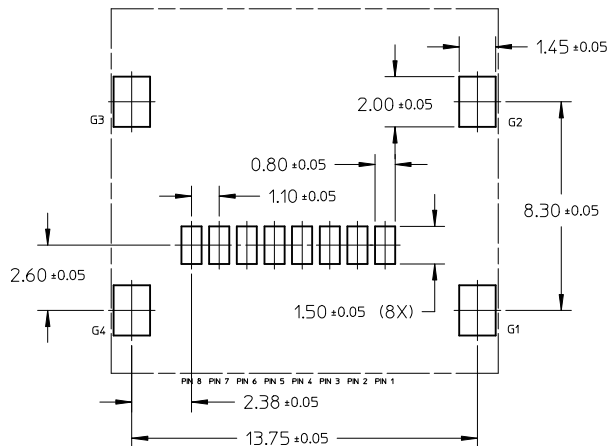
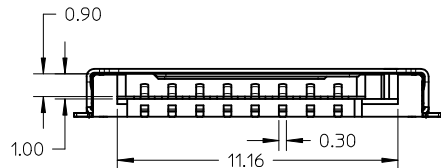
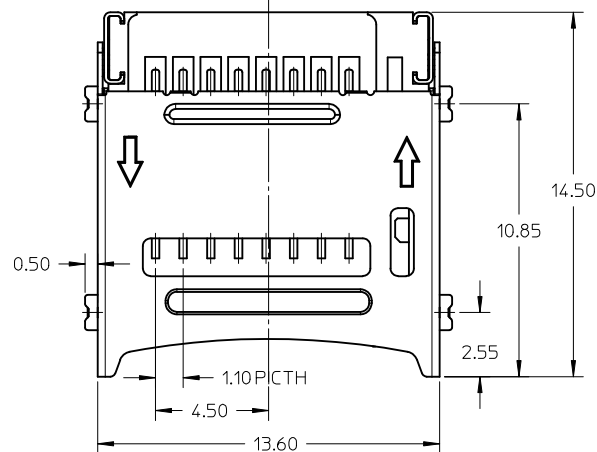




RECOMMENDED PCB LAYOUT

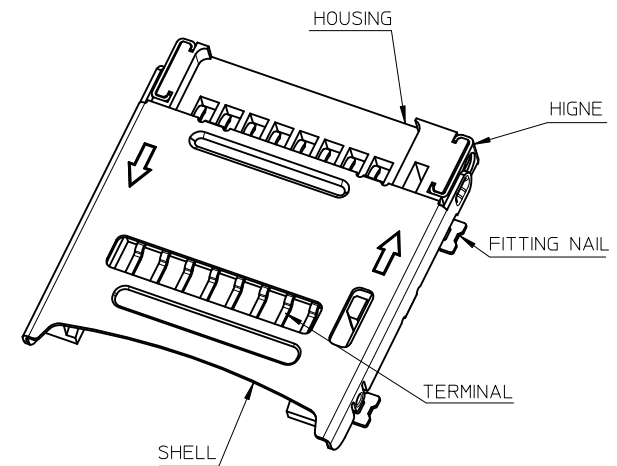
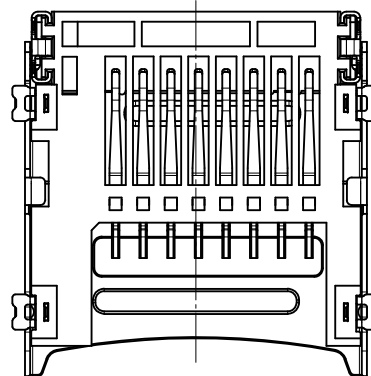
IN CASE OF STENCIL THICKNESS: 0.12MM
OPEN RATIO IS 100%.

IN CASE OF STENCIL THICKNESS : 0.1MM
OPEN RATIO IS 100% MINIMUM.

(0.08MM STENCIL THICKNESS CAN NOT BE RECOMMENDED)

P/N	Gold PLATING ON CONTACT AREA
472190001	2u" min.
472192001	20u" min.

G4	GND
G3	GND
G2	GND
G1	GND
PIN8	DAT1
PIN7	DAT0
PIN6	VSS
PIN5	CLK
PIN4	VDD
PIN3	CMD
PIN2	CD/DAT3
PIN1	DAT2
PIN NUMBER	PIN NAME



NOTES:

- MATERIAL:
 - 1-1 TERMINAL, FITTING NAIL, HIGE : COPPER ALLOY
 - 1-2 SHELL : STAINLESS STEEL
 - 1-3 HOUSING : LCP, UL 94-V0, BLACK
- FINISH:
 - 2-1 TERMINAL : OVERALL NICKEL UNDERPLATED, GOLD PLATING ON CONTACT AREA
TIN PLATING ON SOLDER TAIL
 - 2-2 FITTING NAIL : OVERALL NICKEL UNDERPLATED, TIN PLATED ON SOLDER AREA
 - 2-3 HIGE : OVERALL NICKEL PLATING
- MECHANICAL SPECIFICATION:

DURABILITY MATED 5,000 TIMES AND STILL MEET ELECTRICAL CHARACTERISTICS
- ELECTRICAL SPECIFICATION:
 - 4-1 RATED CURRENT (PER CONTACT) : 0.5AMPS MAX AND
TEMPERATURE RISE SHOULD BE 30°C MAX
 - 4-2 INSULATION RESISTANCE(IIR):100 MEGOHMS MIN.
 - 4-3 DIELECTRIC WITHSTANDING VOLTAGE : N/A
 - 4-4 CONTACT RESISTANCE: 100 MILLIOHMS MAX
- COMPLIANT TO RoHS DIRECTIVE 2002/95/EC
AND ELV 2000/53/EC.
- PRODUCT PACKING: PK-47219-001.
- PRODUCT SPECIFICATION: PS-47219-001.

REVISED	EC NO: SH2012-0233	2012/01/18	2012/01/18	2012/01/20
G	DRWINGLLI	CHKD:XQZHANG	APPR:AYIN	REV

QUALITY SYMBOLS	DESCRIPTION
$\nabla_A = 0$	
$\nabla_G = 0$	
$\nabla_P = 0$	

GENERAL TOLERANCES (UNLESS SPECIFIED)	
	mm
4 PLACES	± 0.15
3 PLACES	± 0.10
2 PLACES	± 0.05
1 PLACE	± 0.02
ANGULAR ± 3 °	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	

DIMENSION STYLE	
MM ONLY	
DRAWN BY	DATE
GLL I	2012/01/18
CHECKED BY	DATE
XQZHANG	2012/01/18
APPROVED BY	DATE
MATERIAL NO.	
SEE TABLE	
SIZE	A3

SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION
5:1	METRIC	
TITLE		
TFR READER HINGE TYPE		
MOLEX INCORPORATED		
DOCUMENT NO.	SHEET NO.	
SD-47219-001	1 OF 1	