

*Customer :

SPECIFICATION

ITEM	TOP LED DEVICE
MODEL	SSC-KWT824
PART NO.	Rev 0.0

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1.Features

- ☐ White colored SMT package
- ☐ Suitable for all SMT assembly methods ; Suitable for all soldering methods
- ☐ Applications :
 - Mobile Handset
 - Illumination

2. Absolute Maximum Ratings^{*1}

 (T_a=25°C)

Parameter	Symbol	Value	Unit
Power Dissipation	P_d	333	mW
Forward Current	I_F	90	mA
Peak Forward Current (per die)	I_{FM}^{*2}	100	mA
Reverse Voltage (per die)	V_R	5	V
LED Junction Temperature	T_j	125	°C
Operating Temperature	T_{opr}	-30 ~ +85	°C
Storage Temperature	T_{stg}	-40 ~ +100	°C

*1 Care is to be taken that Power Dissipation does not exceed the Absolute Maximum Rating of the product.

*2 I_{FM} conditions is Pulse width Tw ≤ 0.1ms, Duty ratio ≤ 1/10.

3. Electro-Optical Characteristics

 (T_a=25°C)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage (per die)	V_F	$I_F = 20\text{mA}$	-	3.2	-	V
Reverse Current (per die)	I_R	$V_R = 5\text{V}$	-	-	50	μA
Luminous Intensity ^{*1}	I_V	$I_F = 60\text{mA}$		4	-	cd
		$I_F = 90\text{mA}$		5.5	-	cd
Peak Luminous Intensity vs. Drive Current (Flash Mode) ^{*2}		$I_F = 170\text{mA}$		12		cd
Viewing Angle ^{*3}	$2\theta_{1/2}$	$I_F = 60\text{mA}$	-	120	-	deg.
Chromaticity Coordinates	x	$I_F = 60\text{mA}$		0.31		-
	y	$I_F = 60\text{mA}$		0.31		-

*1 The luminous intensity I_V is measured at the peak of the spatial pattern which may not be aligned with the mechanical axis of the LED package.
Luminous Intensity Measurement allowance is ± 10%.

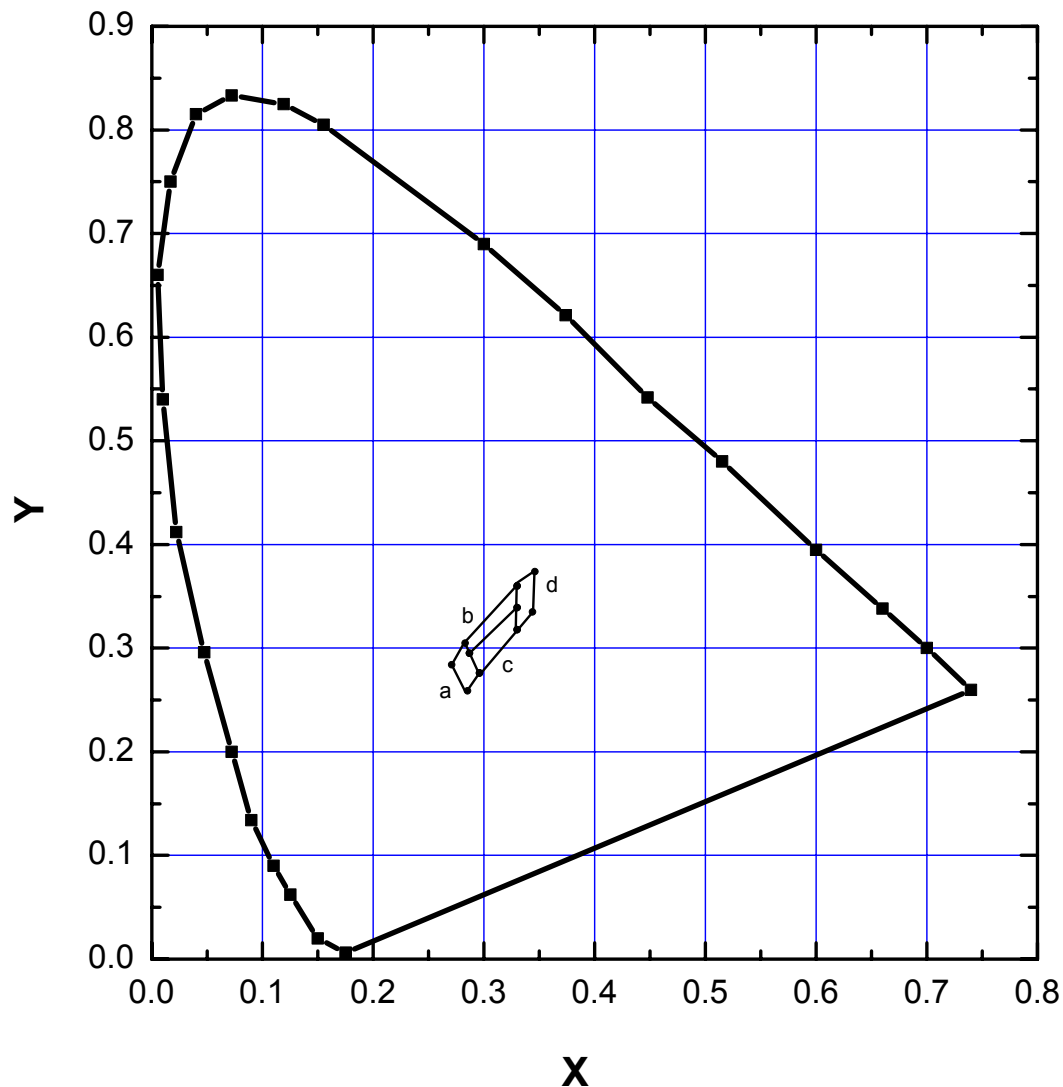
*2 Flash mode condition is Pulse width Tw = 1sec, Duty ratio = 2 / 5

*3 $\theta_{1/2}$ is the off-axis where the luminous intensity is 1/2 of the peak intensity.

Note : All measurements were made under the standardized environment of SSC

4. CIE Chromaticity Diagram

ICI Chromaticity Diagram



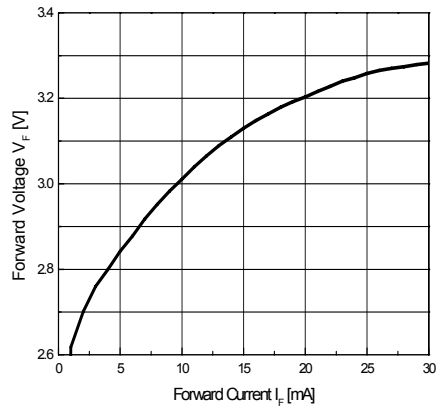
BIN	Chromaticity Coordinates				
A	x	0.271	0.285	0.296	0.283
	y	0.284	0.259	0.276	0.305
B	x	0.283	0.287	0.330	0.330
	y	0.305	0.295	0.339	0.360
C	x	0.287	0.296	0.330	0.330
	y	0.295	0.276	0.318	0.339
D	x	0.330	0.330	0.344	0.346
	y	0.360	0.318	0.335	0.374

*Color Coordinates Measurement allowance is ± 0.01

5. Characteristic Diagram

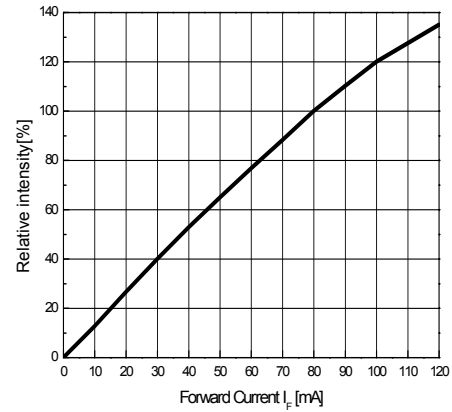
◎ Forward Voltage vs.

Forward Current (per die)



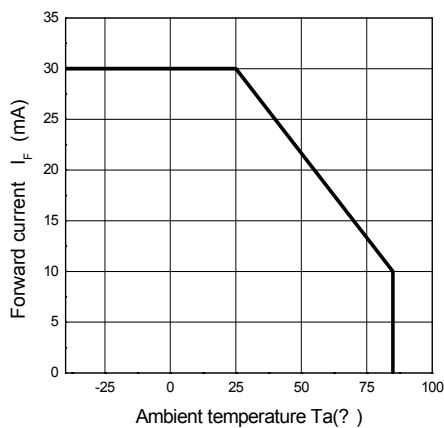
◎ Forward Current vs.

Relative Intensity

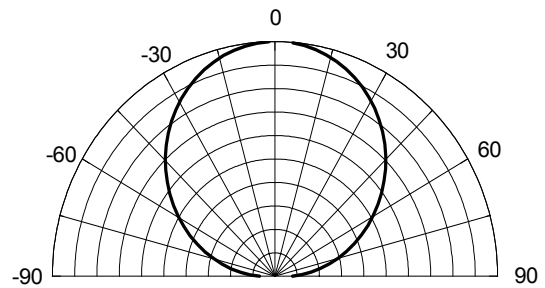


◎ Ambient Temperature vs.

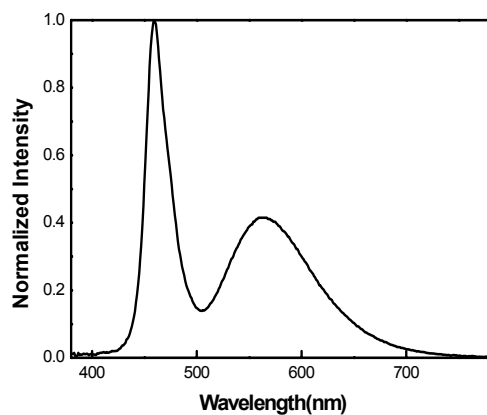
Allowable Forward Current



◎ Radiation Diagram

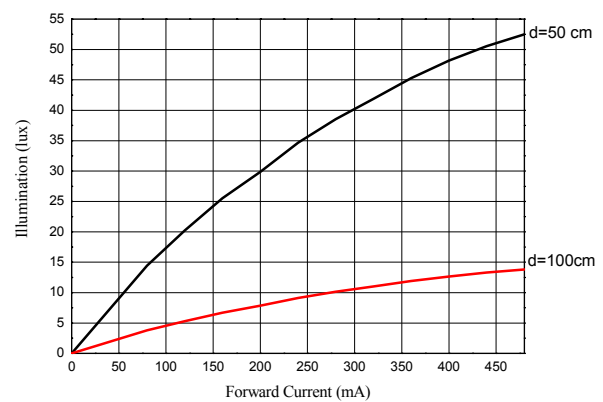


◎ Spectrum



◎ Forward Current vs.

Illumination



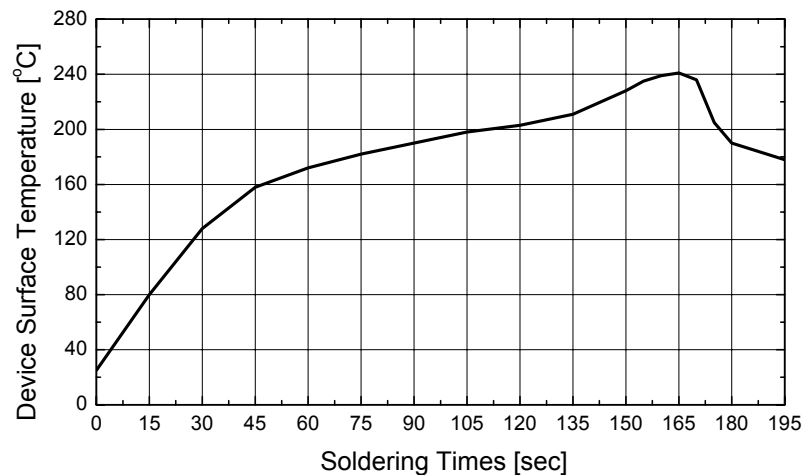
6. Soldering Profile

The LED can be soldered in place using the reflow soldering method.

(1) Lead solder

Preliminary heating to be at maximum 210°C for maximum 2 minutes.

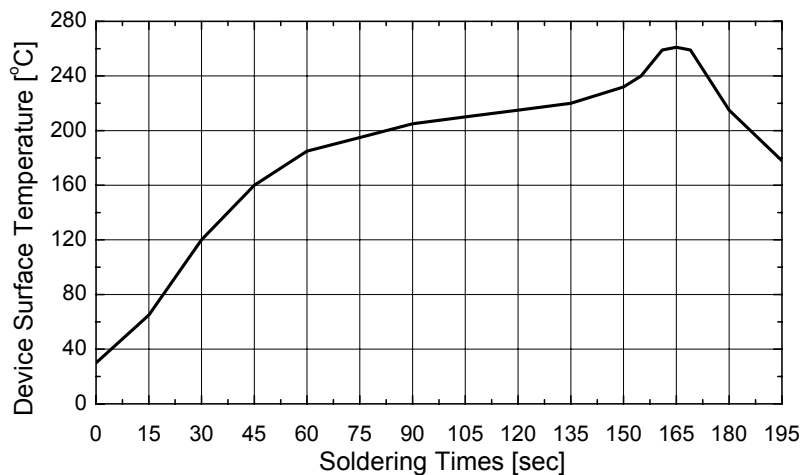
Soldering heat to be at maximum 240°C for maximum 10 seconds.



(2) Lead-free solder

Preliminary heating to be at maximum 220°C for maximum 2 minutes.

Soldering heat to be at maximum 260°C for maximum 10 seconds.

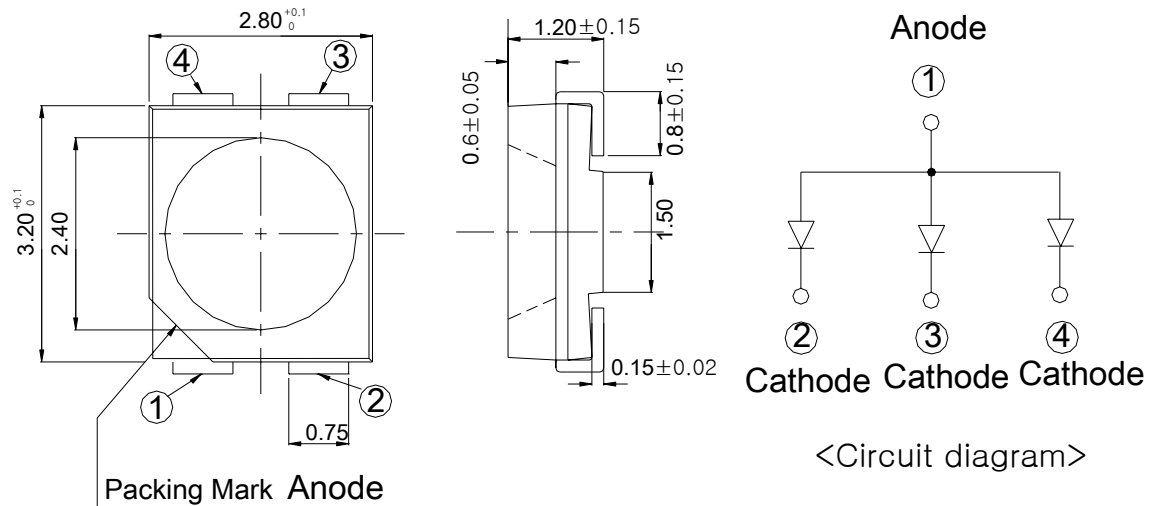


(3) Hand Soldering conditions

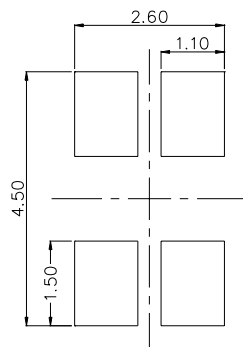
Not more than 5 seconds @MAX300°C, under Soldering iron.

Note : In case that the soldered products are reused in soldering process, we don't guarantee the products

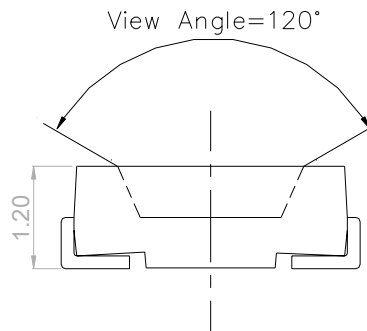
7. Outline Dimensions And Materials


 (Tolerance: ± 0.2 , Unit: mm)

Recommended Solder Pattern



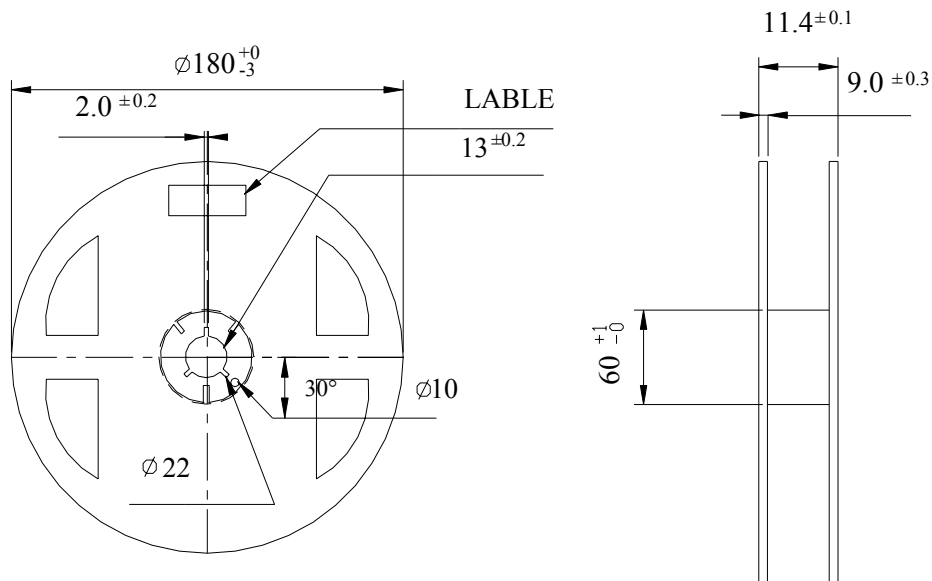
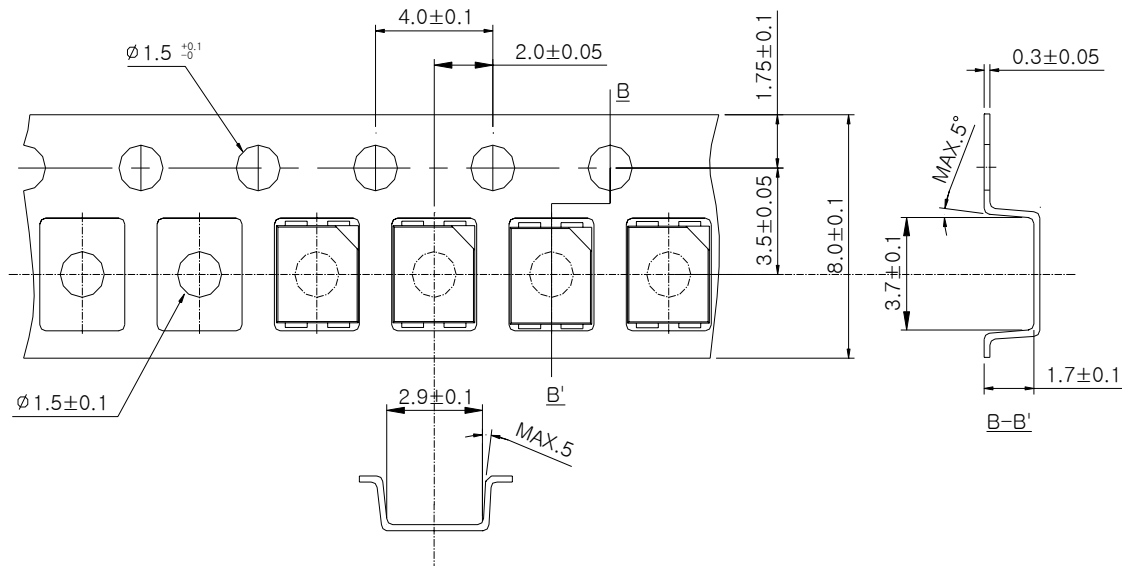
*RECOMMENDED VIEW ANGLE FOR APPLICATION



*MATERIALS

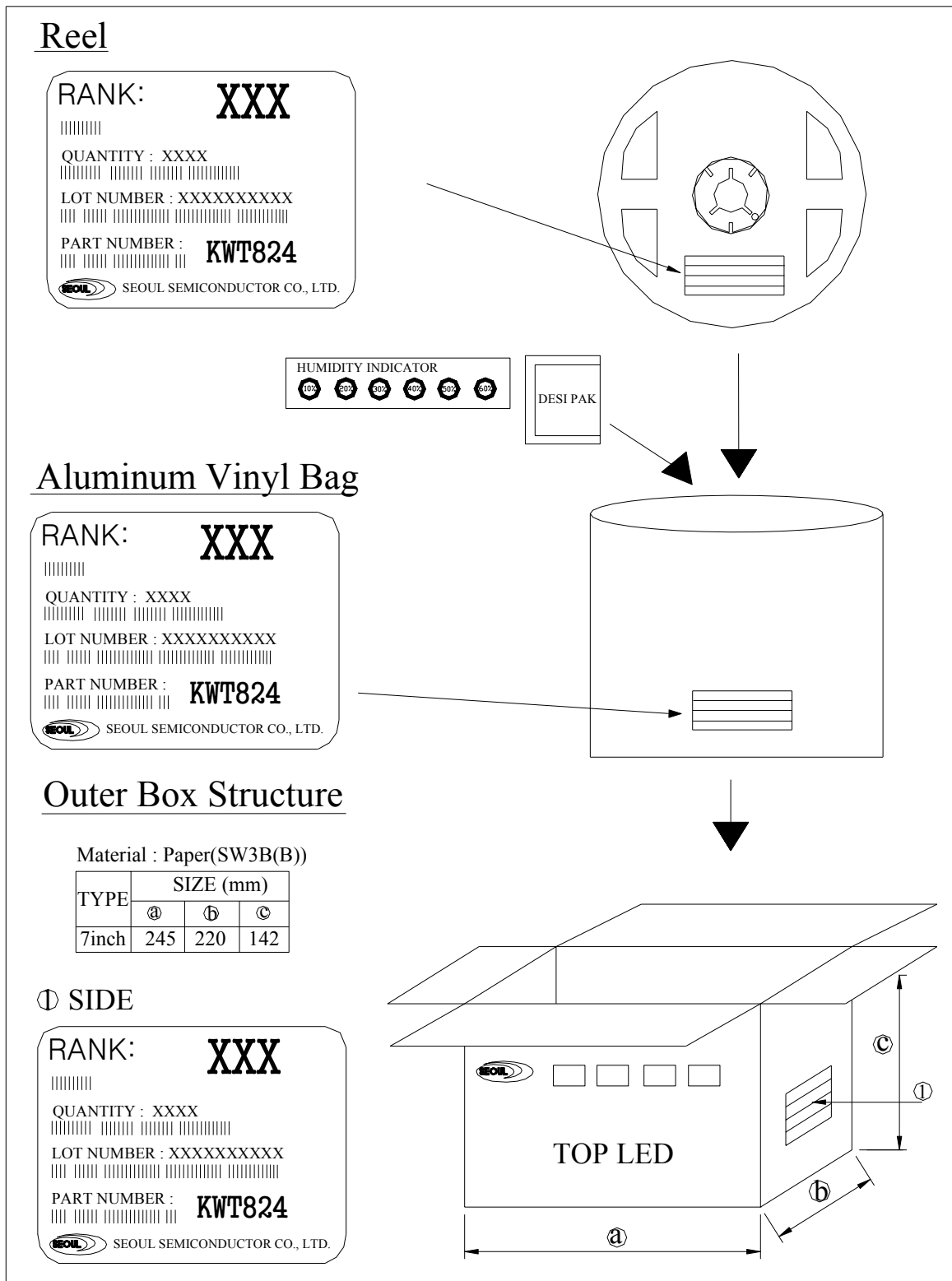
PARTS	MATERIALS
Package	Heat-Resistant Polymer
Encapsulationg Resin	Mold Resin(with Phosphor)
Electrodes	Ag Plating Copper Alloy

8. Reel Dimensions



- (1) Quantity : 2000 pcs/Reel
- (2) Cumulative Tolerance: Cumulative Tolerance/10 pitches to be $\pm 0.2\text{mm}$
- (3) Adhesion Strength of Cover Tape : Adhesion strength to be 0.1 – 0.7N when the cover tape is turned off from the carrier at 10° angle to be the carrier tape.
- (4) Package:P/N, Manufacturing data Code No. and quantity to be indicated on a damp proof Package.

9. Reel Packing Structure



10. Lot Number

The lot number is composed of the following characters;

MWT○○□□◎◎ - ◆◆

○○ – Year (01 for 2001, 02 for 2002, 03 for 2003)

□□ – Month (01 for Jan., 02 for Feb., 11 for Nov., 12 for Dec.)

◎◎ – Day (01, 02, 03, 04, 28, 29, 30, 31.)

◆◆ – Rank (A, B, C, D, W, X, Y, Z.)

RANK:
XXX

QUANTITY : XXXX

LOT NUMBER : XXXXXXXXXXXX

PART NUMBER : KWT824


SEOUL SEMICONDUCTOR CO., LTD.

11. Precaution for use

(1) Storage

In order to avoid the absorption of moisture, it is recommended to store in a dry box (or a desiccator) with a desiccant. Otherwise, to store them in the following environment is recommended.

Temperature : 5℃~30℃ Humidity : maximum 65%HR

(2) Attention after open.

LED is correspond to SMD, when LED be soldered dip, interfacial separation may affect the light transmission efficiency, causing the light intensity to drop. Attention in followed;

- a. After opened and mounted the soldering shall be quickly.
- b. Keeping of a fraction

Temperature : 5 ~ 40℃ Humidity : less than 30%

(3) In the case of more than 1 week passed after opening or change color of indicator on desiccant, components shall be dried 10-12hr. at 60±5℃.

(4) In the case of that the components is humided, the components shall be dried;

24Hr at 80±5℃ or 12Hr at 100±5℃.

(5) Any mechanical force or any excess vibration shall not be accepted to apply during cooling process to normal temperature after soldering.

(6) Quick cooling shall be avoided.

(7) Components shall not be mounted on warped direction of PCB.

(8) Anti radioactive ray design is not considered for the products.

(9) This device should not be used in any type of fluid such as water, oil, organic solvent etc. When washing is required, IPA should be used.

(10) When the LEDs are illuminating, operating current should be decided after considering the ambient maximum temperature.

(11) LEDs must be stored to maintain a clean atmosphere. If the LEDs are stored for 3 months or more after being shipped from SSC, a sealed container with a nitrogen atmosphere should be used for storage.

(12) The LEDs must be soldered within seven days after opening the moisture-proof packing.

(13) Repack unused products with anti-moisture packing, fold to close any opening and then store in a dry place.

(14) The appearance and specifications of the product may be modified for improvement without notice.

12. RELIABILITY

(1) TEST ITEMS AND RESULTS

TEST ITEM	Test conditions	Test Time	Number of Damaged	Judgment
Resistance to Soldering Heat ^{*1}	$T_{sld} = 260^{\circ}\text{C}$, 10 sec.	1 Time	0 / 22	OK.
Solderability ^{*1}	$T_{sld} = 215 \pm 5^{\circ}\text{C}$, 5 sec.	1 Time	0 / 22	OK.
Thermal Shock ^{*1}	-30°C (30 min) ~ 85°C (30 min) Transfer 5 min	100 cycles	0 / 22	OK.
High Temperature Storage ^{*1}	$T_a = 100^{\circ}\text{C}$	1000 hrs.	0 / 22	OK.
Humidity Heat Load ^{*1}	$T_a = 85^{\circ}\text{C}$, $RH = 85\%$	1000 hrs.	0 / 22	OK.
Steady State Life Test	$T_a = 25^{\circ}\text{C}$, $I_F = 20 \text{ mA/die}$	300 hrs	0 / 22	OK.
High Humidity Heat Life Test	$T_a = 85^{\circ}\text{C}$, $RH = 85\%$, $I_F = 10 \text{ mA/die}$	300 hrs	0 / 22	OK.
Low Temperature Life Test	$T_a = -30^{\circ}\text{C}$, $I_F = 20 \text{ mA/die}$	300 hrs	0 / 22	OK.
Pulse Life Test	$T_a = 60^{\circ}\text{C}$, $RH = 90\%$ On=2 sec, Off=2sec $I_F = 30 \text{ mA/die}$	100,000 cycle	0 / 22	OK.

^{*1} Faulty decision of this test item is made by On / Off after the test.

(2) CRITERIA FOR JUDGING THE DAMAGE

Item	Symbol	Test Condition	Criteria for Judgement	
			Min.	Max.
Forward Voltage	V_F	$I_F = 20 \text{ mA/die}$	L.S.L $\times$ 1.2	U.S.L $\times$ 1.2
Reverse Current	I_R	$V_R = 5 \text{ V/die}$	-	U.S.L $\times$ 2.0
Luminous Intensity	I_V	$I_F = 20 \text{ mA/die}$	L.S.L $\times$ 0.5	-

U.S.L. : Upper Standard Level

L.S.L. : Lower Standard Level

(3)ESD guarantee condition

Item	Test Condition	Criteria for Judgement	Test Form
HBM	1000 V	$I_R = 100 \mu\text{A}$ and below	CONTACT