

Status: **Active****SSTVA16859C****DDR 13-Bit to 26-Bit Registered Buffer**

13-bit to 26-bit registered buffer designed for 2.3V-2.7V VDD for PC1600 - PC2700 and 2.5V-2.7V VDD for PC3200, and supports low standby operation. All data inputs and outputs are SSTL\_2 level compatible with JEDEC standard for SSTL\_2.

**Features**

- Differential clock signals
- Meets SSTL\_2 signal data
- Supports SSTL\_2 class I specifications on outputs
- Low-voltage operation
- VDD = 2.3V to 2.7V
- Available in 64 pin TSSOP and 56 pin MLF packages
- Exceeds ICSSSTVN16859 performance

**Product Specification**

Pkg. Code	Pkg. Type	Length (mm)	Width (mm)	Temp. Grade
NLG56, PAG64	VFQFPN, TSSOP	8.17	6.1, 8	C

**Product Options**

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Carrier Type	Sample & Buy
SSTVA16859CGLF	Active	PAG64	TSSOP	64	C	Yes	Tube	<a href="#">Check Availability</a> ▶
SSTVA16859CGLFT	Active	PAG64	TSSOP	64	C	Yes	Reel	<a href="#">Check Availability</a> ▶
SSTVA16859CKLF	Active	NLG56P3	VFQFPN	56	C	Yes	Tray	<a href="#">Check Availability</a> ▶
SSTVA16859CKLFT	Active	NLG56P3	VFQFPN	56	C	Yes	Reel	<a href="#">Check Availability</a> ▶

**Technical Documentation**

Title	Type	Format	File Size	Date
<b>PCNs &amp; PDNs</b>				
PCN# : TB1303-02 Change of Tape & Reel Packing Method for Selective Products	Product Change Notice	PDF	361 KB	03/24/2013

[GET TECHNICAL SUPPORT](#) ▶[CONTACT SALES OFFICE](#) ▶