Unit: mm

TOSHIBA Transistor Silicon NPN Epitaxial Type

# 2SD2092

#### **Switching Applications**

Lamp, Solenoid Drive Applications

- High DC current gain:  $h_{FE}(1) = 500 \text{ to } 1500$
- Low collector saturation voltage: VCE (sat) = 0.3 V (max)

#### **Absolute Maximum Ratings (Tc = 25°C)**

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	100	$(\mathcal{N} \land$	
Collector-emitter voltage		V <sub>CEO</sub>	100	$\langle \downarrow \rangle$	
Emitter-base voltage		V <sub>EBO</sub>	7	<b>&gt;</b>	
Collector current	DC	IC	3	) A	
	Pulse	I <sub>CP</sub>	5	~ A	
Base current		ΙΒ		Α	
Collector power dissipation	Ta = 25°C	PC	2.0	<b>X</b>	
	Tc = 25°C	FC .	25	< <b>~</b>	
Junction temperature		T <sub>j</sub> ((	150	°C/	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

1.1 1.1 2.54±0.25

2.54±0.25 2.54±0.25

2.54±0.25 2.54±0.25

1. BASE
2. COLLECTOR
3. EMITTER

JEDEC —

JEITA SC-67

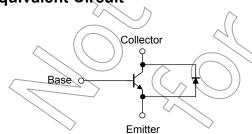
TOSHIBA 2-10R1A

Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

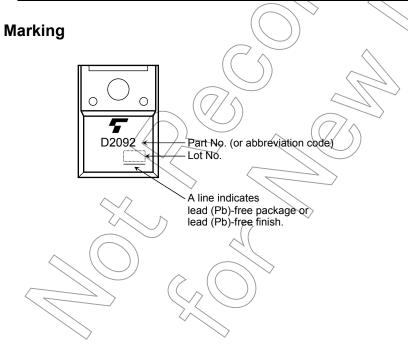
temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Equivalent Circuit**

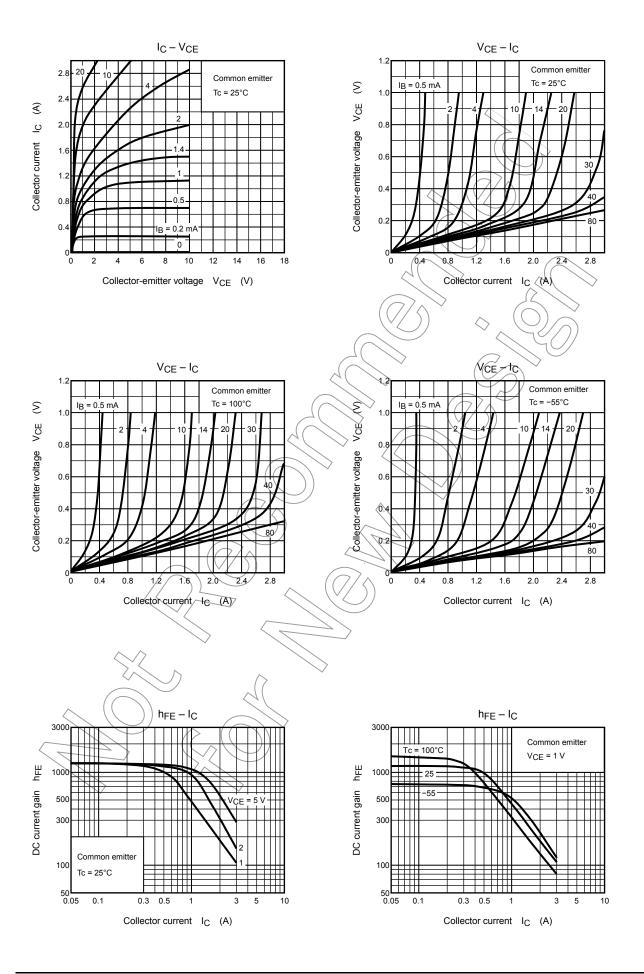


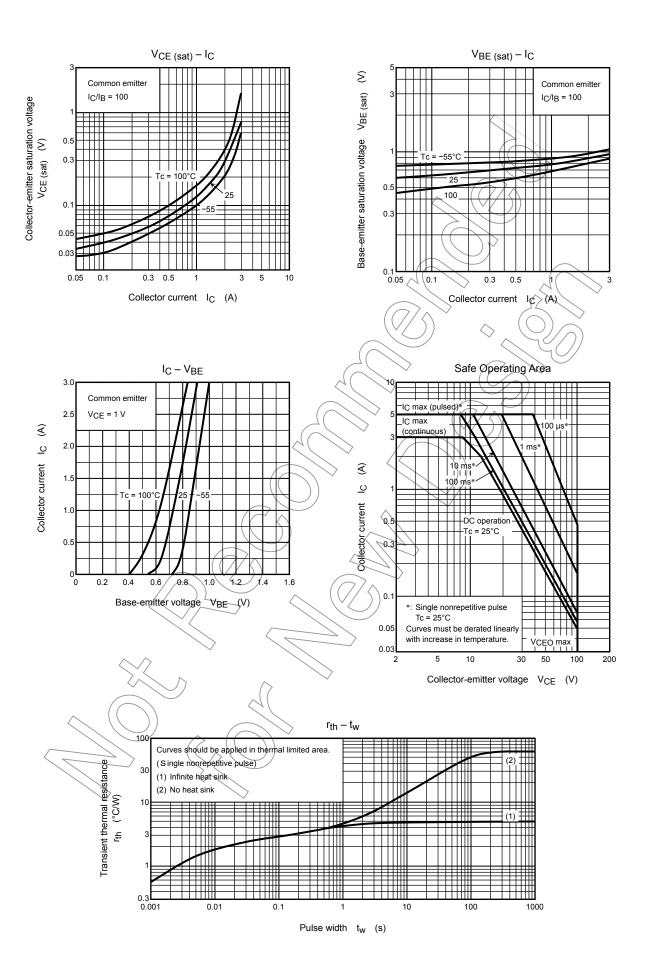
## Electrical Characteristics (Tc = 25°C)

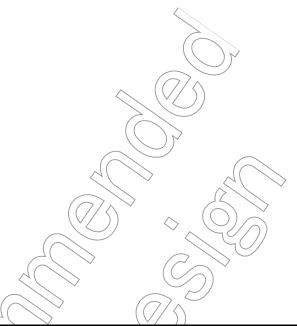
Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off c	urrent	I <sub>CBO</sub>	V <sub>CB</sub> = 100 V, I <sub>E</sub> = 0	_	_	10	μA
Emitter cut-off cur	rent	I <sub>EBO</sub>	V <sub>EB</sub> = 7 V, I <sub>C</sub> = 0	_	_	10	μA
Collector-emitter b	oreakdown voltage	V (BR) CEO	I <sub>C</sub> = 50 mA, I <sub>B</sub> = 0	100	_	_	V
DC current gain		h <sub>FE (1)</sub>	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 0.5 A	500	1	1500	
		h <sub>FE (2)</sub>	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 1 A	150	) >-		
Collector-emitter s	saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 1 A, I <sub>B</sub> = 10 mA	)	_	0.3	٧
Base-emitter satu	ration voltage	V <sub>BE (sat)</sub>	I <sub>C</sub> = 1 A, I <sub>B</sub> = 10 mA	$\rightarrow$		1.2	٧
Collector-emitter f	orward voltage	V <sub>ECF</sub>	I <sub>E</sub> = 1 A, I <sub>B</sub> = 0			2.0	٧
Transition frequer	ісу	f <sub>T</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.5 A	_	140		MHz
Collector output c	apacitance	C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	30	1	pF
Switching time Stor	Turn-on time	t <sub>on</sub>	Output	_	0.5	> <del>-</del>	μs
	Storage time	t <sub>stg</sub>	Input B C C S O V V C C = 30 V		5	<i>–</i>	
	Fall time	t <sub>f</sub>	$I_{B_1} = -I_{B_2} = 10 \text{ mA, duty cycle} \le 1\%$	) –	0.7	_	



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