

Product Summary

V_{RRM} (V)	I_o (A)	V_F (MAX) (V) @ +25°C	I_R (MAX) (mA) @ +25°C
60	1	0.68	0.5

Description

The APD160 is a low voltage dual Schottky rectifier suited for switch mode power supplies and other power converters. This device is intended for use in medium voltage operation, and particularly, in high frequency circuits where low switching losses and low noise are required.

The APD160 is available in standard DO-214AC and DO-41 packages.

Applications

- Low Voltage High Frequency Inverters
- DC-DC Converters
- Free Wheeling
- Polarity Protection

Features

- Low Forward Voltage: 0.68V @ +25°C
- Very Small Conduction Losses
- High Surge Capability
- Surge Overload Rating to 35A Peak
- +125°C Operating Junction Temperature
- 1A Total
- Guard-Ring for Stress Protection
- Pb-free Package is Available DO-41
 - **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- Available in "Green" Packages: DO-214AC and DO-41
 - **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
 - **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: DO-214AC, DO-41
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight
 - DO-41 – 0.33Grams (Approximately)
 - DO-214AC – 0.062Grams (Approximately)



DO-41



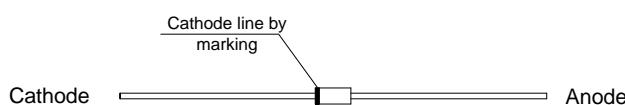
DO-214AC

Notes:

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Assignments

(Top View)



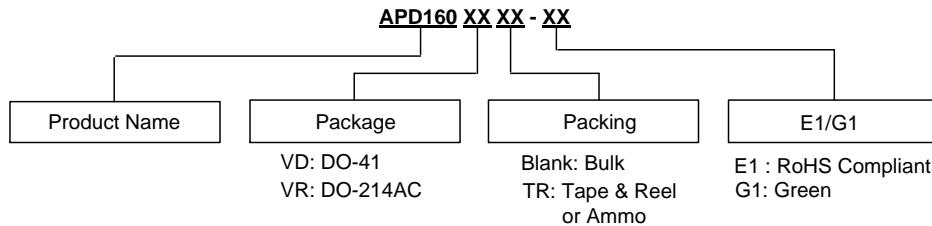
DO-41

(Top View)



DO-214AC

Ordering Information

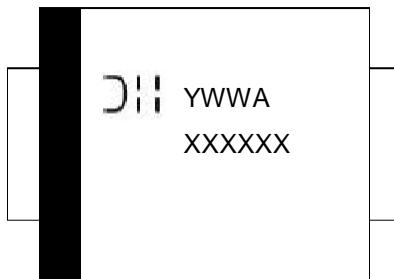


Package	Temperature Range	Part Number	Marking ID	Packing
DO-41	-65 to +125°C	APD160VD-E1	D160VD	1000/Bulk
DO-41	-65 to +125°C	APD160VD-G1	160VDG	1000/Bulk
DO-41	-65 to +125°C	APD160VDTR-E1	D160VD	2500/Ammo
DO-41	-65 to +125°C	APD160VDTR-G1	160VDG	2500/Ammo
DO-214AC	-65 to +125°C	APD160VRTR-G1	160VRG	7500/Tape & Reel

Marking Information

(1) DO-214AC

(Top View)



First Line: Logo and Date Code
Y: Year
WW: Work Week of Molding
A: Assembly House Code
Second Line: Marking ID
(See Ordering Information)

Marking Information (Cont.)

(2) DO-41

(Top View)



First Line: Logo and Date Code

Y: Year

WW: Work Week of Molding

A: Assembly House Code

 Second Line: Marking ID
 (See Ordering Information)

 Maximum Ratings ($T_A = +25^\circ\text{C}$, unless otherwise noted.) (Note 4)

Characteristic	Symbol	Rating	Unit
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	60	V
Maximum DC Blocking Voltage	V_{DC}	60	V
Maximum RMS Voltage	V_{RMS}	42	V
Average Rectified Forward Current 0.375" (9.5mm) Lead Length	$I_{F(AV)}$	1.0	A
Non-repetitive Peak Forward Surge Current 8.3ms Single Half Sine-wave on Rated Load	I_{FSM}	35	A
Operating Junction Temperature Range (Note 5)	T_J	-65 to +125	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-65 to +150	$^\circ\text{C}$

Notes:

4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.
5. The heat generated must be less than the thermal conductivity from Junction to Ambient: $dP_D/dT_J < 1/\theta_{JA}$.

Thermal Characteristics ($T_A = +25^\circ\text{C}$, unless otherwise noted.)

Characteristic	Symbol	Rating		Unit
Typical Thermal Resistance (Note 6)	$R_{\theta JA}$	DO-41	80	$^\circ\text{C}/\text{W}$
		DO-214AC	100	

Note 6: Device mounted on heat sink, with minimum recommended pad layout per <http://www.diodes.com>

Electrical Characteristics ($T_A = +25^\circ\text{C}$, unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage @ $I_F = 1.0\text{A}$	V_F	—	0.68	—	V	—
Reverse Current @ Rated V_R (Note 7)	I_R	—	0.5	—	mA	$T_A = +25^\circ\text{C}$
		—	10	—		$T_A = +100^\circ\text{C}$

Note 7: Short duration pulse test used to minimize self-heating effect, Pulse Test: 300 μs pulse width, 1.0% duty cycle.

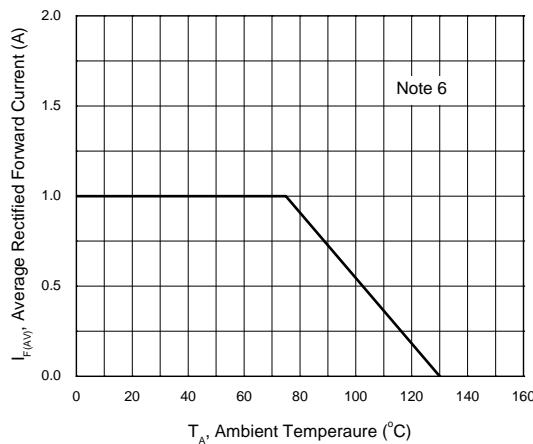


Figure 1. Forward Current Derating Curve

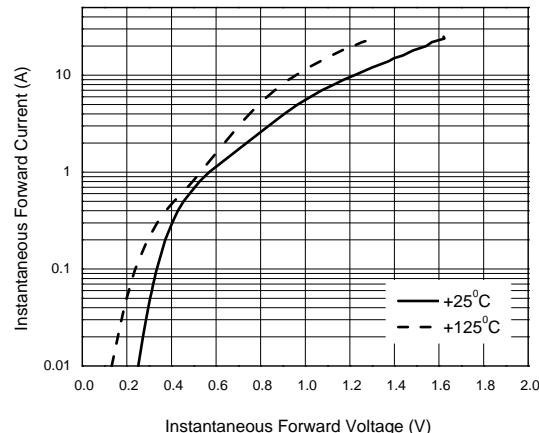


Figure 2. Typical Instantaneous Forward Characteristics

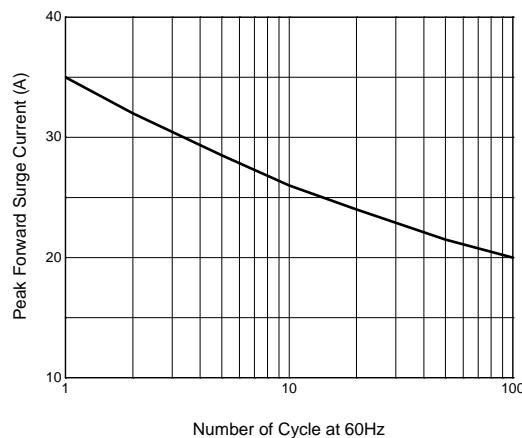


Figure 3. Maximum Non-Repetitive Surge Current

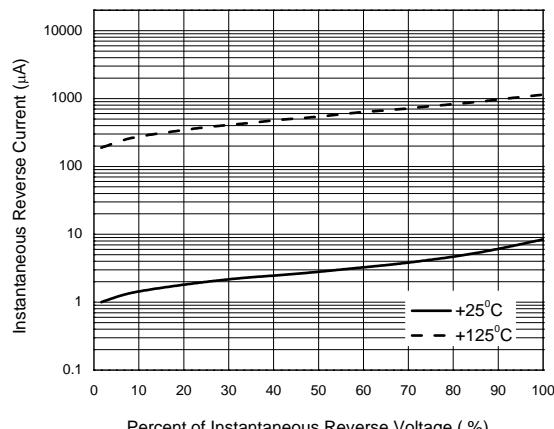


Figure 4. Typical Reverse Characteristics

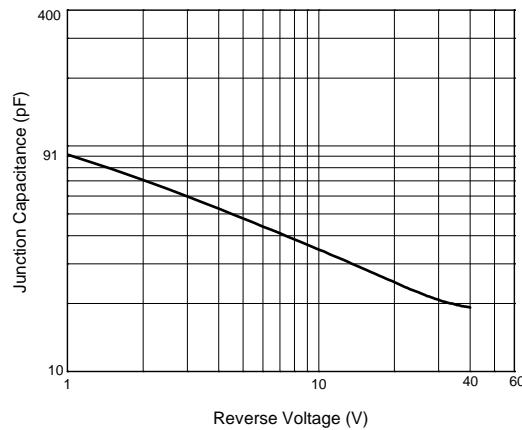
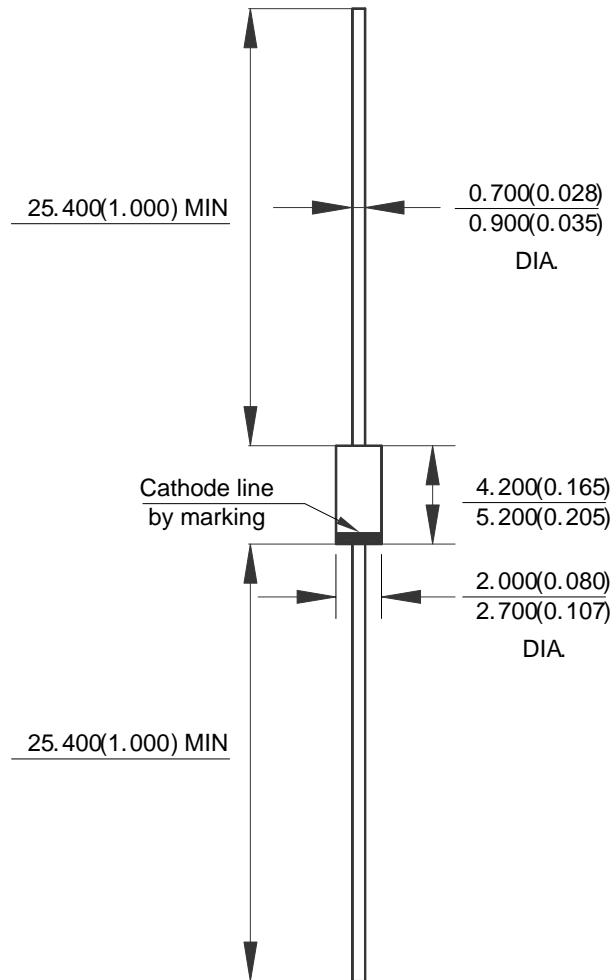


Figure 5. Typical Junction Capacitance

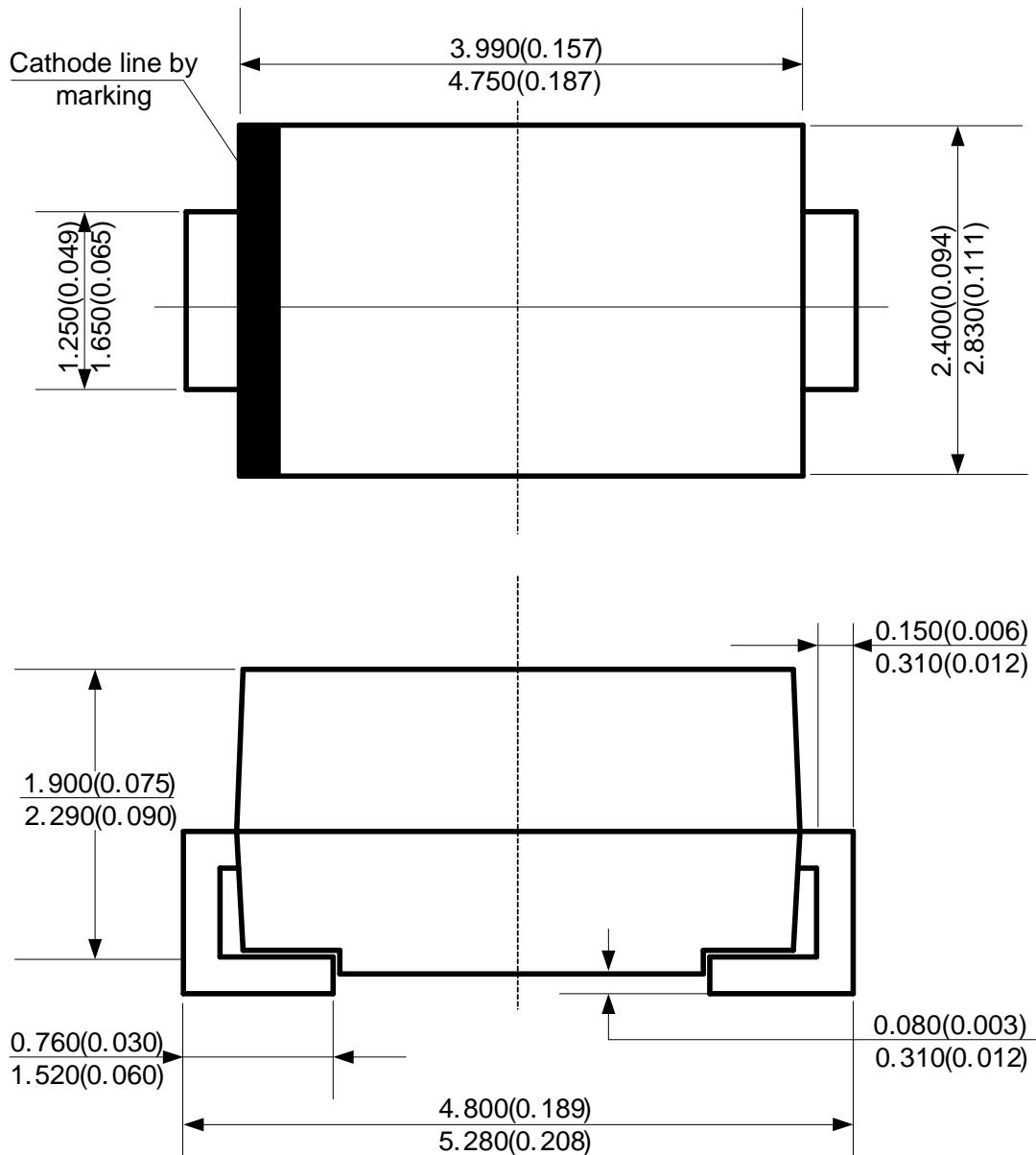
Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: DO-41



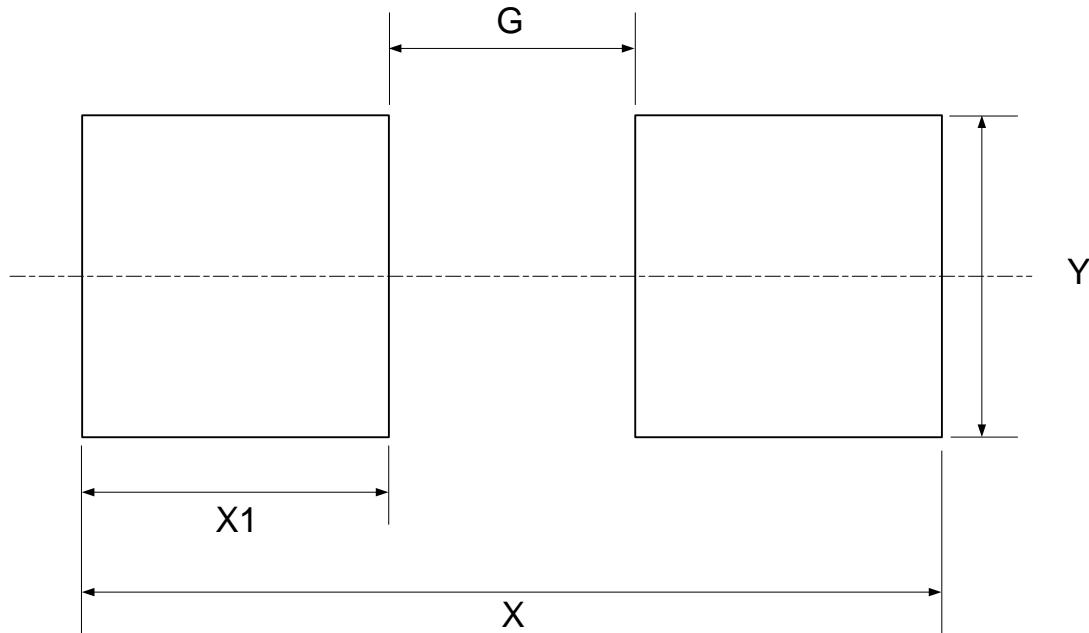
Package Outline Dimensions (Cont. All dimensions in mm(inch).)

(2) Package Type: DO-214AC



Suggested Pad Layout

(1) Package Type: DO-214AC



Dimensions	Y (mm)/(inch)	X1 (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)
Value	2.100/0.083	2.000/0.079	1.600/0.063	5.600/0.220

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