



STB75NH02L

N-Channel 24V - 0.0062Ω - 60A - D²PAK
STripFET™ III Power MOSFET

General features

Type	V _{DSS}	R _{DS(on)}	I _D
STB75NH02L	24V	<0.008Ω	60A ⁽¹⁾

1. Limited by package

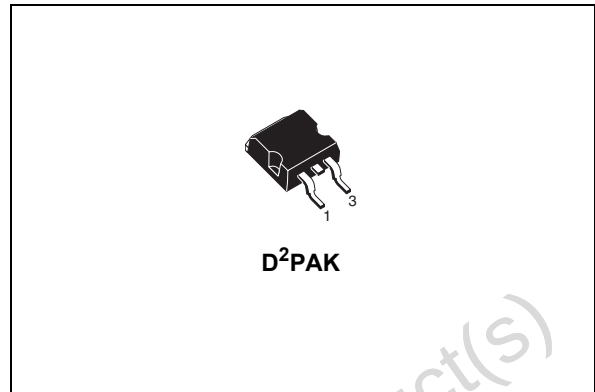
- R_{DS(ON)} * Qg industry's benchmark
- Conduction losses reduced
- Switching losses reduced
- Low threshold device

Description

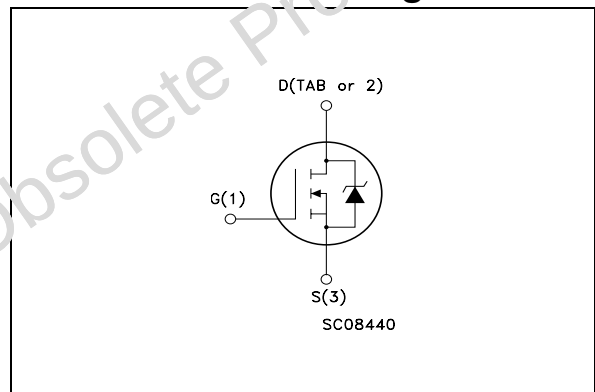
The device utilizes the latest advanced design rules of ST's proprietary STripFET™ technology. This is suitable for the most demanding DC-DC converter application where high efficiency is to be achieved.

Applications

- Switching application



Internal schematic diagram



Order codes

Part number	Marking	Package	Packaging
STB75NH02LT4	B75NH02L	D ² PAK	Tape & Reel

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1 Electrical ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
$V_{\text{spike}}^{(1)}$	Drain-source voltage rating	30	V
V_{DS}	Drain-source voltage ($V_{\text{GS}} = 0$)	24	V
V_{GS}	Gate-source voltage	± 20	V
$I_{\text{D}}^{(2)}$	Drain current (continuous) at $T_{\text{C}} = 25^{\circ}\text{C}$	60	A
I_{D}	Drain current (continuous) at $T_{\text{C}} = 100^{\circ}\text{C}$	53	A
$I_{\text{DM}}^{(3)}$	Drain current (pulsed)	240	A
$P_{\text{TOT}}^{(4)}$	Total dissipation at $T_{\text{C}} = 25^{\circ}\text{C}$	80	W
	Derating factor	0.53	W/ $^{\circ}\text{C}$
$E_{\text{AS}}^{(5)}$	Single pulse avalanche energy	360	mJ
T_{J} T_{stg}	Operating junction temperature storage temperature	-55 to 175	$^{\circ}\text{C}$

1. Guaranteed when external $R_{\text{g}} = 4.7\Omega$ and $t_{\text{f}} < t_{\text{fmax}}$
2. Value limited by wire bonding
3. Pulse width limited by safe operating area
4. This value is rated according to R_{thJC}
5. Starting $T_{\text{J}} = 25^{\circ}\text{C}$, $I_{\text{d}} = 30\text{A}$, $V_{\text{dd}} = 15\text{V}$

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R_{thJC}	Thermal resistance junction-case max	1.88	$^{\circ}\text{C}/\text{W}$
R_{thJA}	Thermal resistance junction-amb max	62.5	$^{\circ}\text{C}/\text{W}$
T_{I}	Maximum lead temperature for soldering purpose	300	$^{\circ}\text{C}$

2 Electrical characteristics

(T_{CASE} = 25 °C unless otherwise specified)

Table 3. On/off states

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	I _D = 25 mA, V _{GS} = 0	24			V
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} = 20 V, V _{DS} = 20 V, T _C = 125°C			1 10	μA μA
I _{GSS}	Gate body leakage current (V _{DS} = 0)	V _{GS} = ±20V			±100	nA
V _{GS(th)}	Gate threshold voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	1.8		V
R _{DS(on)}	Static drain-source on resistance	V _{GS} = 10 V, I _D = 30 A V _{GS} = 5 V, I _D = 30 A		0.0062 0.008	0.008 0.014	Ω Ω

Table 4. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
g _{fs} ⁽¹⁾	Forward Transconductance	V _{DS} = 10V, I _D = 18A		27		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{DS} = 15V, f = 1 MHz, V _{GS} = 0		2050 545 70		pF pF pF
Q _g Q _{gs} Q _{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	V _{DD} = 10V, I _D = 60 A V _{GS} = 5V Figure 14		17 7.7 3.5	22	nC nC nC
R _G	Gate Input Resistance	f = 1MHz Gate DC Bias = 0 Test Signal Level = 20mV Open Drain		1.1		Ω

1. Pulsed: pulse duration = 300μs, duty cycle 1.5%

Table 5. Switching times

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
t _{d(on)} t _r	Turn-on delay time rise time	V _{DD} = 10 V, I _D = 30 A, R _G = 4.7Ω, V _{GS} = 10V Figure 15		12 200		ns ns
t _{d(off)} t _f	Turn-off delay time fall time	V _{DD} = 10 V, I _D = 30 A, R _G = 4.7Ω, V _{GS} = 10V Figure 15		18 25		ns ns

Table 6. Source drain diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain current				60	A
I_{SDM}	Source-drain current (pulsed)				240	A
$V_{SD}^{(1)}$	Forward on Voltage	$I_{SD} = 30\text{ A}$, $V_{GS} = 0$			1.3	V
t_{rr}	Reverse recovery time	$I_{SD}=60\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, $V_{DD}=15\text{ V}$, $T_J=150^\circ\text{C}$		36		ns
Q_{rr}	Reverse recovery charge			35		μC
I_{RRM}	Reverse recovery current			3.6		A

1. Pulsed: pulse duration = 300 μs , duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 1. Safe operating area

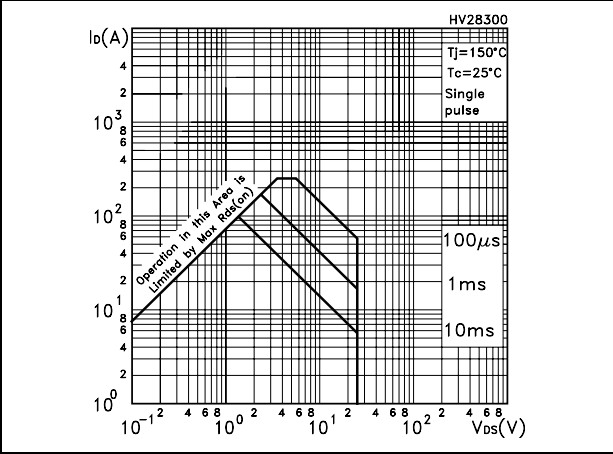


Figure 3. Output characteristics

Figure 2. Thermal impedance

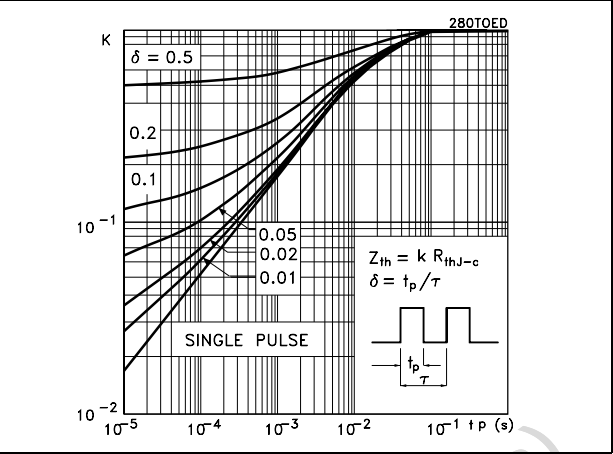


Figure 5. Transfer characteristics

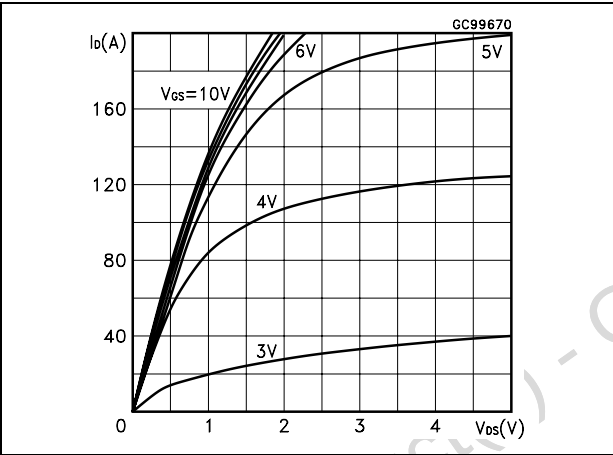


Figure 4. Transconductance

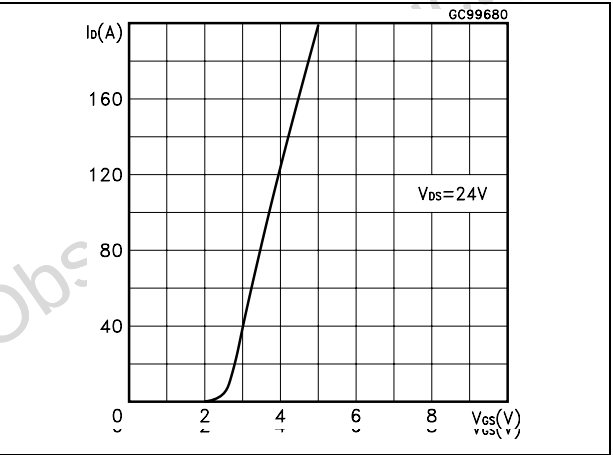


Figure 6. Static drain-source on resistance

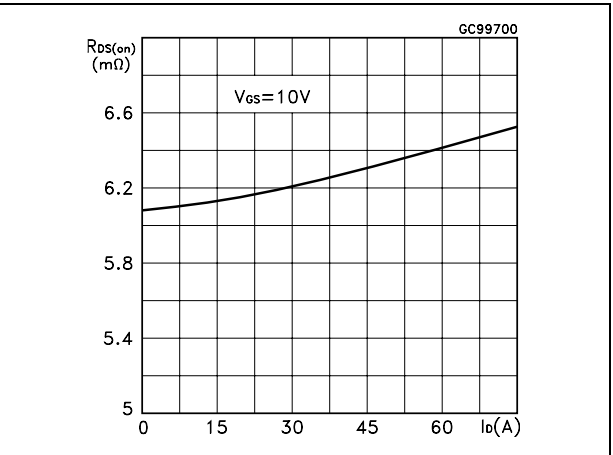
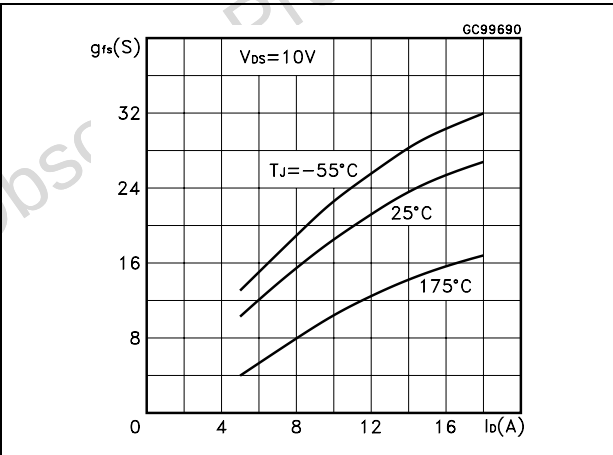


Figure 7. Gate charge vs gate -source voltage Figure 10. Capacitance variations

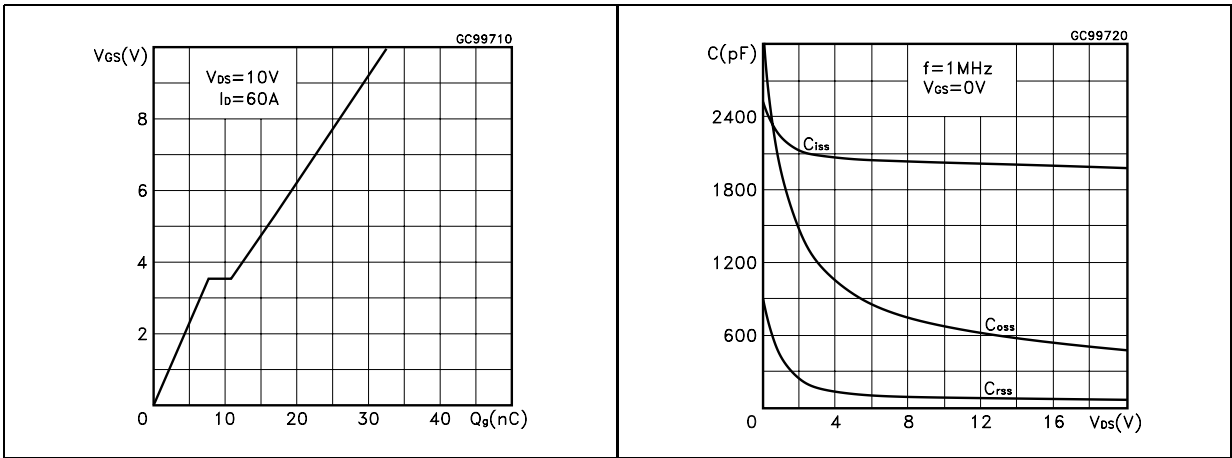


Figure 8. Normalized gate threshold voltage vs temperature Figure 11. Normalized on resistance vs Temperature

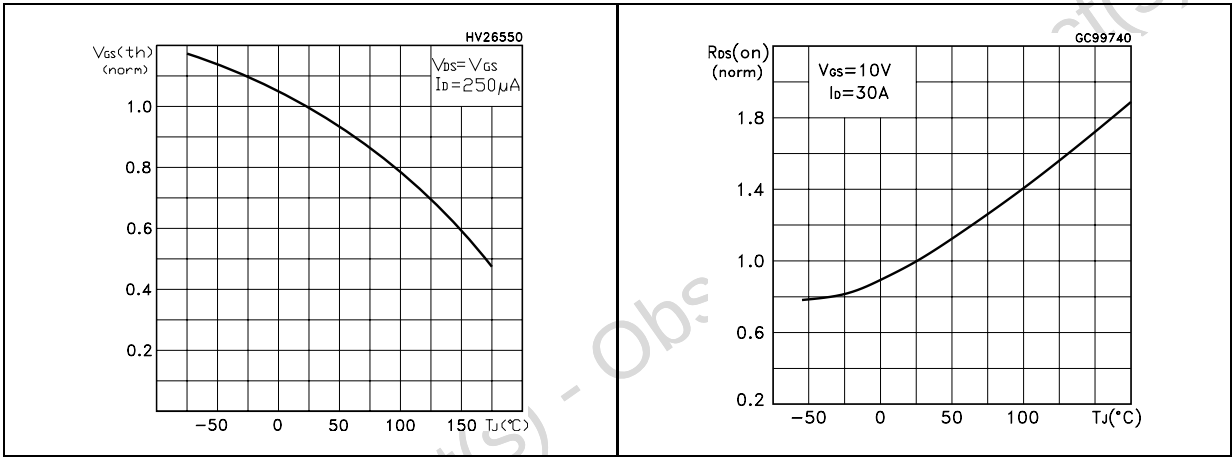
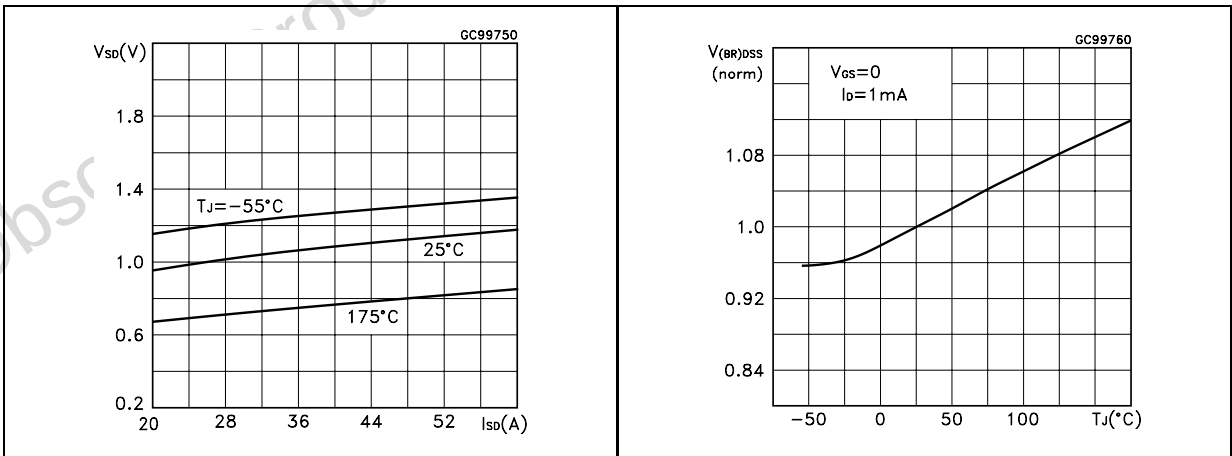


Figure 9. Source-drain diode forward characteristics Figure 12. Normalized BVDSS vs temperature



3 Test circuits

Figure 13. Switching times test circuit for resistive load

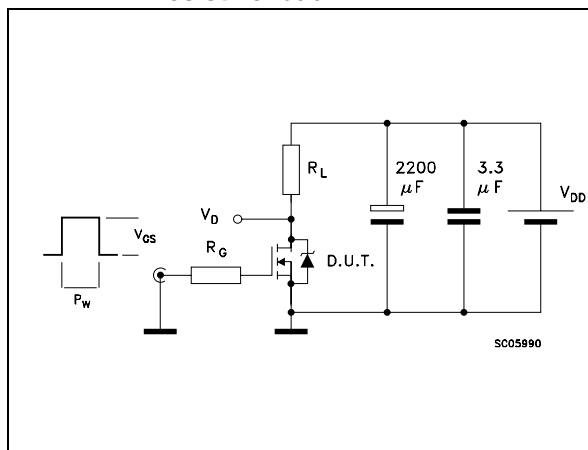


Figure 14. Gate charge test circuit

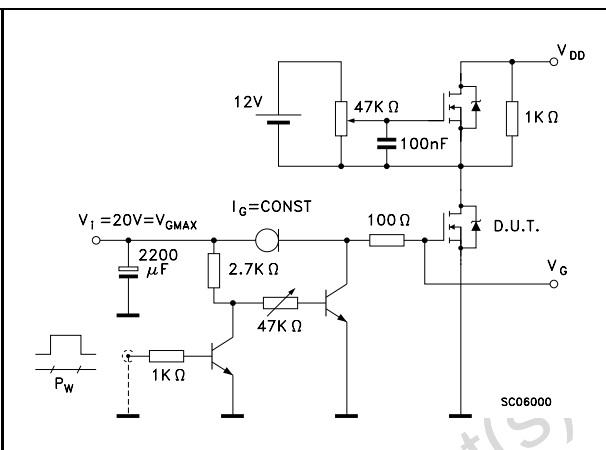


Figure 15. Test circuit for inductive load switching and diode recovery times

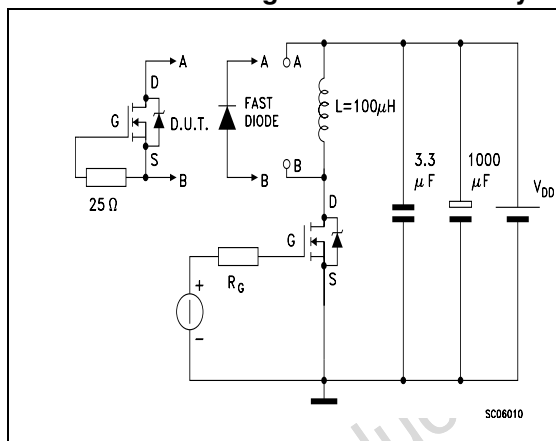


Figure 17. Unclamped inductive load test circuit

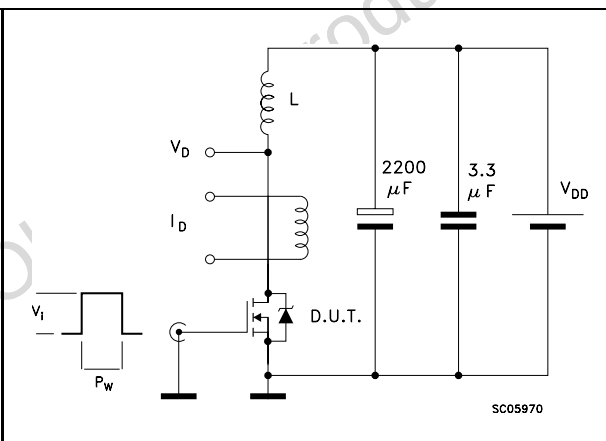
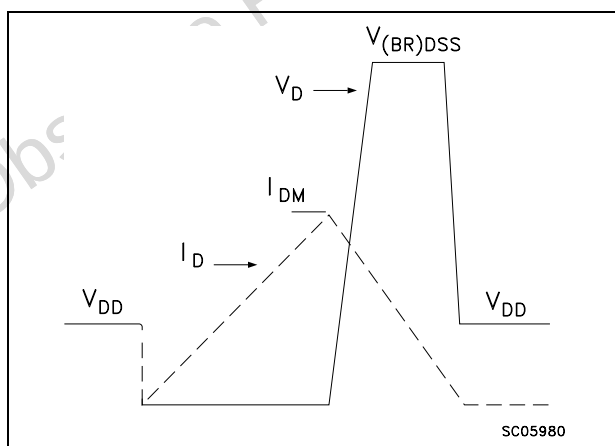


Figure 16. Unclamped inductive waveform



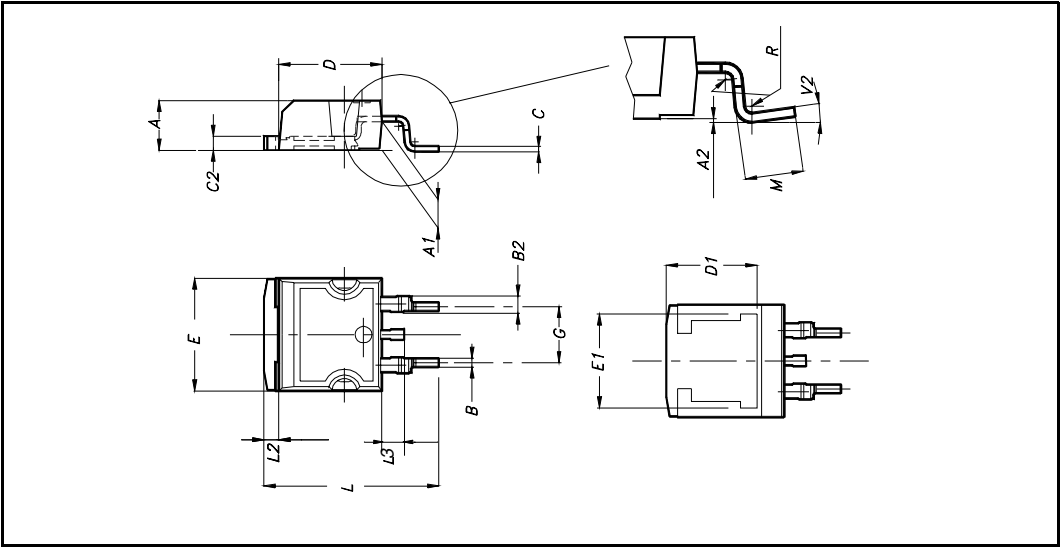
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

Obsolete Product(s) - Obsolete Product(s)

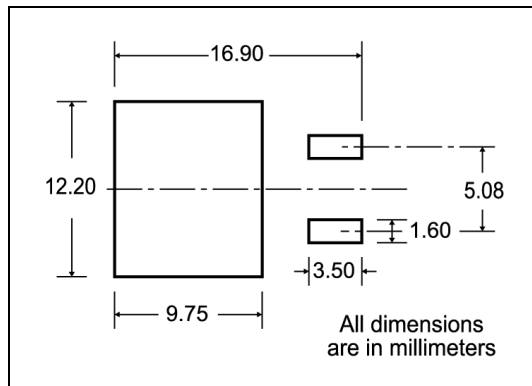
D²PAK MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
B	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
C	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
M	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0°		4°			

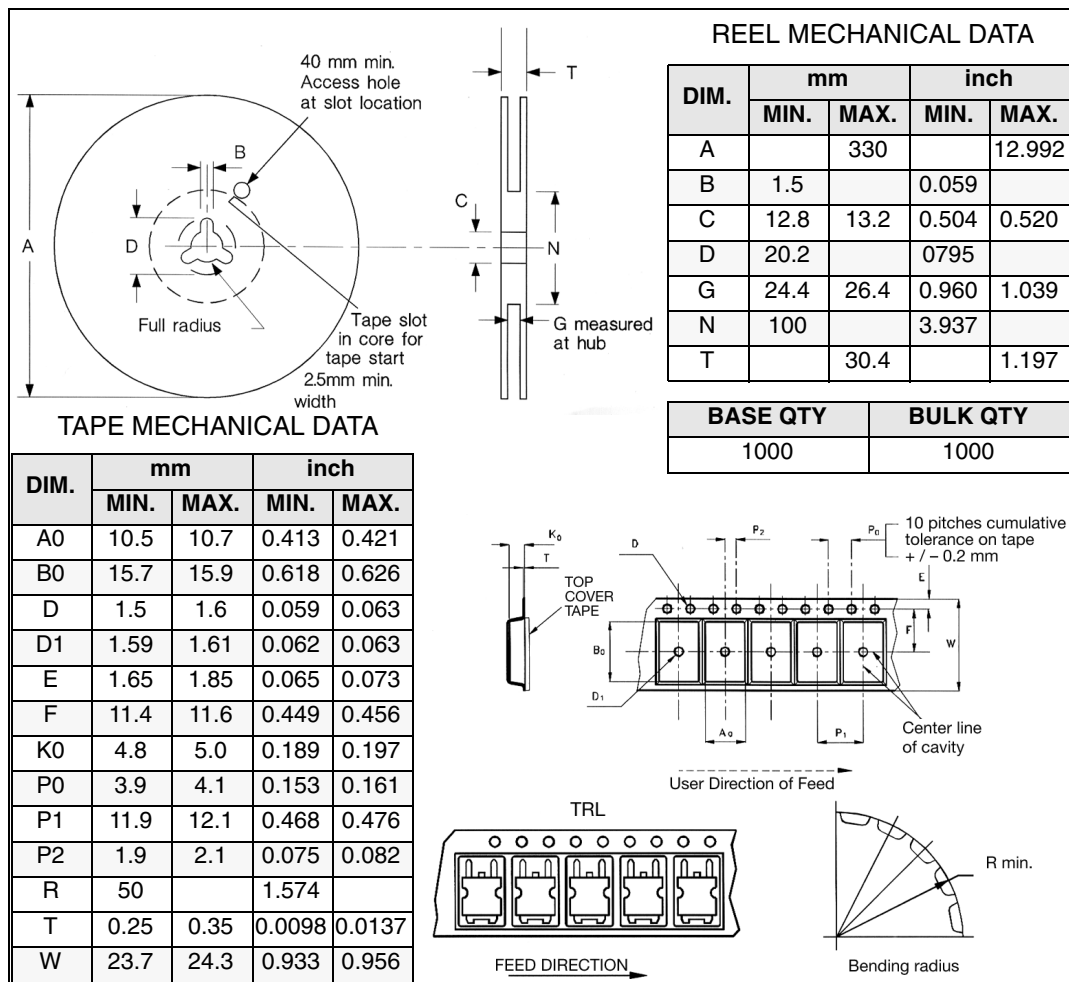


5 Packing mechanical data

D²PAK FOOTPRINT



TAPE AND REEL SHIPMENT



* on sales type

6 Revision history

Table 7. Revision history

Date	Revision	Changes
24-Oct-2005	1	Preliminary version
15-Mar-2006	2	Complete version
22-Jul-2006	3	New template, no content change

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