

Products > ADC1413D105W1 Demo board With FPGA

## ADC1413D105W1 Obsolete

### ADC1413D105W1 Demo board With FPGA

IDT's ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204A feature set.

#### Features

- Single power supply (on-board regulators)
- On-board Virtex 5 FPGA for data-acquisition
- SMA connector for clock and analog input signals
- On-board Data clock generator for ADC and FPGA

#### Product Options

| Orderable Part ID | Part Status | Pkg. Code | Pkg. Type | Lead Count (#) | Temp. Grade | Pb (Lead) Free | Buy Sample                   |
|-------------------|-------------|-----------|-----------|----------------|-------------|----------------|------------------------------|
| ADC1413D105W1-DB  | Obsolete    | BD0       | BOARD     | 0              | I           | No             | <a href="#">Availability</a> |

#### Related Products

| Product ID                    | Title ▼                                     |
|-------------------------------|---------------------------------------------|
| <a href="#">ADC1413D105HN</a> | Dual 14 bits ADC; 105 Msps; serial JESD204A |

#### Quick Links

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#### Images

ADC1413D105W1 - Evaluation Board

