

TOSHIBA High Efficiency Rectifier (HED) Silicon Epitaxial Junction Type

1DL41A

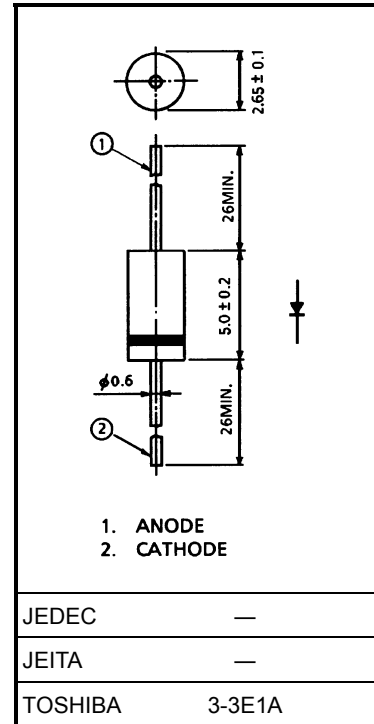
Switching Mode Power Supply Applications

Unit: mm

- Repetitive Peak Reverse Voltage: $V_{RRM} = 200\text{ V}$
- Average Forward Current: $I_F (AV) = 1.0\text{ A}$ ($T_a = 64^\circ\text{C}$)
- Very Fast Reverse-Recovery Time: $t_{rr} = 35\text{ ns (max)}$
- Low Forward Voltage: $V_{FM} = 0.98\text{ V (max)}$
- Available to Reduce Switching Losses and Output Noise

Maximum Ratings ($T_a = 25^\circ\text{C}$)

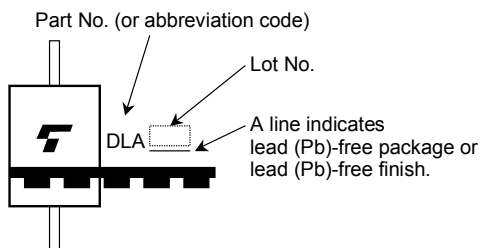
Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	V_{RRM}	200	V
Average forward current ($T_a = 64^\circ\text{C}$)	$I_F (AV)$	1.0	A
Peak one cycle surge forward current (non-repetitive)	I_{FSM}	30 (50 Hz)	A
		33 (60 Hz)	
Junction temperature	T_j	-40 to 150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-40 to 150	$^\circ\text{C}$

Electrical Characteristics ($T_a = 25^\circ\text{C}$)

Weight: 0.225 g (typ.)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Peak forward voltage	V_{FM}	$I_{FM} = 1.0\text{ A}$	—	—	0.98	V
Repetitive peak reverse current	I_{RRM}	$V_{RRM} = 200\text{ V}$	—	—	100	μA
Reverse recovery time	t_{rr}	$I_F = 1\text{ A}$, $di/dt = -30\text{ A}/\mu\text{s}$	—	—	35	ns
Forward recovery time	t_{fr}	$I_F = 1.0\text{ A}$	—	—	100	ns
Thermal resistance	$R_{th(j-a)}$	Junction to Ambient	—	—	115	$^\circ\text{C}/\text{W}$
Thermal resistance	$R_{th(j-l)}$	Junction to Lead	—	—	45	$^\circ\text{C}/\text{W}$

Marking



Abbreviation Code	Part No.
DLA	1DL41A

Handling Precaution

The maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the maximum rating of VRRM for a DC circuit and be no greater than 50% of that of VRRM for an AC circuit. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

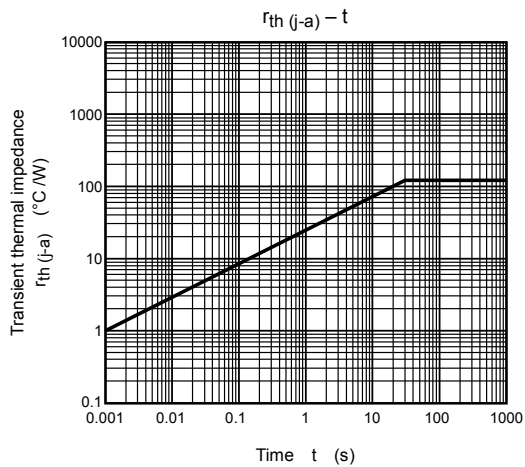
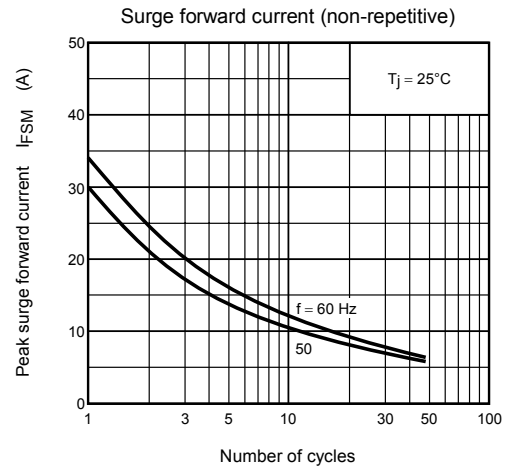
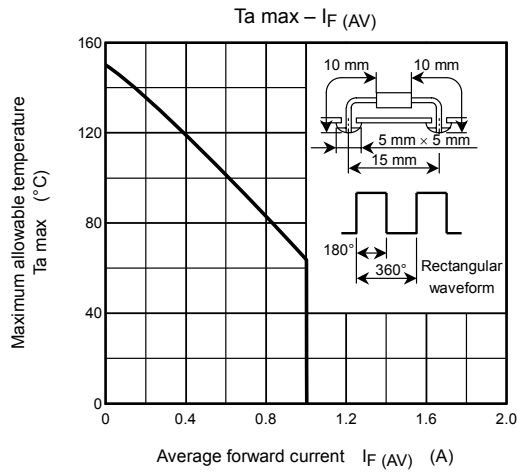
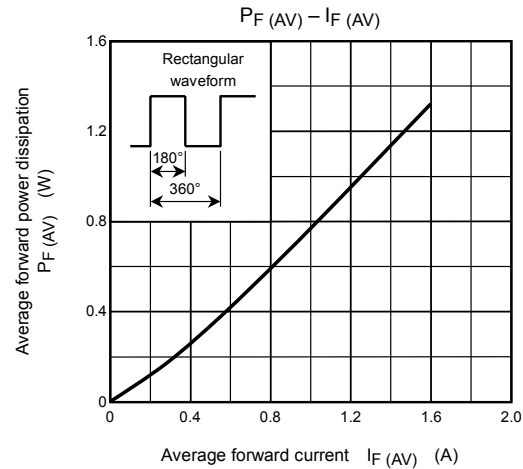
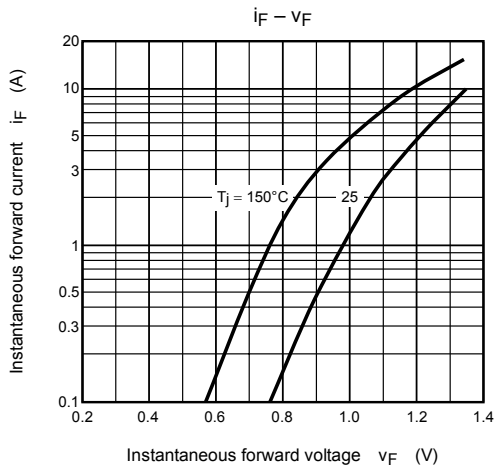
IF(AV): We recommend that the worst case current be no greater than 80% of the maximum rating of IF(AV). Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Tamax-IF(AV) curve.

This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle 180. Therefore, this is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

We recommend that a device be used at a Tj of below 120°C under the worst load and heat radiation conditions.

Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

Please refer to the Rectifiers databook for further information.



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