

APPROVAL SHEET

WF25P, WF20P, WF12P

$\pm 1\%$, $\pm 5\%$ $1\Omega \sim 1M\Omega$

Thick Film High Power Chip Resistors

Size 2512 2W, 2010 1W, 1206 1/2W

*Contents in this sheet are subject to change without prior notice.

FEATURE

1. High power rating and compact size
2. High reliability and stability
3. Reduced size of final equipment
4. RoHS compliant and Lead free products
5. Flame Retardant

APPLICATION

- Power supply
- PDA
- Digital meter
- Computer
- Automotives
- Battery charger
- DC-DC power converter

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

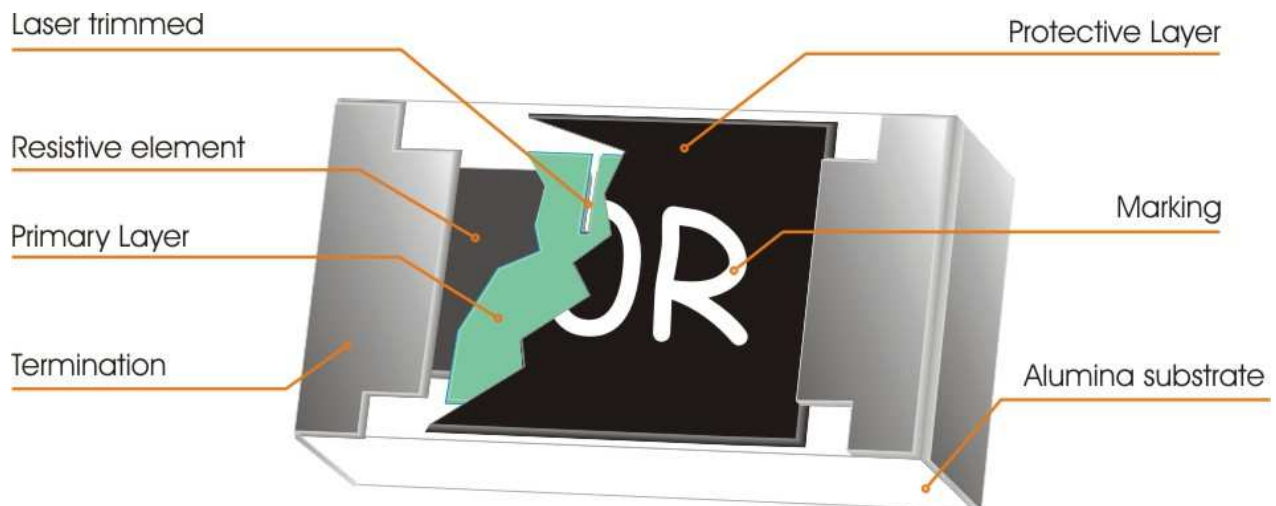


Fig 1. Construction of Chip-R

QUICK REFERENCE DATA

Item	General Specification		
Series No.	WF25P	WF20P	WF12P
Size code	2512 (6432)	2010 (5025)	1206 (3216)
Resistance Tolerance	$\pm 1\%$, $\pm 5\%$		
Resistance Range	0 Ω , 1 Ω ~ 1M Ω		
TCR (ppm/ $^{\circ}$ C)	± 100		
Max. dissipation at T _{amb} =70 $^{\circ}$ C	2 W	1 W	1/2 W
Max. Operation Voltage (DC or RMS)	300V	200V	200V
Max. Overload Voltage (DC or RMS)	500V	400V	400V
Climatic category (IEC 60068)	55/155/56		

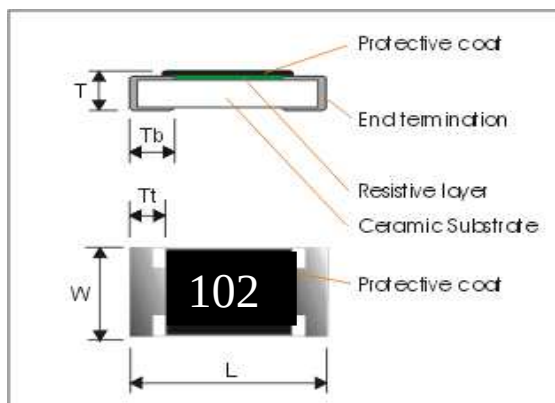
Note :

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}}$$
or Max. RCWV listed above, whichever is lower.
- 2W loading with total solder-pad and trace size of 300 mm²
- 0 Ω maximum resistance R_{max} < 20m Ω

TEST CONDITION FOR JUMPER (0 Ω)

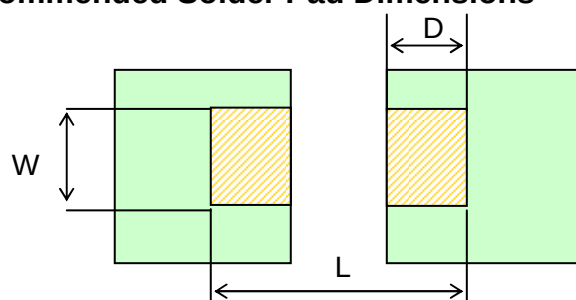
Item	WF25P	WF20P	WF12P
Power Rating At 70 $^{\circ}$ C	2W	1W	1/2W
Resistance	Max. 20m Ω		
Rated Current	10A	7A	5A
Peak Current	25A	17.5A	12.5A
Operating Temperature	-55 ~ +155 $^{\circ}$ C		

MECHANICAL DATA

Unit: mm

Symbol	WF25P	WF20P	WF12P
L	6.30 $\pm$ 0.20	5.00 $\pm$ 0.20	3.10 $\pm$ 0.15
W	3.10 $\pm$ 0.20	2.50 $\pm$ 0.20	1.60 $\pm$ 0.15
T	0.60 $\pm$ 0.15	0.60 $\pm$ 0.10	0.55 $\pm$ 0.10
Tt	0.60 $\pm$ 0.25	0.60 $\pm$ 0.25	0.50 $\pm$ 0.25
Tb	1.80 $\pm$ 0.25	0.60 $\pm$ 0.25	0.50 $\pm$ 0.25

Recommended Solder Pad Dimensions



Type	W	D	L
WF25P	3.7mm	2.45mm	7.6mm

MARKING

Each resistor is marked with a four-digit (WF25P;WF20P,WF12P $\pm 1\%$) and three-digit (WF25P,WF20P,WF12P $\pm 5\%$) code on the protective coating to designate the nominal resistance value.

Example:

$$102 = 1\text{k}\Omega$$



$$16R0 = 16\Omega$$



FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of $\pm 5\%$ & $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

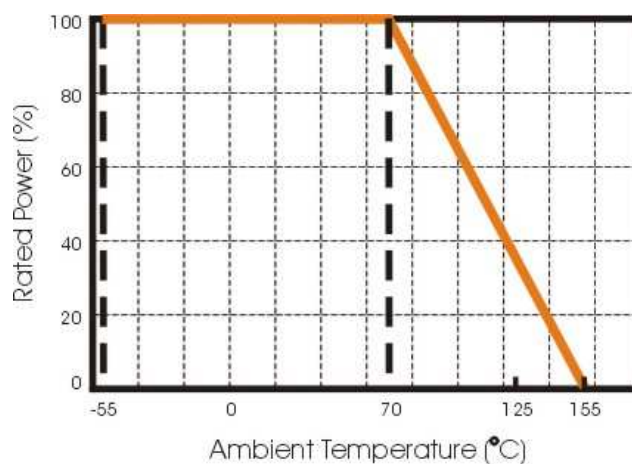


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

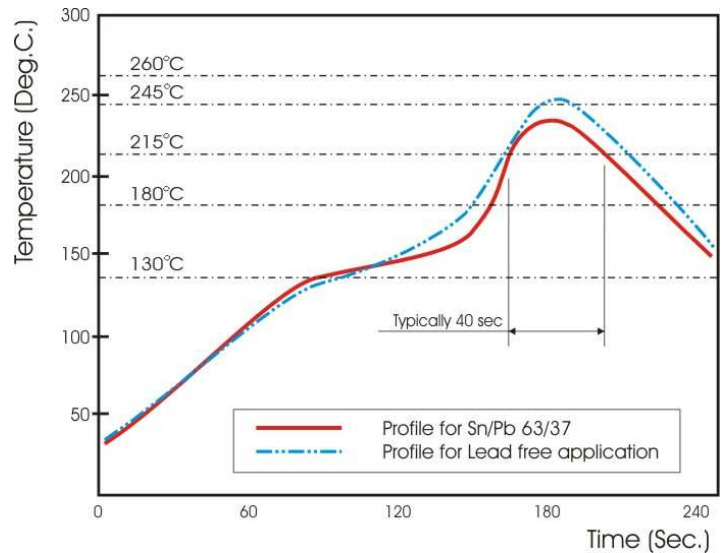


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WF25	P	102	J	T	L
Size code WF25 : 2512 WF20 : 2010 WF12 : 1206	Type code P :Power 2512 size=2 watt 2010 size=1 watt 1206 size=0.5 watt	Resistance code 5% E24: 2 significant digits followed by No. of zeros e.g.: 3ohm =3R0 10ohm =100 56Kohm =563 1% E24+E96: 3 significant digits followed by No. of zeros 100Ω =1000 37.4KΩ =3742	Tolerance J : ±5% F : ±1% P : Jumper	Packaging code T : 7" Reel taping	Termination code L = Sn base (lead free)

Tape packaging WF12 : 8mm width paper taping 5,000pcs per reel.

WF25 ; WF20: 12mm width plastic taping 4,000pcs per reel.

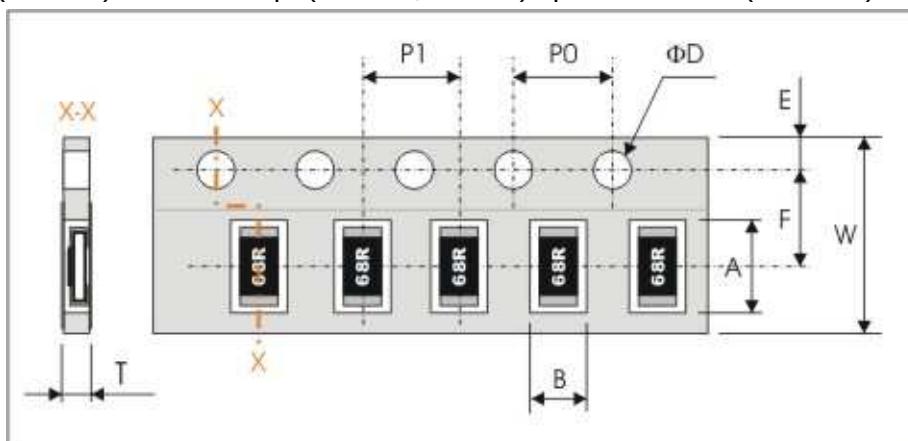
TEST AND REQUIREMENTS

Basic specification : JIS C 5201-1 : 1998

TEST	PROCEDURE	REQUIREMENT
Clause 4.8 Temperature Coefficient of Resistance (TCR)	Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/}^\circ\text{C)}$ R ₁ : Resistance at reference temperature R ₂ : Resistance at test temperature t ₁ : 20°C±5°C-1°C	Refer to quick reference data for T.C.R specification
Clause 4.13 Short time overload	5.0× Rated power or Max. Overload Voltage for 5 sec. Measure resistance after 30 minutes..	ΔR/R max. J: ≤ ±(2%+0.1Ω) F: ≤ ±(1%+0.05Ω)
Clause 4.18 Resistance to soldering heat	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 260°C±5°C	No visible damage ΔR/R max. J: ≤ ±(1%+0.1Ω) F: ≤ ±(0.5%+0.05Ω)
Clause 4.17 Solderability	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at 235°C±5°C	Good tinning (>95% covered) No visible damage
Clause 4.18 Leach Test	Un-mounted chips completely immersed for 60±1second in a solder bath at 260°C±5°C	Ditto
Clause 4.19 Temperature cycling	30 minutes at -55°C±3°C, 2~3 minutes at 20°C±5°C-1° C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C±5°C-1°C, total 5 continuous cycles	No visible damage ΔR/R max. J ≤ ±(1%+0.1Ω) F ≤ ±(0.5%+0.05Ω)
Clause 4.25 Load life (endurance)	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	No visible damage ΔR/R max. J ≤ ±(3%+0.1Ω) F ≤ ±(1%+0.05Ω)
Clause 4.24 Load life in Humidity	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	No visible damage ΔR/R max. J ≤ ±(3%+0.1Ω) F ≤ ±(1%+0.05Ω)
Clause 4.33 Bending strength	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 2 mm(2512;2010) 3mm(1206), once for 10 seconds	No visible damage ΔR/R max. J ≤ ±(1%+0.1Ω) F ≤ ±(0.5%+0.05Ω)
Clause 4.32 Adhesion	Pressurizing force: 5N, Test time: 10±1sec	No remarkable damage or removal of the terminations
Insulation Resistance Clause 4.6	Apply the maximum overload voltage (DC) for 1minute	R ≥ 10GΩ
Dielectric Withstand Voltage Clause 4.7	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover

PACKAGING

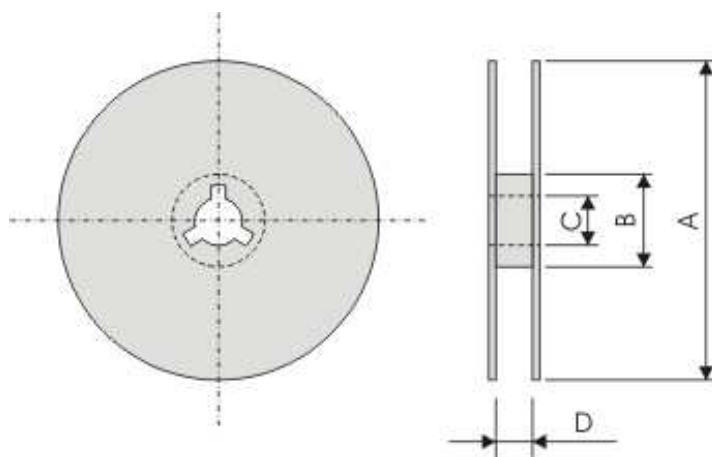
Paper Tape(WF12P) & Plastic Tape(WF25P;WF20P) specifications (unit :mm)



Series No.	A	B	W	F	E
WF25P	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	1.75±0.10
WF20P	5.50±0.20	2.80±0.20			
WF12P	3.60±0.20	2.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10

Series No.	P1	P0	ΦD	T
WF25P	4.00±0.10	4.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	Max. 1.2
WF20P				Max. 1.0
WF12P				

Reel dimensions



(unit : mm)

Reel / Tape	A	B	C	D
7" reel for 12mm tape	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	12.4±1.0
7" reel for 8mm tape				9.0±0.5