



Features

- ✧ UL Recognized File # E-326243
- ✧ For surface mounted application
- ✧ Metal silicon junction, majority carrier conduction
- ✧ Low forward voltage drop
- ✧ Easy pick and place
- ✧ High surge current capability
- ✧ Plastic material used carries Underwriters Laboratory Classification 94V-0
- ✧ Epitaxial construction
- ✧ High temperature soldering: 260°C / 10 seconds at terminals
- ✧ Green compound with suffix "G" on packing code & prefix "G" on datecode.

Mechanical Data

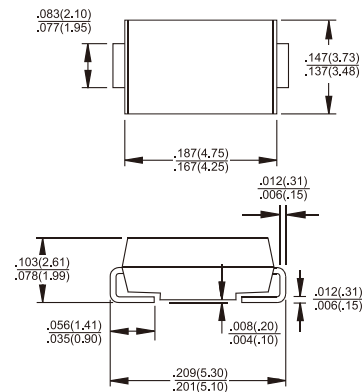
- ✧ Cases: Molded plastic
- ✧ Terminals: Matte tin plating
- ✧ Polarity: Indicated by cathode band
- ✧ Packaging: 12mm tape per EIA STD RS-481
- ✧ Weight: 0.093 gram

SSL22 - SSL24

2.0 AMPS. Surface Mount

Low V_F Schottky Barrier Rectifiers

SMB/DO-214AA



Dimensions in inches and (millimeters)

Marking Diagram



SL2X = Specific Device Code
G = Green Compound
Y = Year
M = Work Month

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	Symbol	SSL22	SSL23	SSL24	Units
Maximum Recurrent Peak Reverse Voltage	VRRM	20	30	40	V
Maximum RMS Voltage	VRMS	14	21	28	V
Maximum DC Blocking Voltage	VDC	20	30	40	V
Maximum Average Forward Rectified Current See Fig. 1	IF(AV)	2.0			A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	IFSM	80			A
Maximum Instantaneous Forward Voltage @ 2.0A	VF	0.41			V
Maximum DC Reverse Current @ TA = 25°C at Rated DC Blocking Voltage @ TA = 100°C (Note 1)	IR	0.4			mA
		50		60	mA
Maximum Thermal Resistance (Note 2)	RθJL RθJA	25			°C /W
		70			
Marking Code		SL22	SL23	SL24	
Operating Temperature Range	TJ	-55 to +125			°C
Storage Temperature Range	TSTG	-55 to + 150			°C

Notes: 1. Pulse Test with PW=300 usec, 1% Duty Cycle.

2. Measured on P.C. Board with 0.4 x .4"(10 x 10mm) Copper Pad Areas.

RATINGS AND CHARACTERISTIC CURVES (SSL22 THRU SSL24)

FIG.1- MAXIMUM FORWARD CURRENT DERATING CURVE

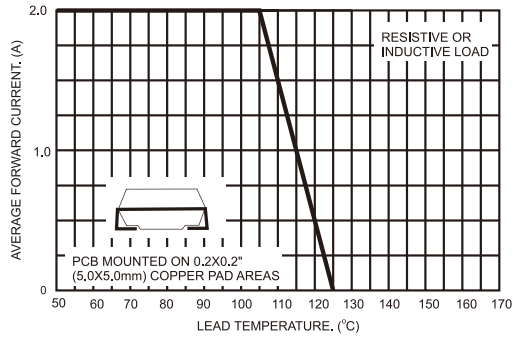


FIG.2- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

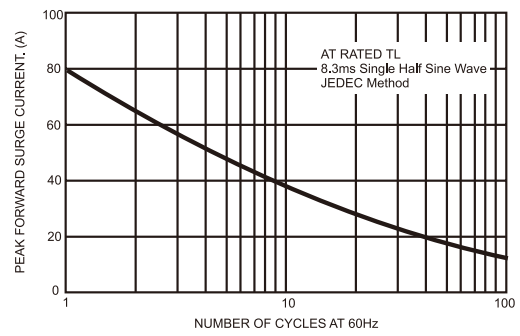


FIG.3- TYPICAL FORWARD CHARACTERISTICS

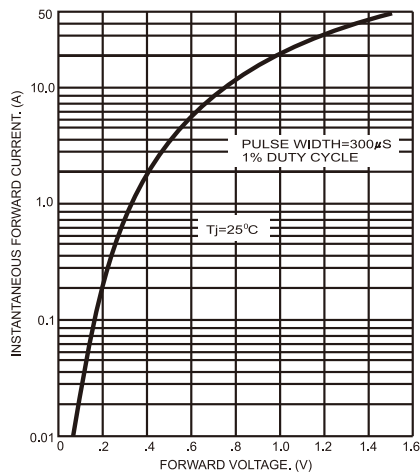


FIG.4- TYPICAL REVERSE CHARACTERISTICS

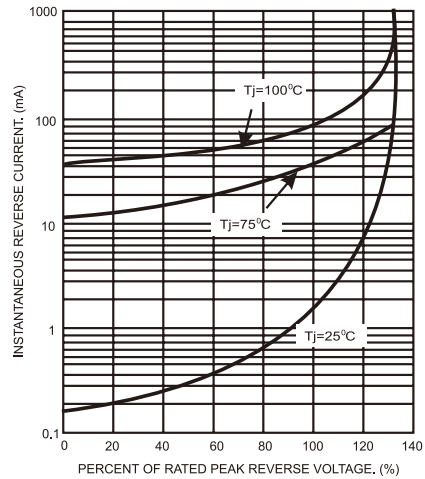
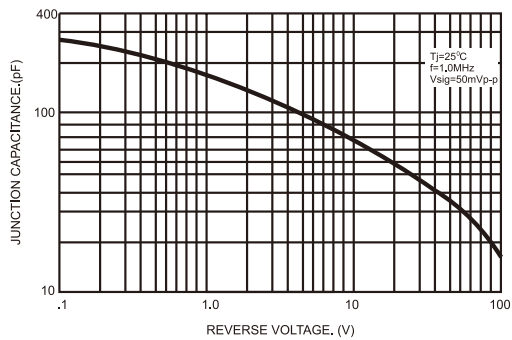


FIG.5- TYPICAL JUNCTION CAPACITANCE



Mouser Electronics

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