

To all our customers

Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

M5295AL/P/FP

WATCHDOG TIMER

DESCRIPTION

M5295A is a semiconductor integrated circuit which is designed for System Reset to detect +5V power supply.

This IC keeps the operation microcomputer watching. When the system is abnormal, it generates Reset output until the system returns to normal states of the System.

It is possible to vary the two detective voltage by connecting the resistor, so it is suitable to high quality and high performance system.

FEATURES

- Watch Dog Timer
- Power on Reset Timer
- Low circuit current 0.8mA(Typ, Vcc=5V)
- Wide Range of power supply

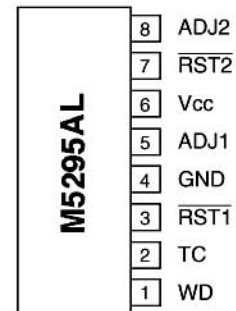
APPLICATION

Microcomputer Systems

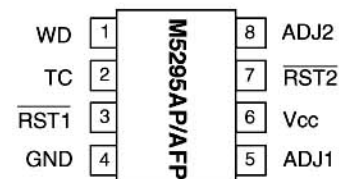
RECOMMENDED OPERATING CONDITIONS

Supply voltage.....4V to 15V
Rated supply voltage.....5V

PIN CONFIGURATION (TOP VIEW)

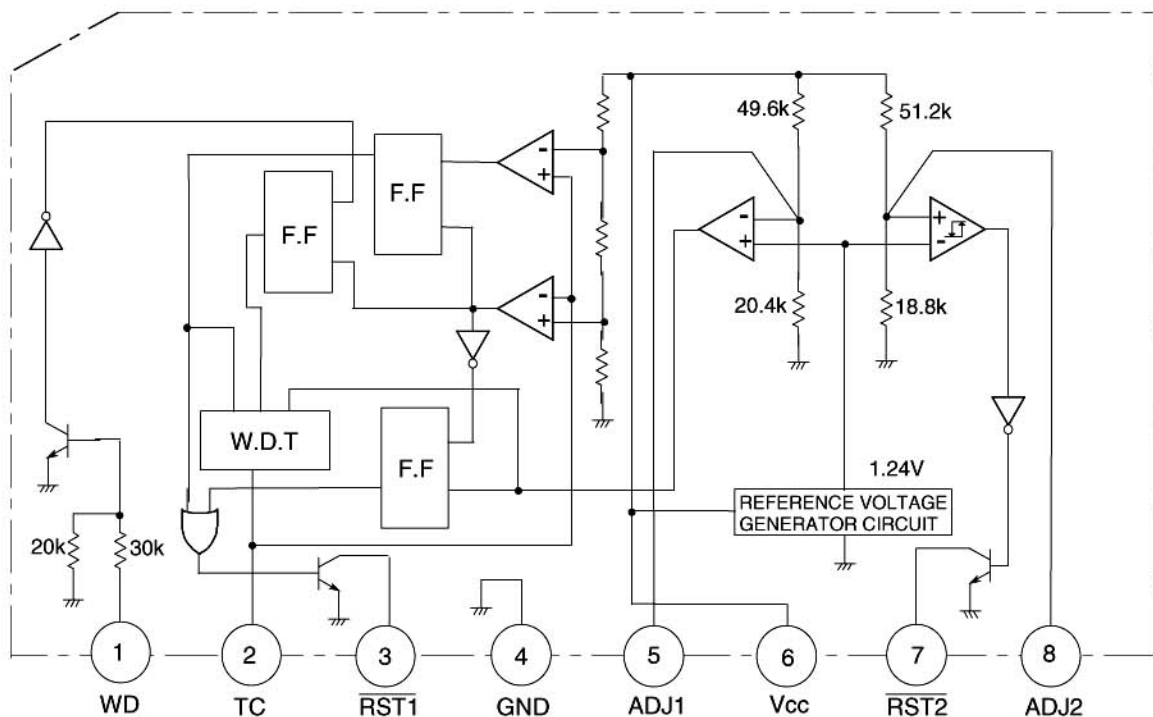


Outline 8P5(AL)



Outline 8P4(AP)
8P2S-A(AFP)

BLOCK DIAGRAM



M5295AL/P/FP

WATCHDOG TIMER

ABSOLUTE MAXIMUM RATINGS (Ta=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
V _{CC}	Supply voltage		15	V
V _{IN}	Input voltage		-10 to +10	V
V _{OUT}	Output voltage		15	V
I _{OUT}	Output current		10	mA
P _d	Power dissipation		800(SIP)/625(DIP)/440(FP)	mW
K _θ	Thermal derating	Ta≥25°C	8(SIP)/6.25(DIP)/4.4(FP)	mW/°C
T _{opr}	Operating temperature		-20 to +75	°C
T _{stg}	Storage temperature		-55 to +125	°C

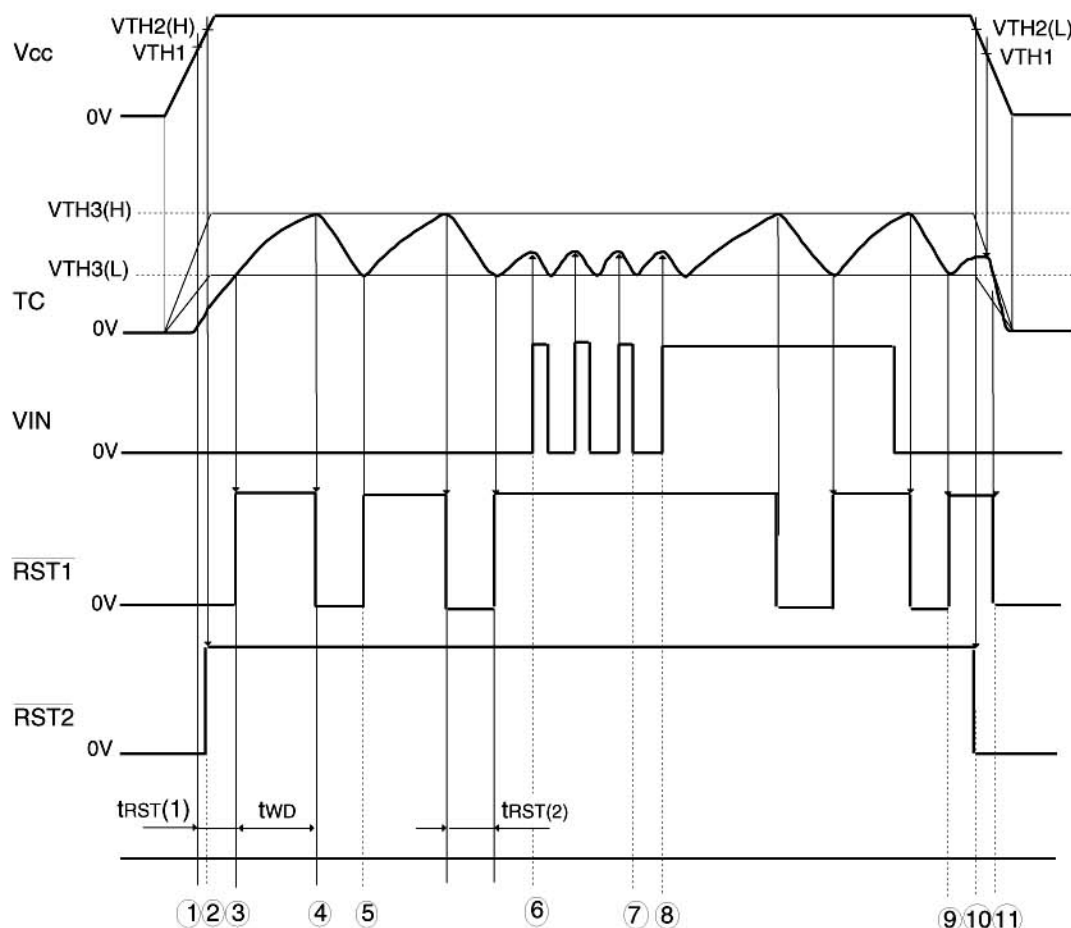
ELECTRICAL CHARACTERISTICS (Ta=25°C, V_{CC}=5V, unless otherwise noted)

(1)DC FEATURES

Symbol	Parameter	Pin	Test conditions	Limits			Unit
				Min.	Typ.	Max.	
I _{IH}	WD input current	WD	V _{IN} =5V	0.06	0.15	0.25	mA
I _{IL}			V _{IN} =-5V	0.05	-0.1	-0.15	mA
V _{IH}	WD input voltage	WD		2			V
V _{IL}						0.8	V
I _{OUT}	TC output current	TC	V _{IN} =1.5V			-1	μA
I _{IN}	TC input current	TC	V _{OUT} =4.2V		3.3		mA
V _{VH3(H)}	Threshold voltage of watch dog timer	TC		3.7	4	4.3	V
V _{VH3(L)}				1.7	2	2.3	V
V _{OL}	Output voltage	$\overline{\text{RST1}}$	I _{OUT} =1mA		0.1	0.5	V
I _{LEAK}	Output leak current	$\overline{\text{RST2}}$	V _{OUT} =15V			5	μA
V _{TH1}	V _{CC} detective voltage(1)	V _{CC}		4.05	4.25	4.45	V
V _{TH2(H)}	V _{CC} detective voltage(2)	V _{CC}		4.5	4.7	4.9	V
V _{TH2(L)}				4.45	4.6	4.75	V
ΔV _{TH2}				0.05	0.1	0.2	V
V ₅	ADJ1 voltage	ADJ1		1.17	1.46	1.75	V
V ₈	ADJ2 voltage	ADJ2		1.07	1.34	1.61	V
$\overline{\text{RST1}}$	$\overline{\text{RST1}}$ on voltage	$\overline{\text{RST1}}$	V _{CC} =1.2V, R _L =4.7k			0.5	V
$\overline{\text{RST2}}$	$\overline{\text{RST2}}$ on voltage	$\overline{\text{RST2}}$	V _{CC} =1.2V, R _L =4.7k			0.5	V
I _{CC}	Circuit current	V _{CC}			0.8	1.5	V

(2)AC FEATURES

Symbol	Parameter	Pin	Test conditions	Limits			Unit
				Min.	Typ.	Max.	
t _{WD}	Watch dog timer	$\overline{\text{RST1}}$			1.1•C•R ₁		s
			C=0.1μF, R ₁ =10kΩ	0.5	1.1	1.7	ms
t _{RST(1)}	Reset timer (1)	$\overline{\text{RST1}}$			0.5•C•R ₁		s
			C=0.1μF, R ₁ =10kΩ	0.2	0.5	1.1	ms
t _{RST(2)}	Reset timer (2)	$\overline{\text{RST1}}$	R ₁ =10kΩ		830•C		s
			C=0.1μF, R ₁ =10kΩ	40	83	220	μs
t _{WD IN}	Input pulse watch	WD		3			μs
t _{d1}	Transmittal delay time	$\overline{\text{RST1}}$			20		μs
t _{d2}		$\overline{\text{RST2}}$			10		μs



- ① The Vcc rises up to 0.8V, then $\overline{\text{Reset1}}$ and 2 generate Low output, and rising up to 4.25V, charge of C1 begins.
- ② The Vcc rises up to 4.7V, then $\overline{\text{Reset2}}$ generates high.
- ③, ④ The voltage at TC pin is 2V, then $\overline{\text{Reset2}}$ generates high, when 4V, C1 is discharged and $\overline{\text{Reset1}}$ generates Low.
- ⑤ The voltage at TC pin falls to 2V, then $\overline{\text{Reset1}}$ generates high unless normal clock signal is entered to WD pin, $\overline{\text{Reset1}}$ repeats this operation.
- ⑥, ⑦ Before the voltage at TC pin reaches 4V, if normal clock signal is entered to WD pin, Low $\overline{\text{Reset1}}$ is canceled.
- ⑧, ⑨ In the case of entrance of abnormal signal input, as the waveform of TC pin repeats charge and discharge of $\overline{\text{Reset1}}$ alternatively from 2V to 4V, the $\overline{\text{Reset1}}$ repeats high and low output operation.
- ⑩ The Vcc falls to 4.6V, then $\overline{\text{Reset2}}$ generates Low, this detective voltage has a 100mV hysteresis.
- ⑪ When Vcc goes down to 4.25V (V_{TH1}), the status of TC pin is switched to discharge. When the potential at TC pin is detected being $V_{TH3}(H)$ or $V_{TH3}(L)$, the status of $\overline{\text{Reset1}}$ becomes "low".

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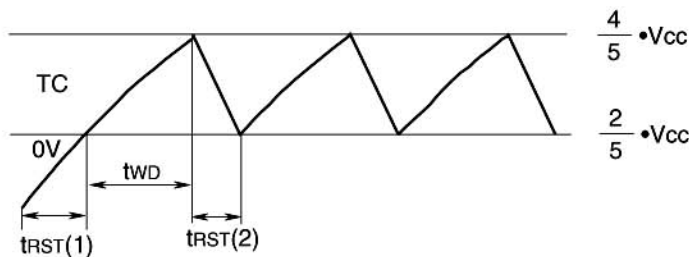
WATCHDOG TIMER

TERMINOLOGY

- t_{RST1}**: Time required for TC pin potential to rise from 0V V_{TH3}(L) when V_{cc} is being applied.
t_{WD}: Time required for TC pin potential to rise from V_{TH3}(L) to V_{TH3}(H).
t_{RST2}: Time required for TC pin potential to go down from V_{TH3}(H) to V_{TH3}(L).

1.Pin ② (TC Pin) Charge Time and Discharge Time

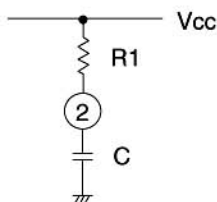
When input to WD pin is abnormal, TC pin output waveform is as shown below:



$$t_{RST(1)} = 0.51 \cdot C \cdot R1$$

$$t_{WD} = 1.1 \cdot C \cdot R1 \text{ (charge time)}$$

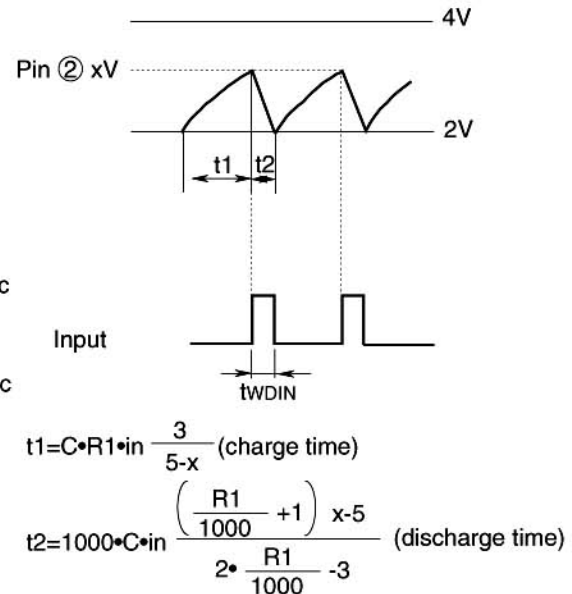
$$t_{RST(2)} = 1000 \cdot C \cdot \ln \frac{4 \cdot \frac{R1}{1000} - 1}{2 \cdot \frac{R1}{1000} - 3} \text{ (discharge time)}$$



Resistance R1: $10k\Omega \leq R1 \leq 30k\Omega$
 When R1 is 10k Ω , t_{RST(2)} is 830•C.

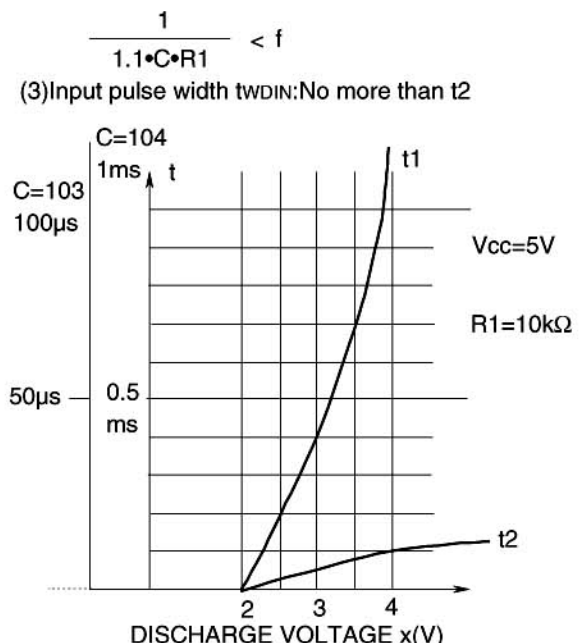
2.Pin ① (WD Pin) Input Frequency, Input Pulse Width, Charge Time and Discharge Time

When input to WD pin ① is normal, TC pin ② output waveform is as shown below: (V_{cc}=5V)



PIN ① (WD PIN) INPUT REQUIREMENTS

- (1) Connect capacitor between WD pin and voltage input. (Refer to Section 3.)
- (2) Input cycle: No more than two
 (Discharge should start before voltage at WD pin reaches 4V.)



3.Relationship between Input Pulse Width and Input Capacitance C_{in}

$$t_3 = C_{in} \cdot 5 \times 10^4 \cdot \ln \frac{V_{in}}{1.5}$$

(t2:Discharge time during normal input)

The circuit diagram shows a differential amplifier implemented with an operational amplifier (op-amp). The op-amp is represented by a triangle with a '+' sign at the non-inverting input and a '-' sign at the inverting input. The circuit includes several resistors and a DC voltage source:

- Resistors:**
 - R_1 and R_2 are connected in series between the input and ground.
 - A $49.6\text{k}\Omega$ resistor is connected between the non-inverting input (+) and the output.
 - A $20.4\text{k}\Omega$ resistor is connected between the inverting input (-) and ground.
 - A load resistor R_L is connected between the output and ground.
- DC Voltage Source:** A 1.24V source is connected to the inverting input (-) through a resistor.
- Nodes:** The circuit is labeled with nodes 3, 4, 5, and 6. Node 5 is at the non-inverting input, node 6 is at the output, node 3 is at the inverting input, and node 4 is at the ground connection of the 1.24V source.

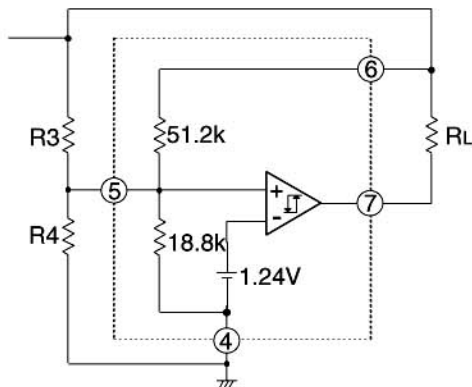
VTH1(V)	R1(kΩ)	R2(kΩ)	Detection voltage calculation formula
13	10	0.92	$V_{TH1} = \frac{R_{01} + R_{02}}{R_{02}} \times 1.24(V)$ $\left(\begin{array}{l} R_{01} = R1 // 49.6k\Omega \\ R_{02} = R2 // 20.4k\Omega \end{array} \right)$
10	10	1.25	
7	10	1.96	
5	10	3.17	
4.25	—	—	
4	10.90	5	
3.5	8.59	5	

$$R_2 = \frac{1}{\frac{1}{R_0} - \frac{1}{20.4k}} \quad \left(R_0 = \frac{8.322k \times 1.24}{V_{TH1} - 1.24} \right)$$
$$R1 = \frac{1}{\frac{1}{R0} - \frac{1}{49.6k}} \quad \left(R0 = \frac{(V_{TH1} - 1.24)4.016k}{1.24} \right)$$

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(2)Detection voltage 2($V_{TH2}(L)$)adjustment



$V_{TH2}(L)(V)$	$R3(k\Omega)$	$R4(k\Omega)$	$\Delta V_{TH2}(mV)$	Detection voltage calculation formula
13	10	0.93	16.3	$V_{TH2}(L) = \frac{R03+R04}{R04} \times 1.24(V)$ $\left(\begin{array}{l} R03=R3//51.2k\Omega \\ R04=R4//18.8k\Omega \end{array} \right)$
10	10	1.26	16.3	
7	10	1.99	16.3	
5	10	3.24	16.3	
4.6	—	—	100	$\Delta V_{TH2} = \frac{R03}{51.2k} \times 100(mV)$
4	10.61	5	17.2	
3.5	8.38	5	14.1	

To adjust detection voltage 2, determine external resistance with the following equations:

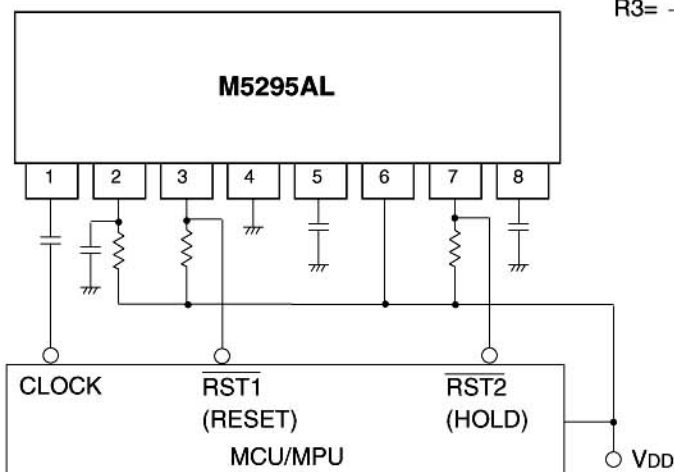
a. $V_{TH2}(L) > 4.6V (R3=10k\Omega)$

$$R4 = \frac{1}{\frac{1}{R0} - \frac{1}{18.8k}} \quad \left(R0 = \frac{8.37k \times 1.24}{V_{TH2}(L) - 1.24} \right)$$

b. $V_{TH2}(L) < 4.6V (R4=5k\Omega)$

$$R3 = \frac{1}{\frac{1}{R0} - \frac{1}{51.2k}} \quad \left(R0 = \frac{(V_{TH2}(L) - 1.24) \times 3.95k}{1.24} \right)$$

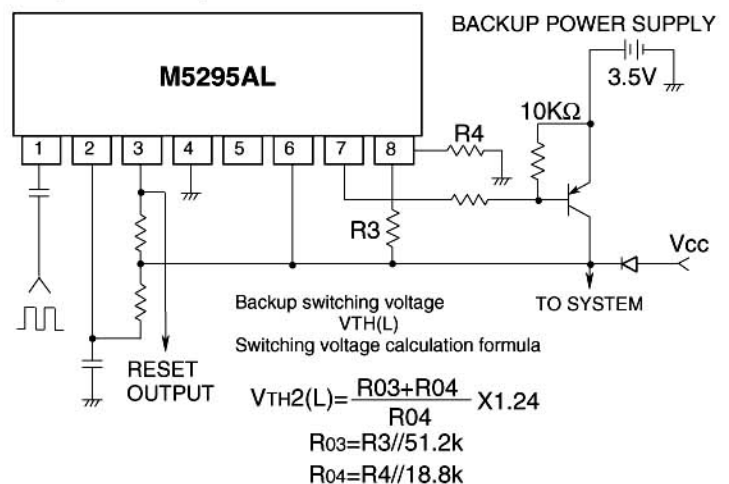
APPLICATION EXAMPLE



OPERATION INSTRUCTIONS

- When malfunction occurs due to noise or other related trouble, connect capacitance of approximately 1000pF between pin ⑤ and GND as well as pin ⑧ and GND to stabilize operation.
- To adjust detection voltage, add resistance of 15k Ω or less to both Vcc and GND via adjusting pins. (Set detection voltage to no less than 3V.)
- Set tWD and tRST(2) as shown below:
 $110\mu s \leq tWD \leq 1.1s$
 $8.3\mu s \leq tRST(2) \leq 83ms$
 $10k\Omega \leq R1 \leq 30k\Omega$
- Input clock pulses to pin ① via capacitor. To determine capacitance, refer to "Relationship between Input Pulse Width and Input Capacitance C_{in}".

Example of Backup Circuit with M5295AL

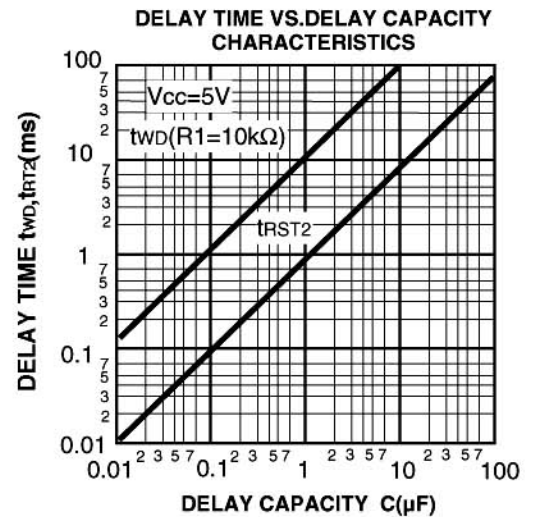
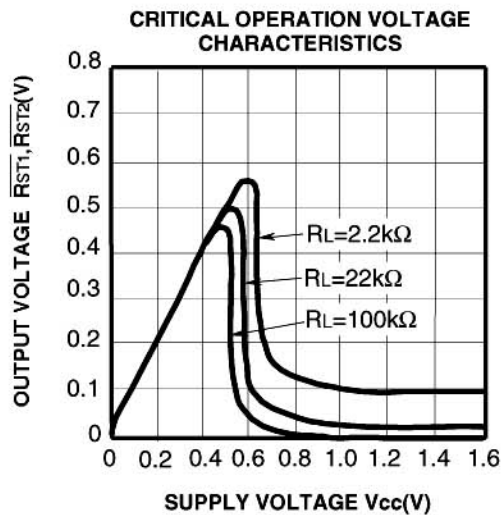
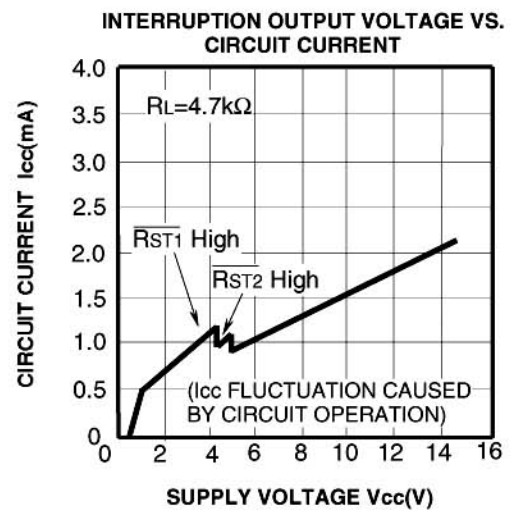
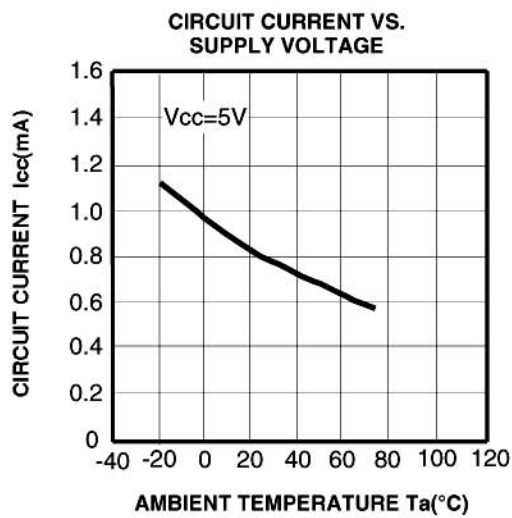
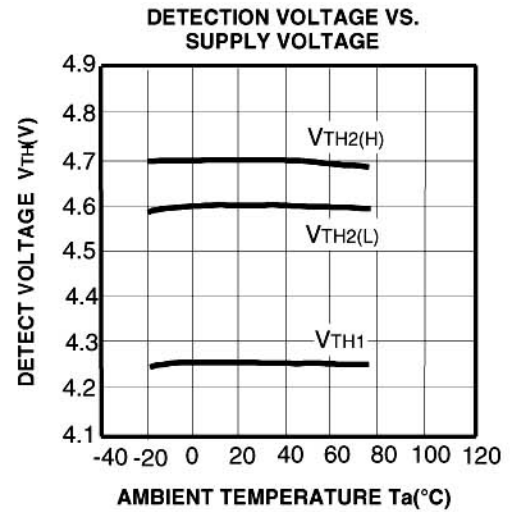
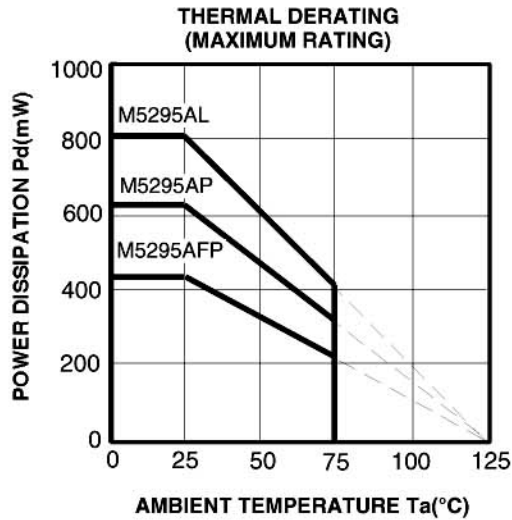


(Note) Set backup switching voltage to be more than or equal to backup supply voltage.

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TYPICAL CHARACTERISTICS



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