

4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Product Summary

V _{BR} (Min)	I _{PP} (Max)	C _T (Typ)
4.5V	45A	2.1pF

Description

The D5V0P4UR6SO is a high-performance device suitable for protecting four high-speed I/Os. These devices are assembled in SOT26 package and have high ESD surge capability and low capacitance.

Applications

Typically used at high-speed ports such as USB 2.0, IEEE1394 (Firewire[®], iLink™), Serial ATA, DVI, HDMI and PCI.





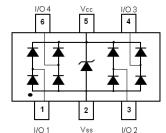
Top View

Features

- Low Clamping Voltage: Typical 7.5V at 12A 100ns, TLP, I/O to Vss; Typical 5.8V at 12A 100ns, TLP, Vcc to Vss
- IEC 61000-4-2 (ESD): Air ±30kV, Contact ±30kV
- IEC 61000-4-4 (EFT): ±80A (5/50ns)
- IEC 61000-4-5 (Lighting): 20A, I/O to V_{SS}; 45A, V_{CC} to V_{SS}
- TLP Dynamic Resistance: 0.15Ω, I/O to V_{SS}; 0.07Ω, V_{CC} to V_{SS}
- Low Channel Input Capacitance of 2.1pF Typical
- 4 Channels of ESD Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT26
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals Finish Matte Tin Pleated Leadframe.
 Solderable per MIL-STD-202, Method 208 ³
- · Weight: 0.016 grams (Approximate)



Device Schematic

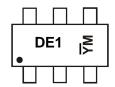
Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D5V0P4UR6SO-7	Standard	DE1	7	8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



DE1 = Product Type Marking Code

YM = Date Code Marking

Y = Year (ex: D = 2016)

M = Month (ex: 9 = September)

Note: "—" Represents Internal Code

Date Code Key

Year	20	16	20	17	20	18	20	19	20	20	20	21
Code	[)		E	I		(3	ŀ	1		
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	Ipp	20	Α	I/O to V _{SS} , 8/20µs
Peak Pulse Current, per IEC 61000-4-5	Ipp	45	Α	V _{CC} to V _{SS} , 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P _{PP}	180	W	I/O to V _{SS} , 8/20µs
Operating Supply Voltage (DC)	V_{DC}	3.6	V	V _{CC} to V _{SS}
ESD Protection – Contact Discharge, per IEC 61000-4-2	V _{ESD_CONTACT}	±30	kV	I/O to V _{SS} , V _{CC} to V _{SS}
ESD Protection – Air Discharge, per IEC 61000-4-2	V _{ESD_AIR}	±30	kV	I/O to V _{SS} , V _{CC} to V _{SS}
Operating Temperature	T _{OP}	-55 to +85	°C	_
Storage Temperature	T _{STG}	-55 to +150	°C	_

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P_{D}	300	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	$R_{ heta JA}$	417	°C/W

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

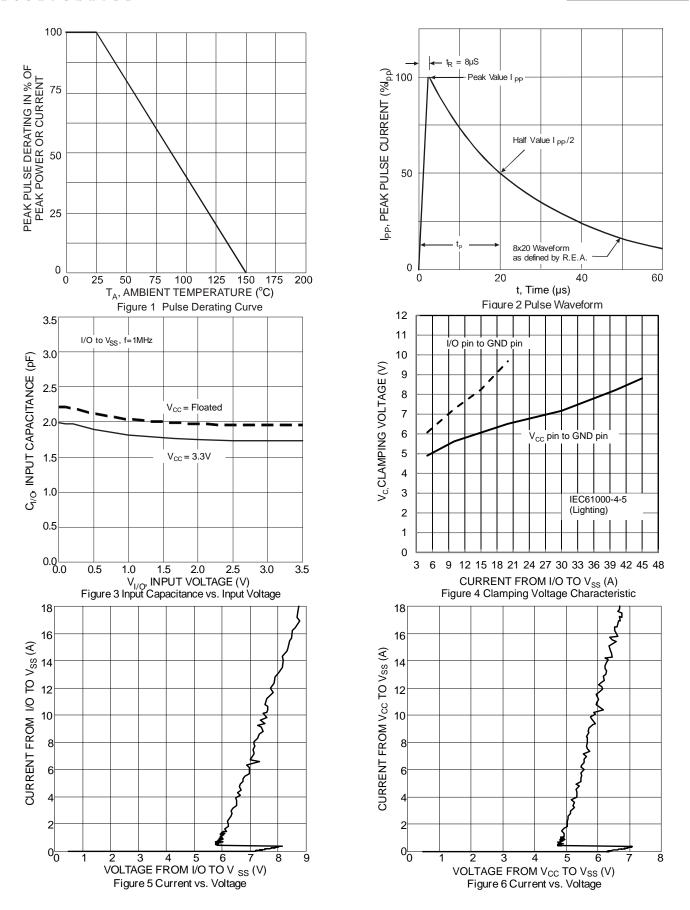
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}	_	_	3.3	V	V _{CC} to V _{SS}
Reverse Leakage Current (Note 6)	I _{LEAK}	_	_	5	μA	$V_{CC} = 3.3V$, V_{CC} to V_{SS}
Channel Leakage Current (Note 6)	I _{CH-LEAK}	_	_	1	μA	$V_{I/O}$ = 3.3V, I/O to V_{SS}
Reverse Breakdown Voltage	V_{BR}	4.5	_	7	V	I _{BR} = 1mA, V _{CC} to V _{SS}
Forward Clamping Voltage	VF	_	0.8	1.2	V	I _F = 15mA, V _{SS} to V _{CC}
Reverse Clamping Voltage (Note 7)	V	_	6	_	V	$I_{PP} = 5A$, I/O to V_{SS} , 8/20 μ s
Reverse Clamping Voltage (Note 7)	V_{C_5A}	_	4.8	_	V	I_{PP} = 5A, V_{CC} to V_{SS} , 8/20 μ s
ESD Clamping Voltage	V _{ESD}	_	7.5	_	V	TLP, 12A, t_P = 100ns, I/O to V_{SS}
L3D Clamping Voltage		_	5.8	_] v	TLP, 12A, $t_P = 100$ ns, V_{CC} to V_{SS}
Dynamic Resistance	R _{DIF}	_	0.15		Ω	TLP, 12A, t_P = 100ns, I/O to V_{SS}
Dynamic Resistance		_	0.07	_	1 12	TLP, 12A, t_P = 100ns, V_{CC} to V_{SS}
Channel Innut Canaditanes	C _{I/O}	_	2.1	2.5	pF	$V_{I/O} = 1.65V$, $V_{CC} = 3.3V$, $f = 1MHz$
Channel Input Capacitance		_	2.4	3.0	pF	$V_{I/O} = 1.65V$, $V_{CC} = $ floated, $f = 1MHz$
	è ΔC _{I/O}		0.05		pF	$V_{SS} = 0V$, I/O = 1.65V, $V_{CC} = 3.3V$, f = 1MHz,
Variation of Channel Input Capacitance			0.05		рг	I/O_x to $V_{SS} - I/O_y$ to V_{SS}
variation of original input dapacitance		_	0.04	_	pF	$V_{SS} = 0V$, $I/O = 1.65V$, $V_{CC} = floated$, $f = 1MHz$, I/O_x to $V_{SS} - I/O_y$ to V_{SS}

Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. website at http://www.diodes.com/package-outlines.html.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an 8x20µs peak pulse current (IPP) waveform.



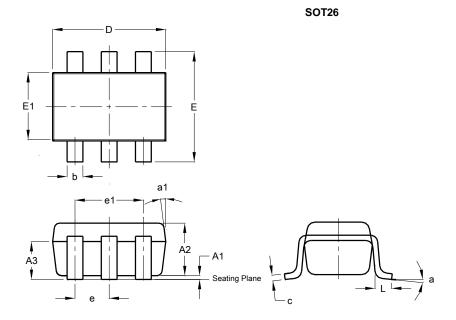






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

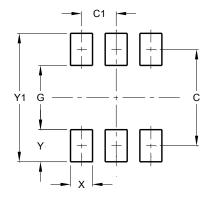


SOT26							
Dim	Min	Max	Тур				
A1	0.013	0.10	0.05				
A2	1.00	1.30	1.10				
А3	0.70	0.80	0.75				
b	0.35	0.50	0.38				
С	0.10	0.20	0.15				
D	2.90	3.10	3.00				
е	-	_	0.95				
e1	-	_	1.90				
Е	2.70	3.00	2.80				
E1	1.50	1.70	1.60				
L	0.35	0.55	0.40				
а	-	-	8°				
a1	_	_	7°				
All	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



Dimensions	Value (in mm)
C	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
V1	3.20



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