

ISL9K30120G3

30A, 1200V Stealth™ Dual Diode

General Description

The ISL9K30120G3 is a StealthTM dual diode optimized for low loss performance in high frequency hard switched applications. The StealthTM family exhibits low reverse recovery current ($I_{RM(REC)}$) and exceptionally soft recovery under typical operating conditions.

This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low $I_{RM(REC)}$ and short t_a phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the Stealth^ $\mbox{\scriptsize TM}$ diode with a 1200V NPT IGBT to provide the most efficient and highest power density design at lower cost.

Formerly developmental type TA49415.

Features

•	Soft Recovery $t_b / t_a > 4.5$
•	Fast Recovery t_{rr} < 56ns
•	Operating Temperature
•	Reverse Voltage

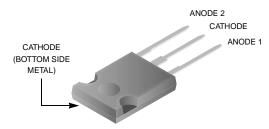
Avalanche Energy Rated

Applications

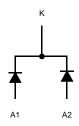
- Switch Mode Power Supplies
- · Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- Snubber Diode

Package

JEDEC STYLE TO-247



Symbol



Device Maximum Ratings (per leg) T_C = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
V _{RWM}	Working Peak Reverse Voltage	1200	V
V _R	DC Blocking Voltage	1200	V
I _{F(AV)}	Average Rectified Forward Current (T _C = 80°C) Total Device Current (Both Legs)	30 60	A A
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	70	Α
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	325	Α
P_{D}	Power Dissipation	166	W
E _{AVL}	Avalanche Energy (1A, 40mH)	20	mJ
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 150	°C
T _L T _{PKG}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10s	300	°C
. 10	Package Body for 10s, See Application Note AN-7528	260	°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Device	Marking	Device	Package	Package Tape Width			Quan	ıtity
K30120G3		ISL9K30120G3	TO-247	N/A			30	
Electric	al Char	acteristics (per le	g) TC = 25°C unles	ss otherwise noted				
Symbol Parameter		Test	Test Conditions		Тур	Max	Units	
Off State	Charact	eristics						
I _R	Instantane	ous Reverse Current	V _R = 1200V	T _C = 25°C	-	-	100	μA
				T _C = 125°C	-	-	1.0	mA
On State	Charact	eristics						
V _F	Instantaneous Forward Voltage	I _F = 30A	T _C = 25°C	_	2.8	3.3	V	
			'	T _C = 125°C	-	2.6	3.1	V
Dynamic	Charact	eristics						
СЈ	Junction C	apacitance	$V_R = 10V, I_F = 0$	V _R = 10V, I _F = 0A		115	-	pF
Switchin	g Charac	eteristics						
t _{rr}	Reverse Recovery Time			= 100A/µs, V _R = 15V	-	45	56	ns
			$I_F = 30A$, $dI_F/dt = 100A/\mu s$, $V_R = 15V$		-	80	100	ns
t _{rr}	Reverse R	Reverse Recovery Time $I_F = 30A$,		-	269	-	ns	
I _{RM(REC)}	Maximum Reverse Recovery Current		$dI_F/dt = 200A/\mu s$,		-	7.5	-	Α
Q_{RR}	Reverse Recovered Charge V _R = 780V, T _C = 25°C		= 25°C	-	930	-	nC	
t _{rr}	Reverse R	ecovery Time	I _F = 30A,		-	529	-	ns
S	Softness Factor (t _b /t _a)			dI _F /dt = 200A/μs,		6.2	-	-
I _{RM(REC)} Maximum R		Reverse Recovery Current	$V_R = 780V,$ $T_C = 125^{\circ}C$		-	11	-	Α
Q_{RR}	Reverse R	ecovered Charge	16 - 120 0		-	3.0	-	μC
t _{rr}	Reverse R	ecovery Time	$I_F = 30A,$ $dI_F/dt = 1000A/\mu s,$ $V_R = 780V,$ $T_C = 125^{\circ}C$		-	260	-	ns
S	Softness F	actor (t _b /t _a)			-	4.8	-	-
I _{RM(REC)}	Maximum	Reverse Recovery Current			-	30	-	Α
Q_{RR}	Reverse R	ecovered Charge			-	3.4	-	μC
dl _M /dt	Maximum	di/dt during t _b	- 520 -			-	A/µs	
Thermal	Characte	eristics						
$R_{\theta JC}$	Thermal R	esistance Junction to Case	TO-247			-	0.75	°C/W
	R _{0JA} Thermal Resistance Junction to Ambient			t TO-247				

Typical Performance Curves (per leg)

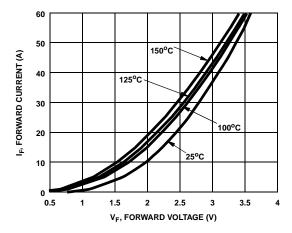
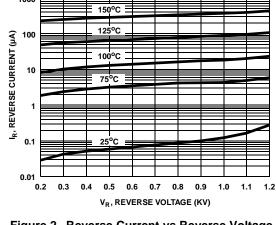


Figure 1. Forward Current vs Forward Voltage



1000

Figure 2. Reverse Current vs Reverse Voltage

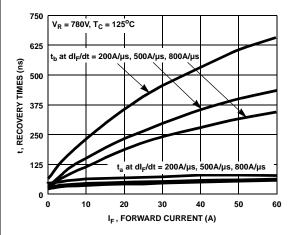


Figure 3. t_a and t_b Curves vs Forward Current

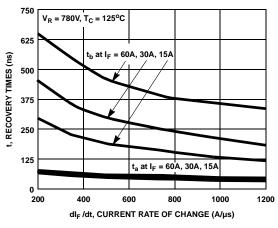


Figure 4. t_a and t_b Curves vs dI_F/dt

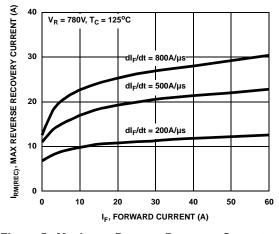


Figure 5. Maximum Reverse Recovery Current vs Forward Current

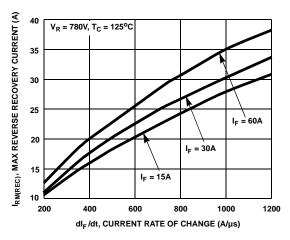


Figure 6. Maximum Reverse Recovery Current vs dl_F/dt

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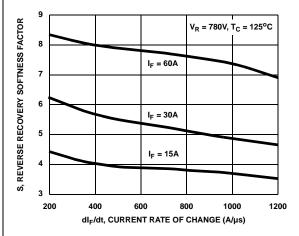


Figure 7. Reverse Recovery Softness Factor vs dI_F/dt

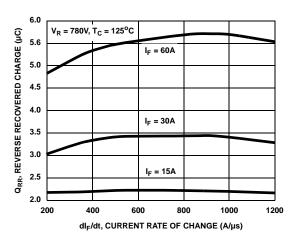


Figure 8. Reverse Recovery Charge vs dl_F/dt

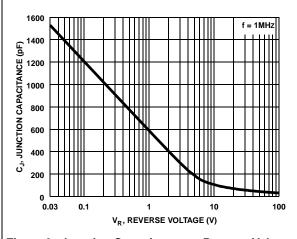


Figure 9. Junction Capacitance vs Reverse Voltage

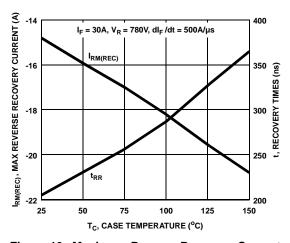


Figure 10. Maximum Reverse Recovery Current and t_{rr} vs Case Temperature

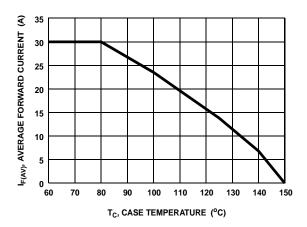


Figure 11. DC CURRENT DERATING CURVE

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Typical Performance Curves (per leg) (Continued)

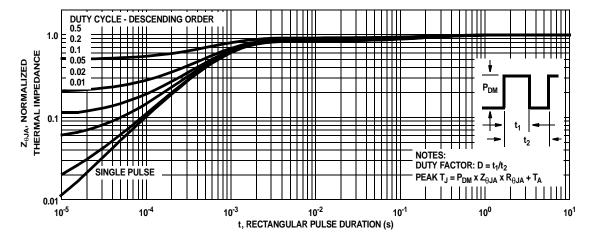
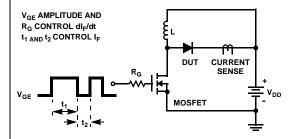


Figure 12. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms



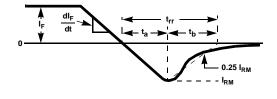
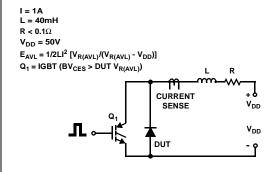


Figure 13. t_{rr} Test Circuit

Figure 14. t_{rr} Waveforms and Definitions



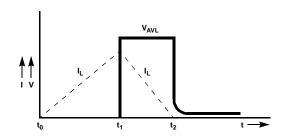


Figure 15. Avalanche Energy Test Circuit

Figure 16. Avalanche Current and Voltage Waveforms

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