

CURRENT SENSE HIGH SIDE SWITCH

Features

- Suitable for 24V systems
- Over current shutdown
- Over temperature shutdown
- Current sensing
- Active clamp
- Reverse circulation immunization
- Low current
- ESD protection
- Optimized Turn On/Off for EMI

Applications

- 21W Filament lamp
- Solenoid
- 24V loads for trucks

Description

The AUIPS7145R is a fully protected four terminal high side switch specifically designed for driving lamp. It features current sensing, over-current, over-temperature, ESD protection and drain to source active clamp. The Ifb pin is used for current sensing. The over-current shutdown is higher than inrush current of the lamp.

Product Summary

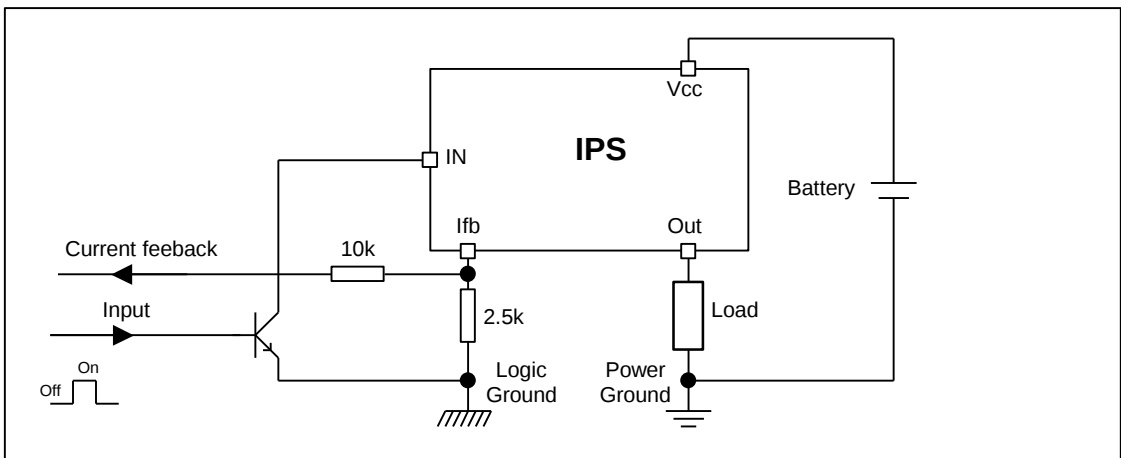
Rds(on)	100mΩ max.
Vclamp	65V
Current shutdown	20A min.

Packages



DPak

Typical Connection



Qualification Information[†]

Qualification Level		Automotive (per AEC-Q100 ^{††})	
		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		DPAK-5L	MSL1, 260°C (per IPC/JEDEC J-STD-020)
ESD	Machine Model	Class M2 (200 V) (per AEC-Q100-003)	
	Human Body Model	Class H1C (1500 V) (per AEC-Q100-002)	
	Charged Device Model	Class C5 (1000 V) (per AEC-Q100-011)	
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site <http://www.irf.com/>

†† Exceptions to AEC-Q100 requirements are noted in the qualification report.

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (T_J= -40°C..150°C, V_{CC}=6..50V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
V _{out}	Maximum output voltage	V _{CC} -60	V _{CC} +0.3	V
I _{rev}	Maximum reverse pulsed current (t=100μs) see page 8	—	30	A
I _{sd} cont.	Maximum diode continuous current T _{ambient} =25°C, R _{th} =70°C/W	—	2.3	A
V _{CC} -V _{in} max.	Maximum V _{CC} voltage	-16	60	V
I _{ifb} , max.	Maximum feedback current	-50	10	mA
V _{CC} sc.	Maximum V _{CC} voltage with short circuit protection see page 8	—	50	V
P _d	Maximum power dissipation (internally limited by thermal protection) R _{th} =50°C/W DPack 6cm ² footprint	—	2.5	W
T _J max.	Maximum operating junction temperature	-40	150	°C
	Maximum storage junction temperature	-55	150	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
R _{th1}	Thermal resistance junction to ambient DPak Std footprint	70	—	°C/W
R _{th2}	Thermal resistance junction to ambient Dpak 6cm ² footprint	50	—	
R _{th3}	Thermal resistance junction to case Dpak	4	—	

Recommended Operating Conditions

These values are given for a quick design.

Symbol	Parameter	Min.	Max.	Units
I _{out}	Continuous output current, T _{ambient} =85°C, T _J =125°C R _{th} =50°C/W, Dpak 6cm ² footprint	—	2.1	A
R _{ifb}	I _{fb} resistor	1.5	—	kΩ

Static Electrical Characteristics

 $T_J = -40^{\circ}\text{C} \dots 150^{\circ}\text{C}$, $V_{CC} = 6\text{--}50\text{V}$ (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Vcc op.	Operating voltage	6	—	60	V	
Rds(on)	ON state resistance $T_J = 25^{\circ}\text{C}$	—	75	100	m Ω	Ids=2A
	ON state resistance $T_J = 150^{\circ}\text{C}(2)$	—	135	180		
Icc off	Supply leakage current	—	1	3	μA	Vin=Vcc / Vifb=Vgnd Vout=Vgnd, $T_J = 25^{\circ}\text{C}$
Iout off	Output leakage current	—	1	3		
I in on	Input current while on	0.6	2	4	mA	Vcc-Vin=28V, $T_J = 25^{\circ}\text{C}$
V clamp1	Vcc to Vout clamp voltage 1	60	64	—	V	Id=10mA
V clamp2	Vcc to Vout clamp voltage 2	60	65	72		Id=6A see fig. 2
Vih(1)	High level Input threshold voltage	—	3	5		Id=10mA
Vil(1)	Low level Input threshold voltage	1.5	2.3	4.7		
Vf	Forward body diode voltage $T_J = 25^{\circ}\text{C}$	—	0.8	0.9		If=1A
	Forward body diode voltage $T_J = 125^{\circ}\text{C}$	—	0.65	0.75		

(1) Input thresholds are measured directly between the input pin and the tab.

Switching Electrical Characteristics

 $V_{CC} = 28\text{V}$, Resistive load=27 Ω , $T_J = 25^{\circ}\text{C}$

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
tdon	Turn on delay time to 20%	4	10	20	μs	See fig. 1
tr	Rise time from 20% to 80% of Vcc	2	5	10		
tdoff	Turn off delay time	20	40	80	μs	
tf	Fall time from 80% to 20% of Vcc	2.5	5	10		

Protection Characteristics

 $T_J = -40^{\circ}\text{C} \dots 150^{\circ}\text{C}$, $V_{CC} = 6\text{--}50\text{V}$ (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	—	$^{\circ}\text{C}$	See fig. 3 and fig.11
I _{sd}	Over-current shutdown	20	25	35	A	See fig. 3 and page 7
I fault	I _{fb} after an over-current or an over-temperature (latched)	2.2	3	5	mA	See fig. 3

Current Sensing Characteristics

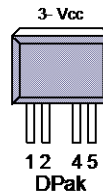
 $T_J = -40^{\circ}\text{C} \dots 150^{\circ}\text{C}$, $V_{CC} = 6\text{--}50\text{V}$ (unless otherwise specified). Specified 500 μs after the turn on. $V_{CC} - V_{ifb} > 4\text{V}$

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ratio	I load / I _{fb} current ratio	2000	2400	2800		Iout<4A
Ratio_TC	I load / I _{fb} variation over temperature(2)	-5%	0	+5	%	$T_J = -40^{\circ}\text{C}$ to $+150^{\circ}\text{C}$
I offset	Load current offset	-0.02	0	0.02	A	Iout<4A
I _{fb} leakage	I _{fb} leakage current On in open load	0	1	10	μA	Iout=0A, Vcc-Vin=28V

(2) Guaranteed by design

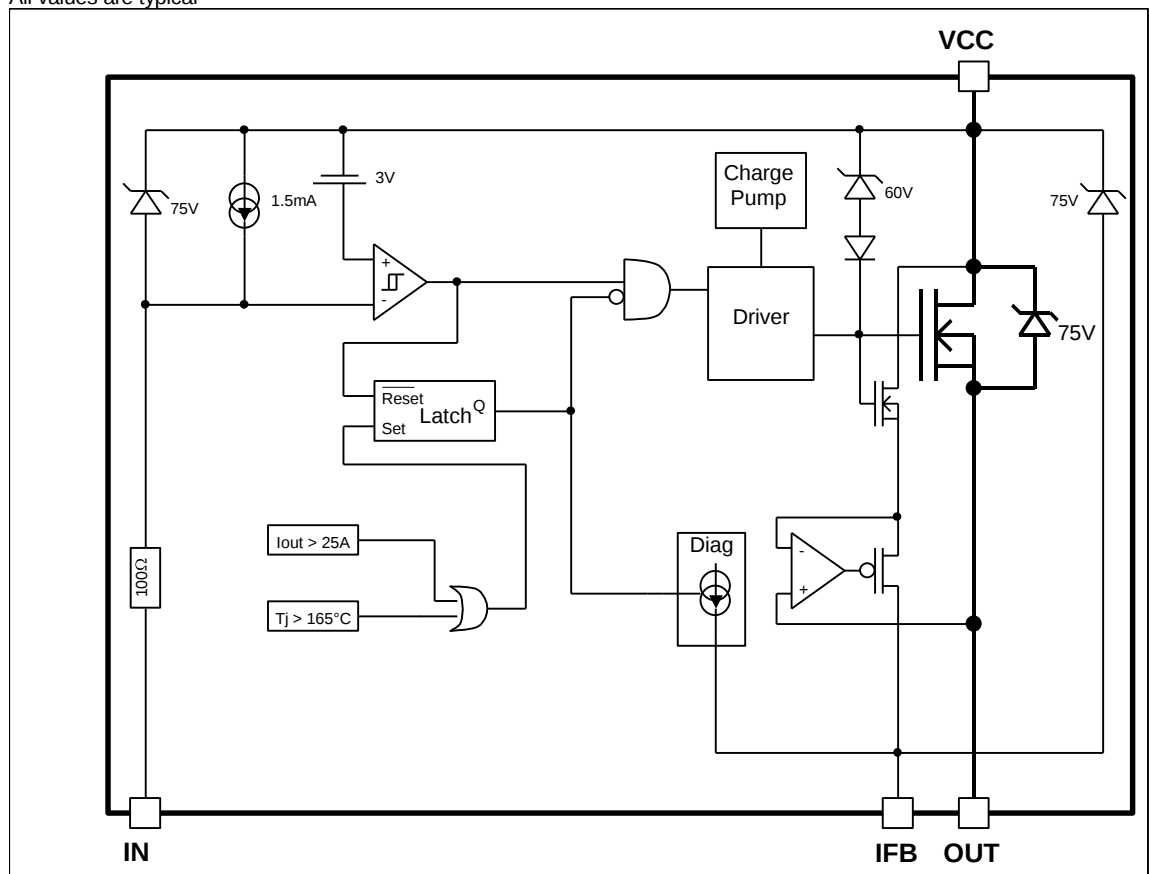
Lead Assignments

1- NC
2- In
3- Vcc
4- Ifb
5- Out



Functional Block Diagram

All values are typical



Truth Table

Op. Conditions	Input	Output	I _{fb} pin voltage
Normal mode	H	L	0V
Normal mode	L	H	I _{load} x R _{fb} / Ratio
Open load	H	L	0V
Open load	L	H	0V
Short circuit to GND	H	L	0V
Short circuit to GND	L	L	V _{fault} (latched)
Over temperature	H	L	0V
Over temperature	L	L	V _{fault} (latched)

Operating voltage

Maximum V_{cc} voltage : this is the maximum voltage before the breakdown of the IC process.

Operating voltage : This is the V_{cc} range in which the functionality of the part is guaranteed. The AEC-Q100 qualification is run at the maximum operating voltage specified in the datasheet.

Reverse battery

During the reverse battery the Mosfet is kept off and the load current is flowing into the body diode of the power Mosfet.

Power dissipation in the IPS : $P = I_{load} * V_f$

There is no protection, so T_j must be lower than 150°C in the worst case condition of current and ambient temperature.

If the power dissipation is too high in R_{fb}, a diode in serial can be added to block the current.

The transistor used to pull-down the input should be a bipolar in order to block the reverse current. The 100ohm input resistor can not sustain continuously 16V (see V_{cc}-V_{in} max. in the Absolute Maximum Ratings section)

Active clamp

The purpose of the active clamp is to limit the voltage across the MOSFET to a value below the body diode break down voltage to reduce the amount of stress on the device during switching.

The temperature increase during active clamp can be estimated as follows:

$$\Delta T_j = P_{CL} \cdot Z_{TH}(t_{CLAMP})$$

Where: $Z_{TH}(t_{CLAMP})$ is the thermal impedance at t_{CLAMP} and can be read from the thermal impedance curves given in the data sheets.

$P_{CL} = V_{CL} \cdot I_{CLavg}$: Power dissipation during active clamp

$V_{CL} = 65V$: Typical V_{CLAMP} value.

$$I_{CLavg} = \frac{I_{CL}}{2} : \text{Average current during active clamp}$$

$$t_{CL} = \frac{I_{CL}}{\left| \frac{di}{dt} \right|} : \text{Active clamp duration}$$

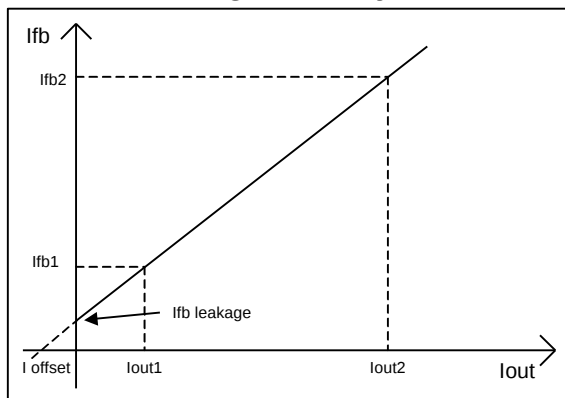
$$\frac{di}{dt} = \frac{V_{Battery} - V_{CL}}{L} : \text{Demagnetization current}$$

Figure 9 gives the maximum inductance versus the load current in the worst case : the part switches off after an over temperature detection. If the load inductance exceeds the curve, a free wheeling diode is required.

Over-current protection

The threshold of the over-current protection is set in order to guarantee that the device is able to turn on a load with an inrush current lower than the minimum of I_{sd} . Nevertheless for high current and high temperature the device may switch off for a lower current due to the over-temperature protection. This behavior is shown in Figure 11.

Current sensing accuracy



The current sensing is specified by measuring 3 points :

- Ifb1 for Iout1
- Ifb2 for Iout2
- Ifb leakage for Iout=0

The parameters in the datasheet are computed with the following formula :

$$\text{Ratio} = (I_{out2} - I_{out1}) / (I_{fb2} - I_{fb1})$$

$$I_{offset} = I_{fb1} \times \text{Ratio} - I_{out1}$$

This allows the designer to evaluate the Ifb for any Iout value using :

$$I_{fb} = (I_{out} + I_{offset}) / \text{Ratio} \text{ if } I_{fb} > I_{fb \text{ leakage}}$$

For some applications, a calibration is required. In that case, the accuracy of the system will depends on the variation of the I_{offset} and the ratio over the temperature range. The ratio variation is given by Ratio_{TC} specified in page 4.

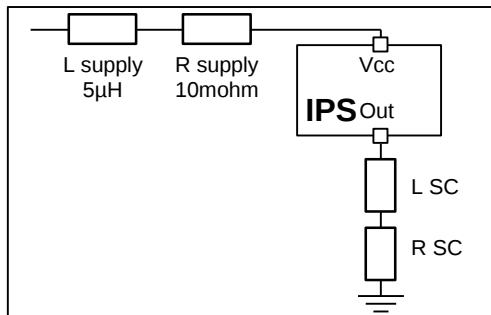
The I_{offset} variation depends directly on the R_{dson} :

$$I_{offset@-40^{\circ}\text{C}} = I_{offset@25^{\circ}\text{C}} / 0.8$$

$$I_{offset@150^{\circ}\text{C}} = I_{offset@25^{\circ}\text{C}} / 1.9$$

Maximum Vcc voltage with short circuit protection

The maximum Vcc voltage with short circuit is the maximum voltage for which the part is able to protect itself under test conditions representative of the application. 2 kind of short circuits are considered : terminal and load short circuit.



	L SC	R SC
Terminal SC	0.1 µH	10 mohm
Load SC	10 µH	100 mohm

Maximum current during reverse circulation

In case of short circuit to battery, a voltage drop of the Vcc may create a current which circulate in reverse mode. When the device is on, this reverse circulation current will not trigger the internal fault latch. This immunization is also true when the part turns on while a reverse current flows into the device. The maximum current (I rev) is specified in the maximum rating section.

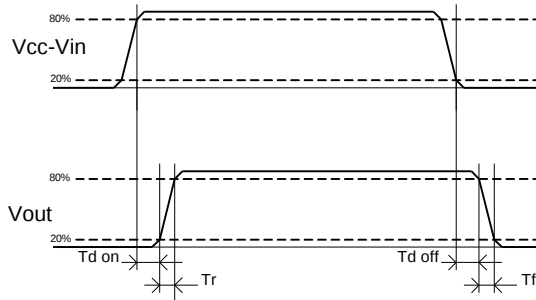


Figure 1 – IN rise time & switching definitions

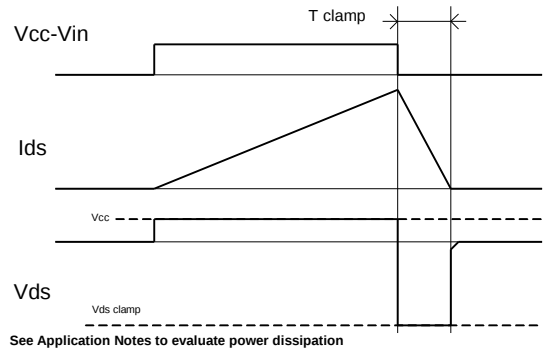


Figure 2 – Active clamp waveforms

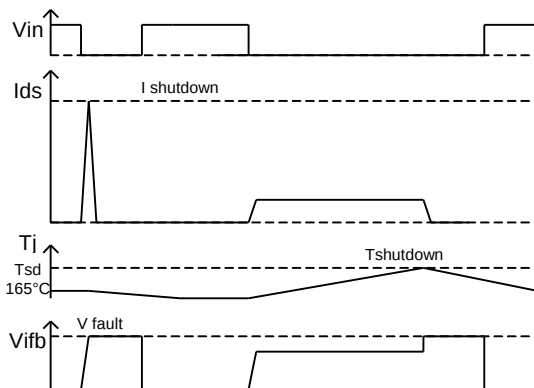


Figure 3 – Protection timing diagram

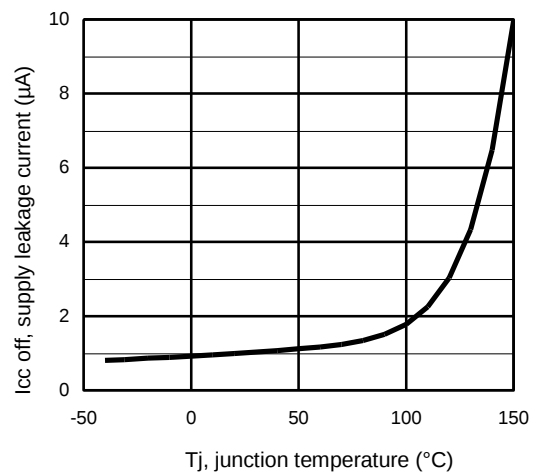


Figure 4 – Icc off (µA) Vs Tj (°C)

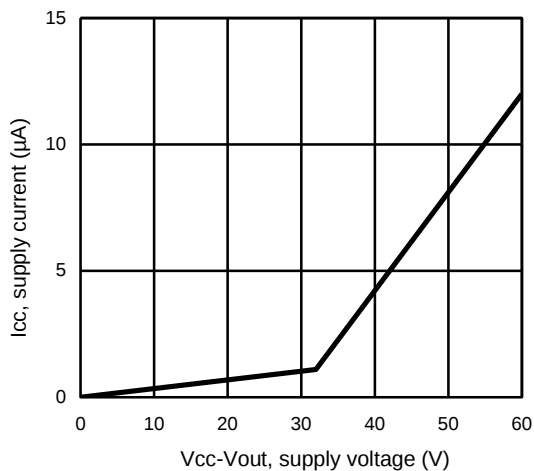


Figure 5 – Icc off (μA) Vs Vcc-Vout (V)

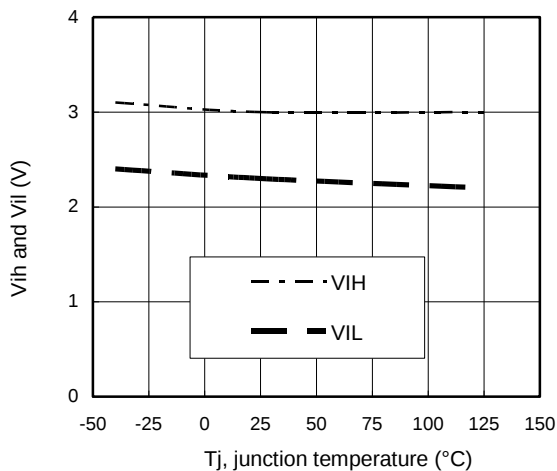


Figure 6 – Vih and Vil (V) Vs Tj (°C)

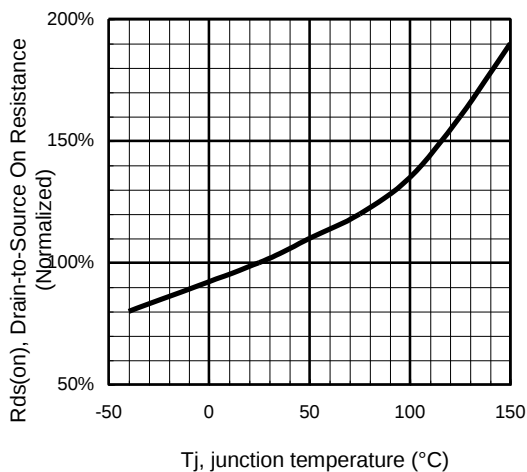


Figure 7 - Normalized Rds(on) (%) Vs Tj (°C)

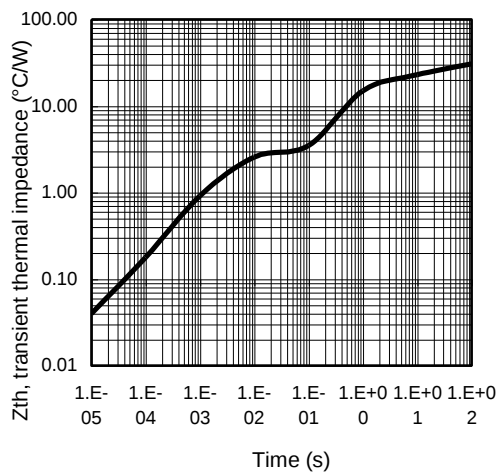


Figure 8 – Transient thermal impedance (°C/W) Vs time (s)

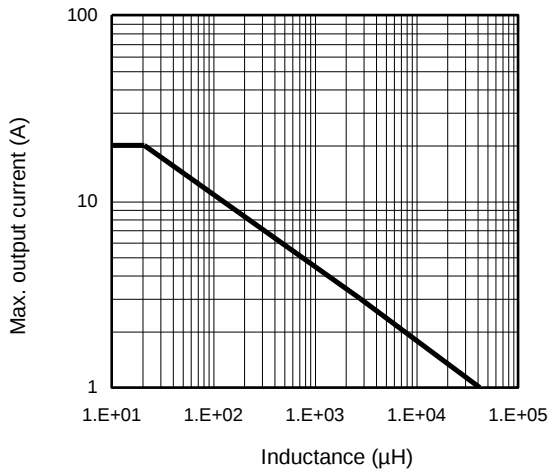


Figure 9 – Max. I_{out} (A) Vs inductance (μH)

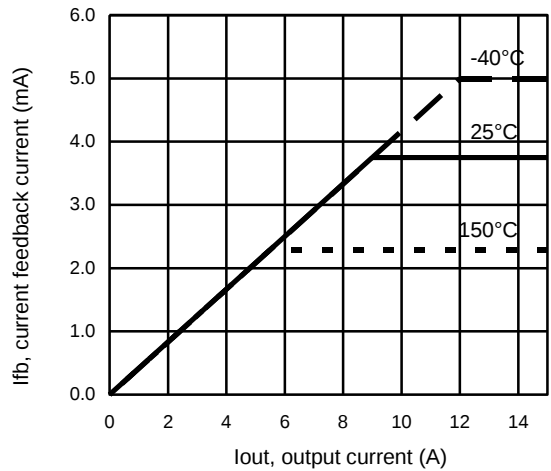
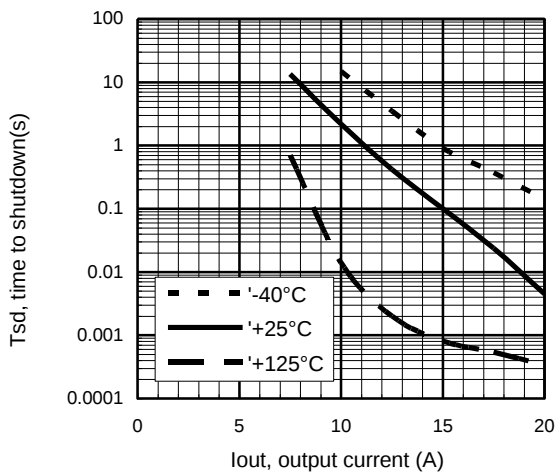
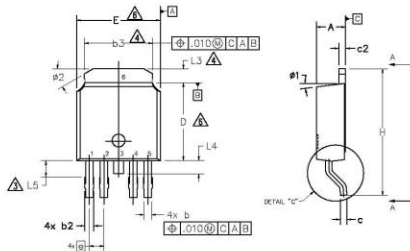
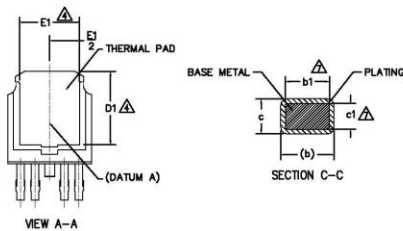
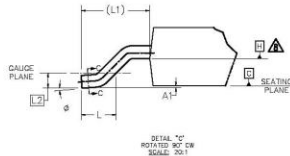


Figure 10 – I_{fb} (mA) Vs I_{out} (A)



**Figure 11 – T_{sd} (s) Vs I_{out} (A)
SMD with 6cm²**

Case Outline 5 Lead – DPAK

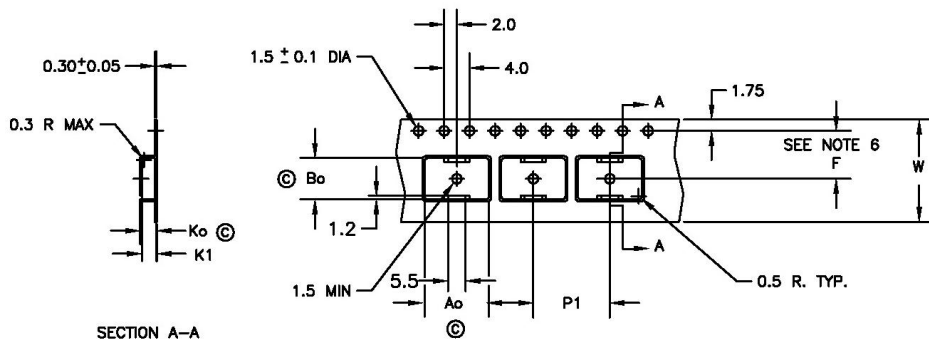


SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	2.18	2.39	.086	.094	
A1	—	0.13	—	.005	
b	0.56	0.79	.022	.031	
b1	.056	0.74	.022	.029	2
b2	0.65	0.89	.026	.035	
b3	4.95	5.46	.195	.215	2
c	0.46	0.61	.018	.024	
c1	0.41	0.56	.016	.022	2
c2	0.46	0.89	.018	.035	
D	5.97	6.22	.235	.245	3
D1	5.21	—	.205	—	
E	6.35	6.73	.250	.265	3
E1	4.32	—	.170	—	
e	1.14 BSC		.045 BSC		
H	9.40	10.41	.370	.410	
L	1.40	1.78	.055	.070	
L1	2.74 BSC		.108 REF.		
L2	0.51 BSC		.020 BSC		
L3	0.89	1.27	.035	.050	
L4	—	1.02	—	.040	
L5	1.14	1.52	.045	.060	
ø	0"	10"	0"	10"	
ø1	0"	15"	0"	15"	
ø2	28"	32"	28"	32"	

NOTES:

- 1.— DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M-1994
- 2.— DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS]
- 3.— LEAD DIMENSION UNCONTROLLED IN L5.
- 4.— DIMENSION D1, E1, L3 & b3 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.
- 5.— SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 AND 0.10 [0.13 AND 0.25] FROM THE LEAD TIP.
- 6.— DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005 [0.13] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
- 7.— DIMENSION b1 & c1 APPLIED TO BASE METAL ONLY.
- 8.— DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- 9.— OUTLINE CONFORMS TO JEDEC OUTLINE TO-252.
10. LEADS AND DRAIN ARE PLATED WITH 100% Sn

Tape & Reel 5 Lead – DPAK

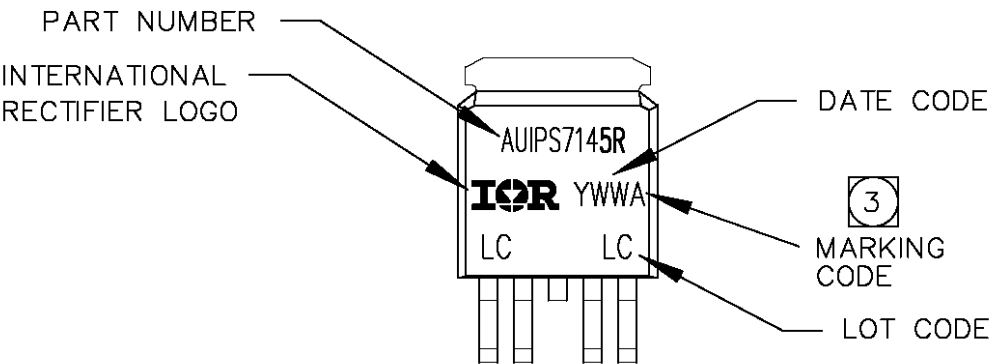


NOTES:

1. 10 SPROCKET HOLE PUNCH CUMULATIVE TOLERANCE ± 0.02
2. CAMBER NOT TO EXCEED 1mm IN 100mm
3. MATERIAL: CONDUCTIVE BLACK POLYSTYRENE
4. A_o AND B_o MEASURED ON A PLANE 0.3mm ABOVE THE BOTTOM OF THE POCKET
5. K_o MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
6. POCKET POSITION RELATIVE TO THE SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE
7. VENDOR: (OPTIONAL)
8. MUST ALSO MEET REQUIREMENTS OF EIA STANDARD #EIA-481A, TAPING OF SURFACE-MOUNT COMPONENTS FOR AUTOMATIC PLACEMENT.
9. TOLERANCE TO BE MANUFACTURER STANDARD
10. SURFACE RESISTIVITY OF MOLDED MATL: MUST MEASURE LESS THAN OR EQUAL TO 10⁸ OHMS PER SQUARE, MEASURED IN ACCORDANCE TO PROCEDURE GIVEN IN ASTM D-257 & ASTM D-991 (REF. C-9000 SPEC.)
11. TOTAL LENGTH PER REEL MUST BE 79 METERS
12. © CRITICAL DIMENSION

Ao = 10.5 mm
 Bo = 7.0 mm
 Ko = 2.8 mm
 K1 = 2.4 mm
 F = 7.5 mm
 P1 = 12.0 mm
 W = 16.0 ± .3 mm

Part Marking Information



Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIPS7145R	D-Pak-5-Lead	Tube	75	AUIPS7145R
		Tape and reel	2000	AUIPS7145RTR
		Tape and reel left	3000	AUIPS7145RTRL
		Tape and reel right	3000	AUIPS7145RTRR

IMPORTANT NOTICE

Unless specifically designated for the automotive market, International Rectifier Corporation and its subsidiaries (IR) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or services without notice. Part numbers designated with the "AU" prefix follow automotive industry and / or customer specific requirements with regards to product discontinuance and process change notification. All products are sold subject to IR's terms and conditions of sale supplied at the time of order acknowledgment.

IR warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with IR's standard warranty. Testing and other quality control techniques are used to the extent IR deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

IR assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using IR components. To minimize the risks with customer products and applications, customers should provide adequate design and operating safeguards.

Reproduction of IR information in IR data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alterations is an unfair and deceptive business practice. IR is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of IR products or serviced with statements different from or beyond the parameters stated by IR for that product or service voids all express and any implied warranties for the associated IR product or service and is an unfair and deceptive business practice. IR is not responsible or liable for any such statements.

IR products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or in any other application in which the failure of the IR product could create a situation where personal injury or death may occur. Should Buyer purchase or use IR products for any such unintended or unauthorized application, Buyer shall indemnify and hold International Rectifier and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that IR was negligent regarding the design or manufacture of the product.

Only products certified as military grade by the Defense Logistics Agency (DLA) of the US Department of Defense, are designed and manufactured to meet DLA military specifications required by certain military, aerospace or other applications. Buyers acknowledge and agree that any use of IR products not certified by DLA as military-grade, in applications requiring military grade products, is solely at the Buyer's own risk and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

IR products are neither designed nor intended for use in automotive applications or environments unless the specific IR products are designated by IR as compliant with ISO/TS 16949 requirements and bear a part number including the designation "AU". Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, IR will not be responsible for any failure to meet such requirements.

For technical support, please contact IR's Technical Assistance Center
<http://www.irf.com/technical-info/>

WORLD HEADQUARTERS:

101 N. Sepulveda Blvd., El Segundo, California 90245
Tel: (310) 252-7105

Revision History

Revision	Date	Notes/Changes
A1	12/11/2009	First release
A2	10/03/2010	Add tri-temp
A3	20/04/2010	DR3 release
B	26/08/2011	Add Vil max page 4
C	10/02/2012	Update storage temperature page 3