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LED2 POLARITY			LED1 POLARITY		
PIN 11	PIN 12	COLOR	PIN 13	PIN 14	COLOR
+	1	GREEN	+	ı	GREEN

ELECTRICAL CHARACTERISTICS @ 25°C

TURNS RATIO

TP1 1CT : 1CT ±2% TP2 1CT : 1CT ±2% 1CT : 1CT ±2% TP3 TP4 1CT : 1CT ±2%

DCL @ 100kHz/100mVRMS

350µH MIN. 8mA DC BIAS (0°C - 70°C)

INS, LOSS

100KHz TD 125MHz -1.2 dB MAX

RET, LOSS (MIN) @ 1000HMS ±15%

0.5MHz-40MHz -18 dB

40MHz-100MHz $-12+20L\Box G(f/80MHz) dB$

CM TO CM REJ

100kHz - 100MHz -30 dB MIN

CM TO DM REJ

100kHz - 100MHz -35 dB MIN

CROSS TALK

1MHz-100MHz -35 dB MIN

HIPOT (Isolation Voltage): 1500 Vrms

100% OF PRODUCTION TESTED TO COMPLY WITH IEEE 802.3 ISOLATION REQUIREMENTS.

LED 1 & 2

IF=20mA GREEN 2.2V TYP. VF (FORWARD VOLTAGE)

λD (DOMINANT WAVELENGTH) IF=20mA GREEN 565nm TYP.

13 •————————————————————————————————————	
GREEN 💭	<u>SCHEMATIC</u>
14	RJ45
TRD1-1 •—	1CT : 1CT
TRD1+2 ←	2 TRP1-
TRD2-3 ←	3 TRP2+
TRD2+4 •─	├ }
TRD3-5 •─	1CT : 1CT
TRD3+6 •—	5 TRP3-
TRD4-7 •	1CT : 1CT
TRD4+8 •— COMMON CT 9 •—	8 TRP4-
10 •—	
11 • GREEN 12	1000pF 2kV
LED2	SHIELD 7777

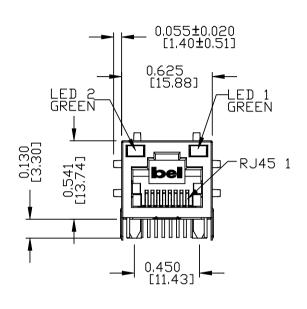
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CHOW WANGCHUNG	i 07-05-10	grg with thing grant the	0875-1G1T-E3	TOL. IN INCH	UNIT : INCH [mm] REV. : A
DRAWN BY	DATE	(Dual Stuff Option, 8 cores)	FILE NAME	.X .XX	SCALE: N/A SIZE: A4
LEE WEIDE	07-05-10	PATENTED	0875-1G1T-E3_A.DWG	.xxx	PAGE : 2

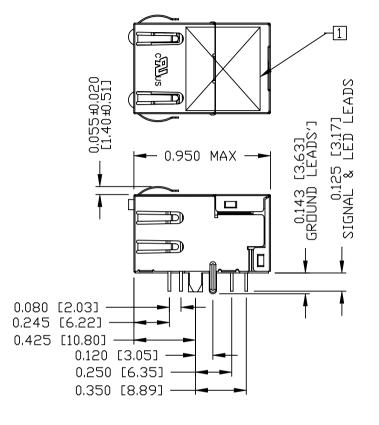


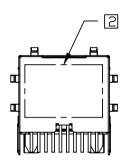
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MECHANICAL SPECIFICATION









NOTES:

PLASTIC HOUSING: THERMOPLASTIC PBT.

FLAMMABILITY RATING UL 94V-0

CONTACTS: 30 MICRO-INCH HARD GOLD PLATING

DUTPUT PINS: LEAD FREE COPPER WIRES, DIA 0.018 INCH.

METAL SHIELD: NICKEL PLATED ON COPPER ALLOY.

(ALL GROUND LEADS ARE SOLDER DIPPED)

1. MARK PART WITH MFG. LOGO, MFG. NAME, PART NUMBER, AND DATE CODE.

2. MARKING "PATENTED".

3. RoHS COMPLIANCE, PER EU DIRECTIVE 2002/95/EC.

c Nus ul recognized - file #E196366 And E169987.

- 4. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS. PART 68 SUBPART F.
- 5. THE PRODUCT IS PATENTED, THE PATENT NUMBER IS U.S. PAT. 5,736,910 AND U.S. PAT. 6,840,817.

ORIGINATED BY	DATE	
CHEN BILL	07-05-10	
DRAWN BY	DATE	
NIXAIL NAIX	07-05-10	

gigabit MagJack®

(Dual Stuff Option, 8 cores)

PATENTED

0875-1G1T-E3	
FILE NAME	1
0875-1G1T-E3_A.DWG	ŀ

PART NO. / DRAWING NO.

STANDARD DIM.				
TOL. I	N INCH			
.X				
.XX				

±0.010

ı	[] METRIC DIM. AS	REFERENC	ŀ
_	UNIT : INCH [mm]	REV. :	4
-	SCALE: N/A	SIZE: A4	ŀ
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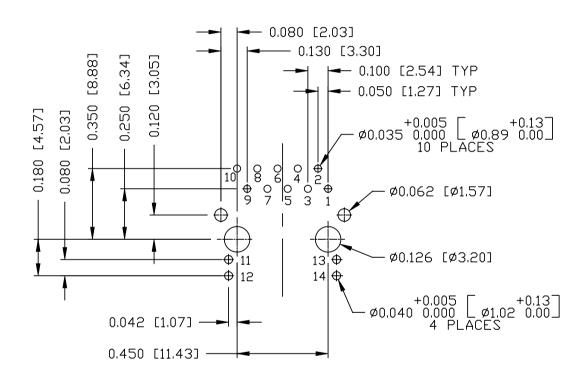
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RECOMMENDED PCB FOOTPRINT COMPONENT SIDE VIEW



ORIGINATED BY	DATE
CHEN BILL	07-05-10
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TITLE			
gigabit	MagJack	⟨®	
(Dual Stuff	\square ption,	8	cores)
PA	TENTED		

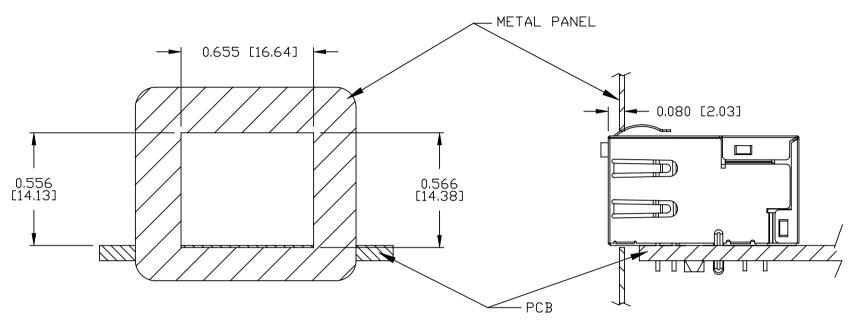
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SUGGESTED PANEL OPENING





NOTE:

THE DISTANCE OF PANEL INSIDE SURFACE RELATIVE TO FRONT SURFACE OF PART IS ONLY A SUGGESTION. IN CASE THIS DISTANCE IS DIFFERENT, THE REQUIRED PANEL OPENING DIMENSIONS CHANGE ACCORDINGLY.

PACKING INFORMATION

PACKING TRAY: 0200-9999-H7 (TOP)

0200-9999-H8 (BOTTOM)

PACKING QUANTITY: 60 PCS FINISHED GOODS PER TRAY

10 TRAYS (600 PCS FINISHED GOODS) PER CARTON BOX

NOTE: CARDBOARDS ARE PLACED BETWEEN LAYERS OF PACKING TRAY INSIDE CARTON BOX (INCLUDE THE UPPERMOST AND LOWERMOST TRAY)

ORIGINATED BY	DATE	
CHEN BILL	07-05-10	
DRAWN BY	DATE	

TITLE
gigabit MagJack®
(Dual Stuff Option, 8 cores)
PATENTED

PART NO. / DRAWING NO. 0875-1G1T-E3		ARD DIM. N INCH	[
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