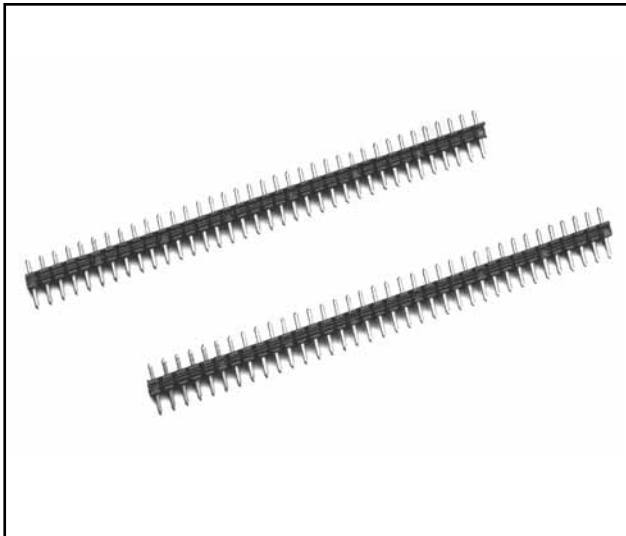


3M™ Pin Strip Header

.100" and .100" x .100", .093" Mating Length, Straight, Solder Tails

929 Series



- Low profile for close board stacking
- Solder stand-offs to facilitate production wave solder and board cleaning
- Board pin retention feature available
- See the Regulatory Information Appendix (RIA) in the "RoHS Compliance" section of www.3Mconnectors.com for compliance information.

Date Modified: February 3, 2012

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Sheet 1 of 2

Physical

Insulation:

Material: Glass Filled Polyester (PBT) or PCT

Flammability: UL 94V-0

Color: Black

Contact:

Material: Copper Alloy

Plating:

Underplating: 50 μ [1.27 μ m] Nickel

Wiping Area & Solder Tails: See ordering information

Marking: None

Electrical

Current Rating: 2.5 A

Insulation Resistance: $> 5 \times 10^9 \Omega$ at 500 V_{DC}

Withstanding Voltage: 1500 V_{RMS} at Sea Level

Environmental

Temperature Rating: -40°C to +105°C

Process Rating: PCT Insulator: 260°C per J-STD-020

PBT Insulator: Maximum insulator temperature 191°C for solder wave process only

Moisture Sensitivity Level: 1 (per J-STD-020C) for PCT insulator only

UL File No.: E68080



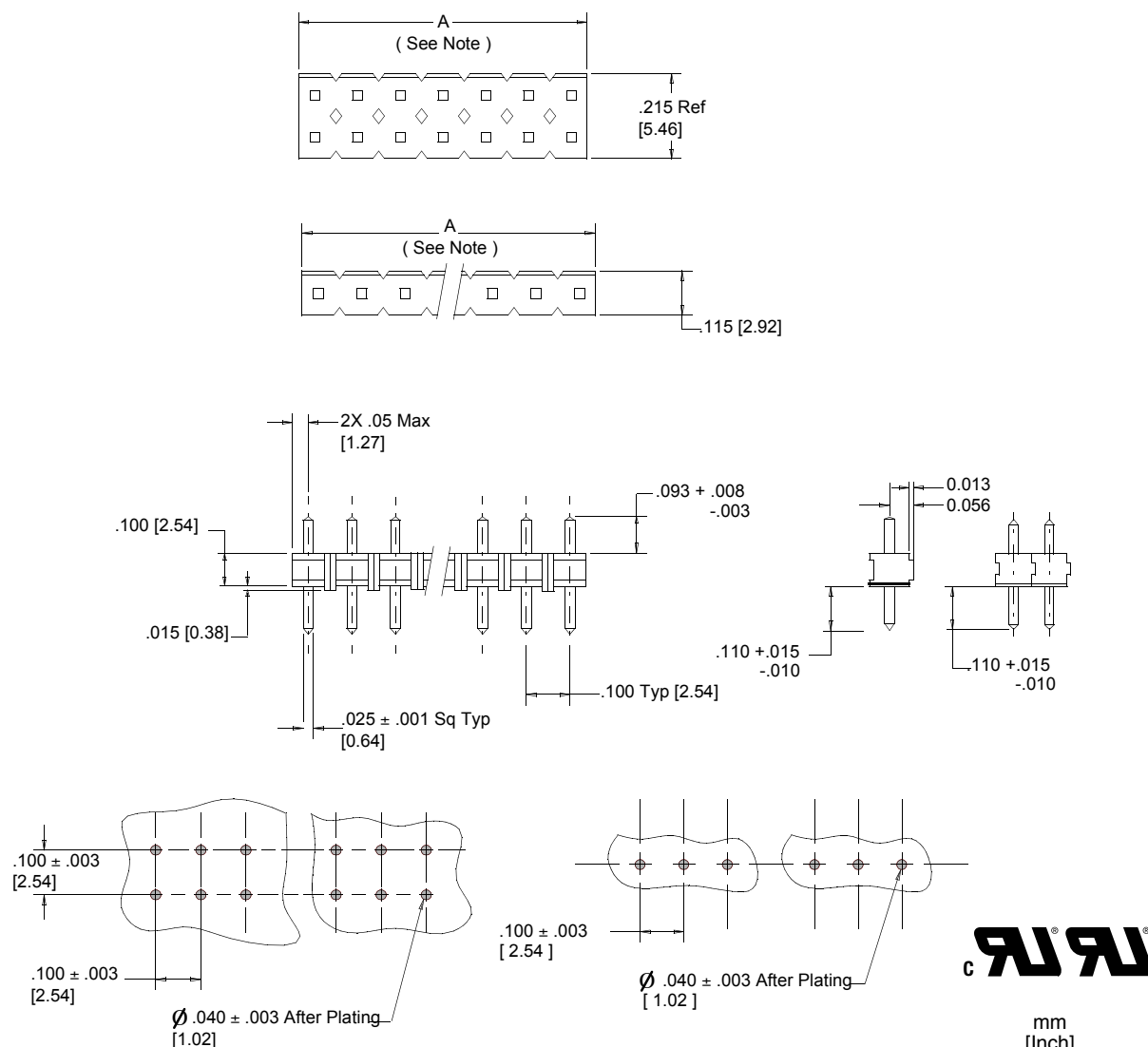
Electronic Solutions Division
Interconnect Solutions
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3M™ Pin Strip Header

.100" and .100" x .100", .093" Mating Length, Straight, Solder Tails

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Recommended Hole Pattern

Note:

A = .10" [2.5] $\times$ pin quantity per row. Dimension A is a maximum when the pin strip is cut to length, not when the strip is broken to length. 2 row parts not recommended to be broken to length.

Tolerance Unless Noted			
	.0	.00	.000
mm	$\pm .1$	$\pm .01$	$\pm .005$

[] Dimensions for Reference Only

Ordering Information

Tin

9X9800-01-XX-XX

Pin Quantity (01-36)

Board Retention Feature

2 = None

R = Kinked Tails

Plating Suffix:

blank = 100 μ " [2.54 μ m] tin-lead, PBT insulator (RIA E3 & C2 apply)

RK = 200 μ " [5 μ m] matte tin, PCT insulator (RIA E1 & C1 apply)

Gold

9X98XX-01-XX-XX

05 = 1 row

15 = 2 row

Board Retention Feature

2 = None

R = Kinked Tails

Gold Plating Suffix

I = 10 μ " [0.25 μ m] all over, PBT insulator (RIA E1 & C1 apply)

EU = 10 μ " [0.25 μ m] all over, PCT insulator (RIA E1 & C1 apply)

Pin Quantity per row (01-36)

[(02-72) $\div$ 2]

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Sheet 2 of 2



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