

# SN54HC259, SN74HC259 8-BIT ADDRESSABLE LATCHES

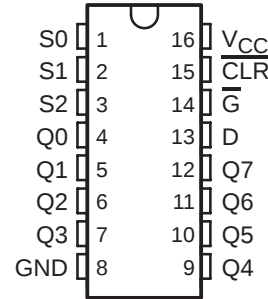
SCLS134E – DECEMBER 1982 – REVISED SEPTEMBER 2003

- Wide Operating Voltage Range of 2 V to 6 V
- High-Current Inverting Outputs Drive Up To 10 LSTTL Loads
- Low Power Consumption, 80- $\mu$ A Max  $I_{CC}$
- Typical  $t_{pd} = 14$  ns
- $\pm 4$ -mA Output Drive at 5 V
- Low Input Current of 1  $\mu$ A Max
- 8-Bit Parallel-Out Storage Register Performs Serial-to-Parallel Conversion With Storage
- Asynchronous Parallel Clear
- Active-High Decoder
- Enable Input Simplifies Expansion
- Expandable for n-Bit Applications
- Four Distinct Functional Modes

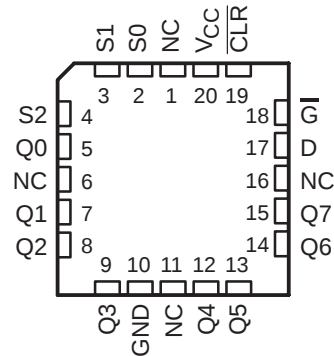
## description/ordering information

These 8-bit addressable latches are designed for general-purpose storage applications in digital systems. Specific uses include working registers, serial-holding registers, and active-high decoders or demultiplexers. They are multifunctional devices capable of storing single-line data in eight addressable latches and being a 1-of-8 decoder or demultiplexer with active-high outputs.

SN54HC259 . . . J OR W PACKAGE  
SN74HC259 . . . D, N, NS, OR PW PACKAGE  
(TOP VIEW)



SN54HC259 . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection

## ORDERING INFORMATION

| T <sub>A</sub> | PACKAGE <sup>†</sup> |              | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
|----------------|----------------------|--------------|-----------------------|------------------|
| –40°C to 85°C  | PDIP – N             | Tube of 25   | SN74HC259N            | SN74HC259N       |
|                |                      | Tube of 40   | SN74HC259D            | HC259            |
|                | SOIC – D             | Reel of 2500 | SN74HC259DR           |                  |
|                |                      | Reel of 250  | SN74HC259DT           |                  |
|                | SOP – NS             | Reel of 2000 | SN74HC259NSR          | HC259            |
| –55°C to 125°C | TSSOP – PW           | Reel of 2000 | SN74HC259PWR          | HC259            |
|                |                      | Reel of 250  | SN74HC259PWT          |                  |
|                | CDIP – J             | Tube of 25   | SNJ54HC259J           | SNJ54HC259J      |
|                | CFP – W              | Tube of 150  | SNJ54HC259W           | SNJ54HC259W      |
|                | LCCC – FK            | Tube of 55   | SNJ54HC259FK          | SNJ54HC259FK     |

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 2003, Texas Instruments Incorporated  
On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

# SN54HC259, SN74HC259

## 8-BIT ADDRESSABLE LATCHES

SCLS134E – DECEMBER 1982 – REVISED SEPTEMBER 2003

### description/ordering information (continued)

Four distinct modes of operation are selectable by controlling the clear ( $\overline{\text{CLR}}$ ) and enable ( $\overline{\text{G}}$ ) inputs. In the addressable-latch mode, data at the data-in terminal is written into the addressed latch. The addressed latch follows the data input, with all unaddressed latches remaining in their previous states. In the memory mode, all latches remain in their previous states and are unaffected by the data or address inputs. To eliminate the possibility of entering erroneous data in the latches,  $\overline{\text{G}}$  should be held high (inactive) while the address lines are changing. In the 1-of-8 decoding or demultiplexing mode, the addressed output follows the level of the D input with all other outputs low. In the clear mode, all outputs are low and unaffected by the address and data inputs.

### Function Tables

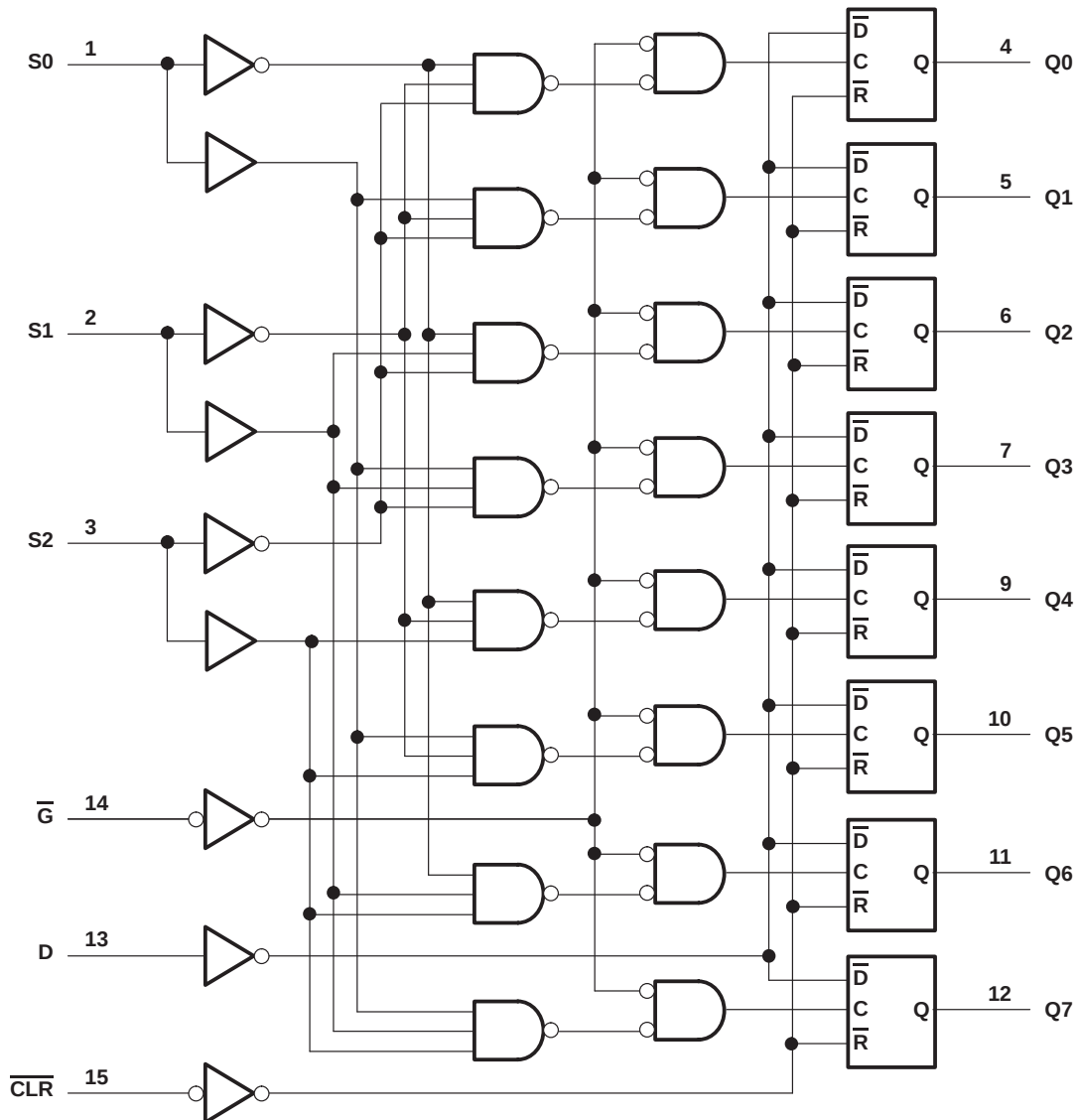
FUNCTION

| INPUTS                  |                       | OUTPUT OF<br>ADDRESSED<br>LATCH | EACH<br>OTHER<br>OUTPUT | FUNCTION             |
|-------------------------|-----------------------|---------------------------------|-------------------------|----------------------|
| $\overline{\text{CLR}}$ | $\overline{\text{G}}$ |                                 |                         |                      |
| H                       | L                     | D                               | $Q_{iO}$                | Addressable latch    |
| H                       | H                     | $Q_{iO}$                        | $Q_{iO}$                | Memory               |
| L                       | L                     | D                               | L                       | 8-line demultiplexer |
| L                       | H                     | L                               | L                       | Clear                |

LATCH SELECTION

| SELECT INPUTS |    |    | LATCH<br>ADDRESSED |
|---------------|----|----|--------------------|
| S2            | S1 | S0 |                    |
| L             | L  | L  | 0                  |
| L             | L  | H  | 1                  |
| L             | H  | L  | 2                  |
| L             | H  | H  | 3                  |
| H             | L  | L  | 4                  |
| H             | L  | H  | 5                  |
| H             | H  | L  | 6                  |
| H             | H  | H  | 7                  |

**logic diagram**

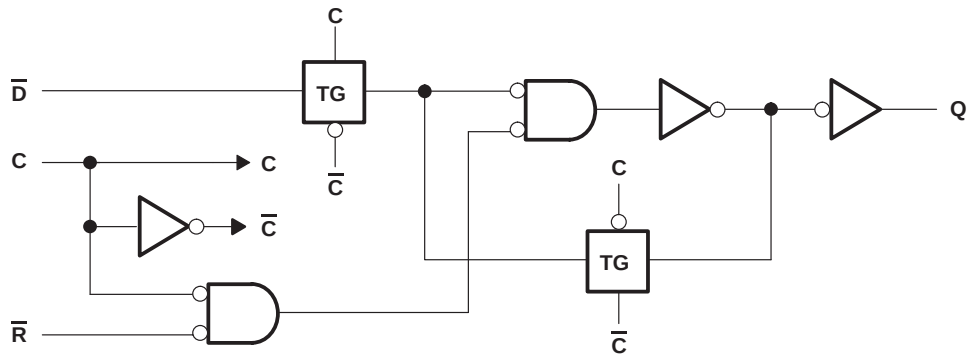


Pin numbers shown are for the D, J, N, NS, PW, and W packages.

SN54HC259, SN74HC259  
8-BIT ADDRESSABLE LATCHES

SCLS134E – DECEMBER 1982 – REVISED SEPTEMBER 2003

logic diagram, each internal latch (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

|                                                                             |               |
|-----------------------------------------------------------------------------|---------------|
| Supply voltage range, $V_{CC}$                                              | –0.5 V to 7 V |
| Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ ) (see Note 1)  | ±20 mA        |
| Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ ) (see Note 1) | ±20 mA        |
| Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ )                  | ±25 mA        |
| Continuous current through $V_{CC}$ or GND                                  | ±50 mA        |
| Package thermal impedance, $\theta_{JA}$ (see Note 2): D package            | 73°C/W        |
| N package                                                                   | 67°C/W        |
| NS package                                                                  | 64°C/W        |
| PW package                                                                  | 108°C/W       |

Storage temperature range,  $T_{stg}$  –65°C to 150°C

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.  
2. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions (see Note 3)

|                 |                                 |                         | SN54HC259 |                 |     | SN74HC259 |                 |     | UNIT |
|-----------------|---------------------------------|-------------------------|-----------|-----------------|-----|-----------|-----------------|-----|------|
|                 |                                 |                         | MIN       | NOM             | MAX | MIN       | NOM             | MAX |      |
| V <sub>CC</sub> | Supply voltage                  |                         | 2         | 5               | 6   | 2         | 5               | 6   | V    |
| V <sub>IH</sub> | High-level input voltage        | V <sub>CC</sub> = 2 V   | 1.5       |                 |     | 1.5       |                 |     | V    |
|                 |                                 | V <sub>CC</sub> = 4.5 V | 3.15      |                 |     | 3.15      |                 |     |      |
|                 |                                 | V <sub>CC</sub> = 6 V   | 4.2       |                 |     | 4.2       |                 |     |      |
| V <sub>IL</sub> | Low-level input voltage         | V <sub>CC</sub> = 2 V   | 0.5       |                 |     | 0.5       |                 |     | V    |
|                 |                                 | V <sub>CC</sub> = 4.5 V | 1.35      |                 |     | 1.35      |                 |     |      |
|                 |                                 | V <sub>CC</sub> = 6 V   | 1.8       |                 |     | 1.8       |                 |     |      |
| V <sub>I</sub>  | Input voltage                   |                         | 0         | V <sub>CC</sub> |     | 0         | V <sub>CC</sub> |     | V    |
| V <sub>O</sub>  | Output voltage                  |                         | 0         | V <sub>CC</sub> |     | 0         | V <sub>CC</sub> |     | V    |
| Δt/Δv           | Input transition rise/fall time | V <sub>CC</sub> = 2 V   | 1000      |                 |     | 1000      |                 |     | ns   |
|                 |                                 | V <sub>CC</sub> = 4.5 V | 500       |                 |     | 500       |                 |     |      |
|                 |                                 | V <sub>CC</sub> = 6 V   | 400       |                 |     | 400       |                 |     |      |
| T <sub>A</sub>  | Operating free-air temperature  |                         | −55       | 125             |     | −40       | 85              |     | °C   |

NOTE 3: All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.



# SN54HC259, SN74HC259 8-BIT ADDRESSABLE LATCHES

SCLS134E – DECEMBER 1982 – REVISED SEPTEMBER 2003

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER       | TEST CONDITIONS                                           |                           | V <sub>CC</sub> | T <sub>A</sub> = 25°C |       |      | SN54HC259 |       | SN74HC259 |       | UNIT |
|-----------------|-----------------------------------------------------------|---------------------------|-----------------|-----------------------|-------|------|-----------|-------|-----------|-------|------|
|                 |                                                           |                           |                 | MIN                   | TYP   | MAX  | MIN       | MAX   | MIN       | MAX   |      |
| V <sub>OH</sub> | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>       | I <sub>OH</sub> = -20 µA  | 2 V             | 1.9                   | 1.998 |      | 1.9       |       | 1.9       |       | V    |
|                 |                                                           |                           | 4.5 V           | 4.4                   | 4.499 |      | 4.4       |       | 4.4       |       |      |
|                 |                                                           |                           | 6 V             | 5.9                   | 5.999 |      | 5.9       |       | 5.9       |       |      |
|                 |                                                           | I <sub>OH</sub> = -4 mA   | 4.5 V           | 3.98                  | 4.3   |      | 3.7       |       | 3.84      |       |      |
|                 |                                                           | I <sub>OH</sub> = -5.2 mA | 6 V             | 5.48                  | 5.8   |      | 5.2       |       | 5.34      |       |      |
| V <sub>OL</sub> | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>       | I <sub>OL</sub> = 20 µA   | 2 V             |                       | 0.002 | 0.1  |           | 0.1   |           | 0.1   | V    |
|                 |                                                           |                           | 4.5 V           |                       | 0.001 | 0.1  |           | 0.1   |           | 0.1   |      |
|                 |                                                           |                           | 6 V             |                       | 0.001 | 0.1  |           | 0.1   |           | 0.1   |      |
|                 |                                                           | I <sub>OL</sub> = 4 mA    | 4.5 V           |                       | 0.17  | 0.26 |           | 0.4   |           | 0.33  |      |
|                 |                                                           | I <sub>OL</sub> = 5.2 mA  | 6 V             |                       | 0.15  | 0.26 |           | 0.4   |           | 0.33  |      |
| I <sub>I</sub>  | V <sub>I</sub> = V <sub>CC</sub> or 0                     |                           | 6 V             |                       | ±0.1  | ±100 |           | ±1000 |           | ±1000 | nA   |
| I <sub>CC</sub> | V <sub>I</sub> = V <sub>CC</sub> or 0, I <sub>O</sub> = 0 |                           | 6 V             |                       |       | 8    |           | 160   |           | 80    | µA   |
| C <sub>i</sub>  |                                                           |                           | 2 V to 6 V      |                       | 3     | 10   |           | 10    |           | 10    | pF   |

timing requirements over recommended operating free-air temperature range (unless otherwise noted)

|                 |                                                                  |                             | V <sub>CC</sub> | T <sub>A</sub> = 25°C |     | SN54HC259 |     | SN74HC259 |     | UNIT |
|-----------------|------------------------------------------------------------------|-----------------------------|-----------------|-----------------------|-----|-----------|-----|-----------|-----|------|
|                 |                                                                  |                             |                 | MIN                   | MAX | MIN       | MAX | MIN       | MAX |      |
| t <sub>w</sub>  | Pulse duration                                                   | $\overline{\text{CLR}}$ low | 2 V             | 80                    |     | 120       |     | 100       |     | ns   |
|                 |                                                                  |                             | 4.5 V           | 16                    |     | 24        |     | 20        |     |      |
|                 |                                                                  |                             | 6 V             | 14                    |     | 20        |     | 17        |     |      |
|                 | $\overline{\text{G}}$ low                                        | 2 V                         | 80              |                       | 120 |           | 100 |           |     |      |
|                 |                                                                  | 4.5 V                       | 16              |                       | 24  |           | 20  |           |     |      |
|                 |                                                                  | 6 V                         | 14              |                       | 20  |           | 17  |           |     |      |
| t <sub>su</sub> | Setup time, data or address before $\overline{\text{G}}\uparrow$ |                             | 2 V             | 75                    |     | 115       |     | 95        |     | ns   |
|                 |                                                                  |                             | 4.5 V           | 15                    |     | 23        |     | 19        |     |      |
|                 |                                                                  |                             | 6 V             | 13                    |     | 20        |     | 16        |     |      |
| t <sub>h</sub>  | Hold time, data or address after $\overline{\text{G}}\uparrow$   |                             | 2 V             | 5                     |     | 5         |     | 5         |     | ns   |
|                 |                                                                  |                             | 4.5 V           | 5                     |     | 5         |     | 5         |     |      |
|                 |                                                                  |                             | 6 V             | 5                     |     | 5         |     | 5         |     |      |



# SN54HC259, SN74HC259

## 8-BIT ADDRESSABLE LATCHES

SCLS134E – DECEMBER 1982 – REVISED SEPTEMBER 2003

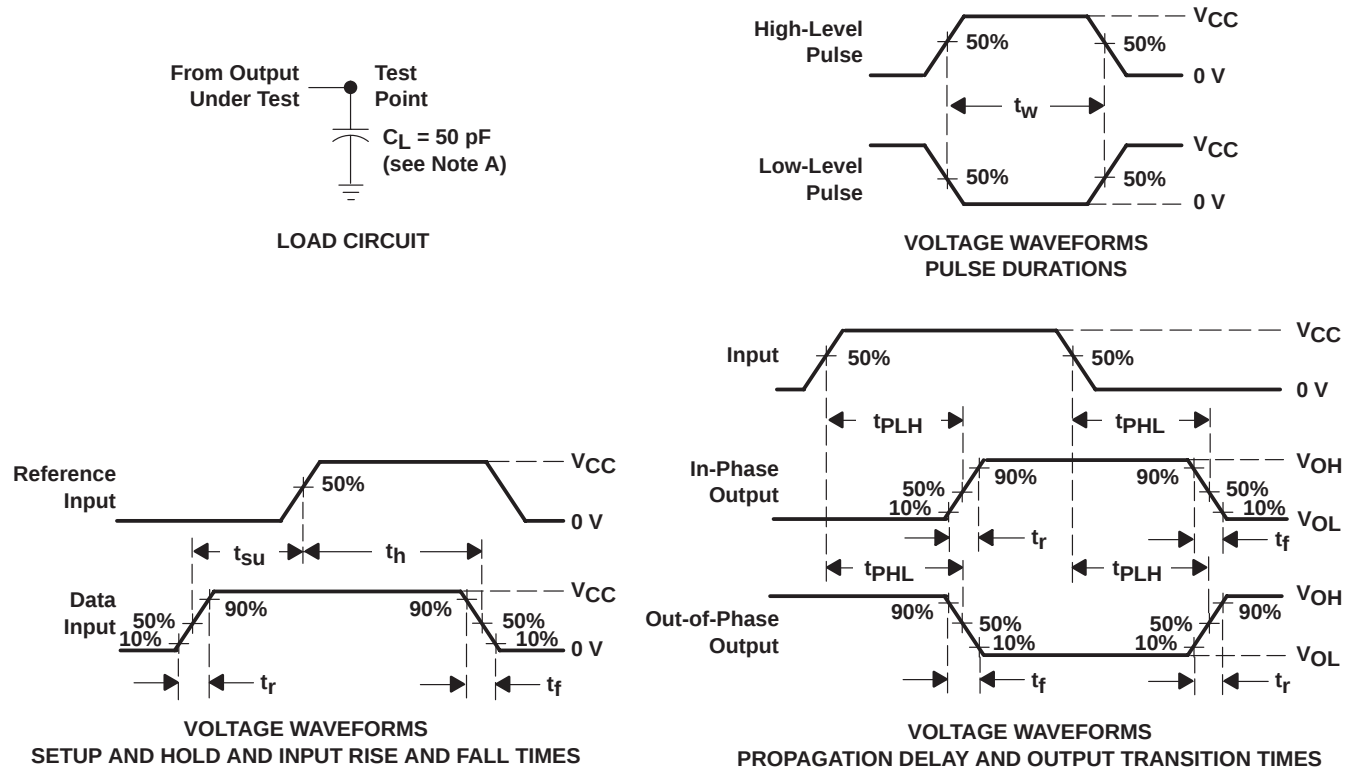
switching characteristics over recommended operating free-air temperature range,  $C_L = 50$  pF (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM<br>(INPUT)  | TO<br>(OUTPUT) | $V_{CC}$ | $T_A = 25^\circ\text{C}$ |     |     | SN54HC259 |     | SN74HC259 |     | UNIT |
|-----------|------------------|----------------|----------|--------------------------|-----|-----|-----------|-----|-----------|-----|------|
|           |                  |                |          | MIN                      | TYP | MAX | MIN       | MAX | MIN       | MAX |      |
| $t_{PHL}$ | $\overline{CLR}$ | Any Q          | 2 V      |                          | 60  | 150 |           | 225 |           | 190 | ns   |
|           |                  |                | 4.5 V    |                          | 18  | 30  |           | 45  |           | 38  |      |
|           |                  |                | 6 V      |                          | 14  | 26  |           | 38  |           | 32  |      |
| $t_{pd}$  | Data             | Any Q          | 2 V      |                          | 56  | 130 |           | 195 |           | 165 | ns   |
|           |                  |                | 4.5 V    |                          | 17  | 26  |           | 39  |           | 33  |      |
|           |                  |                | 6 V      |                          | 13  | 22  |           | 33  |           | 28  |      |
|           | Address          | Any Q          | 2 V      |                          | 74  | 200 |           | 300 |           | 250 |      |
|           |                  |                | 4.5 V    |                          | 21  | 40  |           | 60  |           | 50  |      |
|           |                  |                | 6 V      |                          | 17  | 34  |           | 51  |           | 43  |      |
|           | $\overline{G}$   | Any Q          | 2 V      |                          | 66  | 170 |           | 255 |           | 215 |      |
|           |                  |                | 4.5 V    |                          | 20  | 34  |           | 51  |           | 43  |      |
|           |                  |                | 6 V      |                          | 16  | 29  |           | 43  |           | 37  |      |
| $t_t$     |                  | Any            | 2 V      |                          | 28  | 75  |           | 110 |           | 95  | ns   |
|           |                  |                | 4.5 V    |                          | 8   | 15  |           | 22  |           | 19  |      |
|           |                  |                | 6 V      |                          | 6   | 13  |           | 19  |           | 16  |      |

operating characteristics,  $T_A = 25^\circ\text{C}$

| PARAMETER |                                         | TEST CONDITIONS | TYP | UNIT |
|-----------|-----------------------------------------|-----------------|-----|------|
| $C_{pd}$  | Power dissipation capacitance per latch | No load         | 33  | pF   |

## PARAMETER MEASUREMENT INFORMATION



- NOTES:
- A.  $C_L$  includes probe and test-fixture capacitance.
  - B. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 1 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r = 6 \text{ ns}$ ,  $t_f = 6 \text{ ns}$ .
  - C. The outputs are measured one at a time with one input transition per measurement.
  - D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

Figure 1. Load Circuit and Voltage Waveforms

**PACKAGING INFORMATION**

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5)       | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|------------------|----------------------|--------------|-------------------------------|-------------------------|
| 85519012A        | ACTIVE        | LCCC         | FK                 | 20   | 1              | TBD                        | POST-PLATE       | N / A for Pkg Type   | -55 to 125   | 85519012A<br>SNJ54HC<br>259FK | <a href="#">Samples</a> |
| 8551901EA        | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42              | N / A for Pkg Type   | -55 to 125   | 8551901EA<br>SNJ54HC259J      | <a href="#">Samples</a> |
| 8551901FA        | ACTIVE        | CFP          | W                  | 16   | 1              | TBD                        | A42              | N / A for Pkg Type   | -55 to 125   | 8551901FA<br>SNJ54HC259W      | <a href="#">Samples</a> |
| JM38510/65402BEA | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42              | N / A for Pkg Type   | -55 to 125   | JM38510/<br>65402BEA          | <a href="#">Samples</a> |
| M38510/65402BEA  | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42              | N / A for Pkg Type   | -55 to 125   | JM38510/<br>65402BEA          | <a href="#">Samples</a> |
| SN54HC259J       | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42              | N / A for Pkg Type   | -55 to 125   | SN54HC259J                    | <a href="#">Samples</a> |
| SN74HC259D       | ACTIVE        | SOIC         | D                  | 16   | 40             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DE4     | ACTIVE        | SOIC         | D                  | 16   | 40             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DG4     | ACTIVE        | SOIC         | D                  | 16   | 40             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DR      | ACTIVE        | SOIC         | D                  | 16   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DRE4    | ACTIVE        | SOIC         | D                  | 16   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DRG4    | ACTIVE        | SOIC         | D                  | 16   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DT      | ACTIVE        | SOIC         | D                  | 16   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DTE4    | ACTIVE        | SOIC         | D                  | 16   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259DTG4    | ACTIVE        | SOIC         | D                  | 16   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259N       | ACTIVE        | PDIP         | N                  | 16   | 25             | Pb-Free<br>(RoHS)          | CU NIPDAU        | N / A for Pkg Type   | -40 to 85    | SN74HC259N                    | <a href="#">Samples</a> |
| SN74HC259NE4     | ACTIVE        | PDIP         | N                  | 16   | 25             | Pb-Free<br>(RoHS)          | CU NIPDAU        | N / A for Pkg Type   | -40 to 85    | SN74HC259N                    | <a href="#">Samples</a> |

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5)       | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|------------------|----------------------|--------------|-------------------------------|-------------------------|
| SN74HC259NSR     | ACTIVE        | SO           | NS                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259NSRE4   | ACTIVE        | SO           | NS                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259NSRG4   | ACTIVE        | SO           | NS                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259PWLE    | OBSOLETE      | TSSOP        | PW                 | 16   |                | TBD                        | Call TI          | Call TI              | -40 to 85    |                               |                         |
| SN74HC259PWR     | ACTIVE        | TSSOP        | PW                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259PWRE4   | ACTIVE        | TSSOP        | PW                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259PWRG4   | ACTIVE        | TSSOP        | PW                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259PWT     | ACTIVE        | TSSOP        | PW                 | 16   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259PWTE4   | ACTIVE        | TSSOP        | PW                 | 16   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SN74HC259PWTG4   | ACTIVE        | TSSOP        | PW                 | 16   | 250            | Green (RoHS<br>& no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM   | -40 to 85    | HC259                         | <a href="#">Samples</a> |
| SNJ54HC259FK     | ACTIVE        | LCCC         | FK                 | 20   | 1              | TBD                        | POST-PLATE       | N / A for Pkg Type   | -55 to 125   | 85519012A<br>SNJ54HC<br>259FK | <a href="#">Samples</a> |
| SNJ54HC259J      | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42              | N / A for Pkg Type   | -55 to 125   | 8551901EA<br>SNJ54HC259J      | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

<sup>(4)</sup> There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

<sup>(5)</sup> Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "--" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

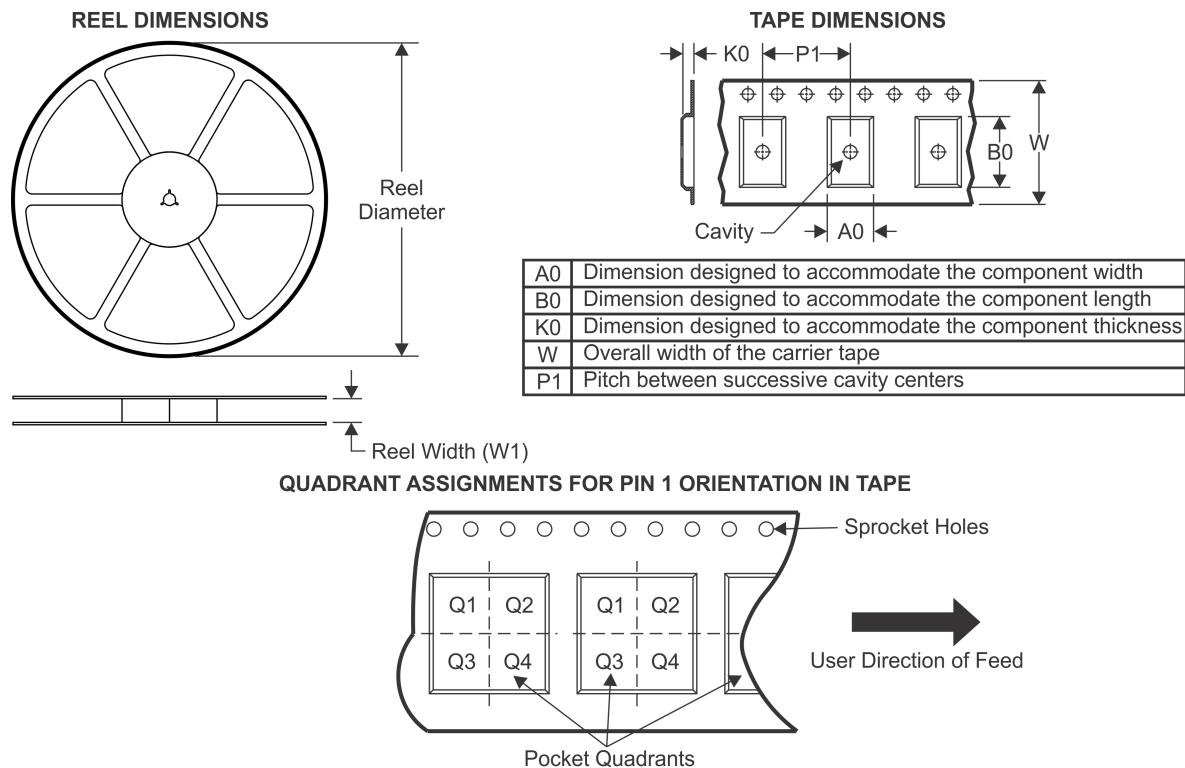
**OTHER QUALIFIED VERSIONS OF SN54HC259, SN74HC259 :**

● Catalog: [SN74HC259](#)

● Military: [SN54HC259](#)

NOTE: Qualified Version Definitions:

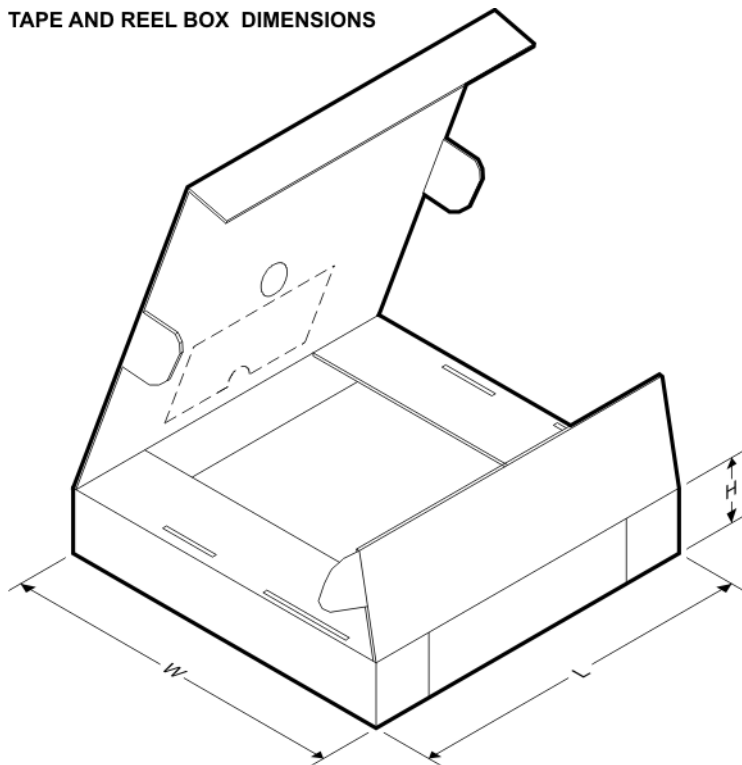
- Catalog - TI's standard catalog product
- Military - QML certified for Military and Defense Applications

**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|---------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74HC259DR   | SOIC         | D               | 16   | 2500 | 330.0              | 16.8               | 6.5     | 10.3    | 2.1     | 8.0     | 16.0   | Q1            |
| SN74HC259DRG4 | SOIC         | D               | 16   | 2500 | 330.0              | 16.4               | 6.5     | 10.3    | 2.1     | 8.0     | 16.0   | Q1            |
| SN74HC259NSR  | SO           | NS              | 16   | 2000 | 330.0              | 16.4               | 8.2     | 10.5    | 2.5     | 12.0    | 16.0   | Q1            |
| SN74HC259PWR  | TSSOP        | PW              | 16   | 2000 | 330.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |
| SN74HC259PWT  | TSSOP        | PW              | 16   | 250  | 330.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS



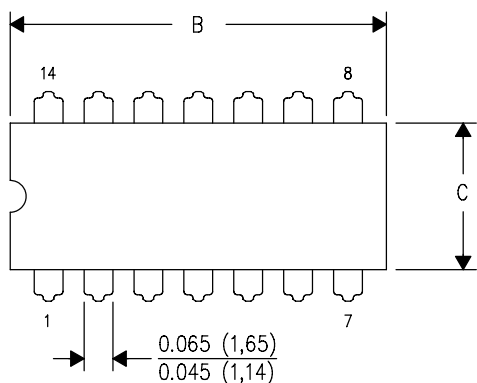
\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|---------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74HC259DR   | SOIC         | D               | 16   | 2500 | 364.0       | 364.0      | 27.0        |
| SN74HC259DRG4 | SOIC         | D               | 16   | 2500 | 333.2       | 345.9      | 28.6        |
| SN74HC259NSR  | SO           | NS              | 16   | 2000 | 367.0       | 367.0      | 38.0        |
| SN74HC259PWR  | TSSOP        | PW              | 16   | 2000 | 367.0       | 367.0      | 35.0        |
| SN74HC259PWT  | TSSOP        | PW              | 16   | 250  | 367.0       | 367.0      | 35.0        |

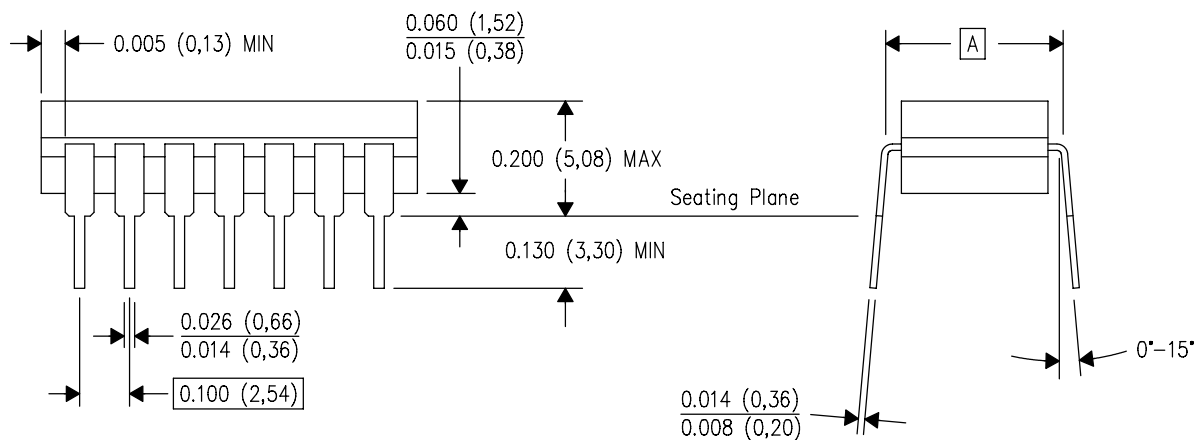
J (R-GDIP-T\*\*)

14 LEADS SHOWN

# CERAMIC DUAL IN-LINE PACKAGE



| PINS **<br>DIM | 14                     | 16                     | 18                     | 20                     |
|----------------|------------------------|------------------------|------------------------|------------------------|
| A              | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC |
| B MAX          | 0.785<br>(19,94)       | .840<br>(21,34)        | 0.960<br>(24,38)       | 1.060<br>(26,92)       |
| B MIN          | —                      | —                      | —                      | —                      |
| C MAX          | 0.300<br>(7,62)        | 0.300<br>(7,62)        | 0.310<br>(7,87)        | 0.300<br>(7,62)        |
| C MIN          | 0.245<br>(6,22)        | 0.245<br>(6,22)        | 0.220<br>(5,59)        | 0.245<br>(6,22)        |

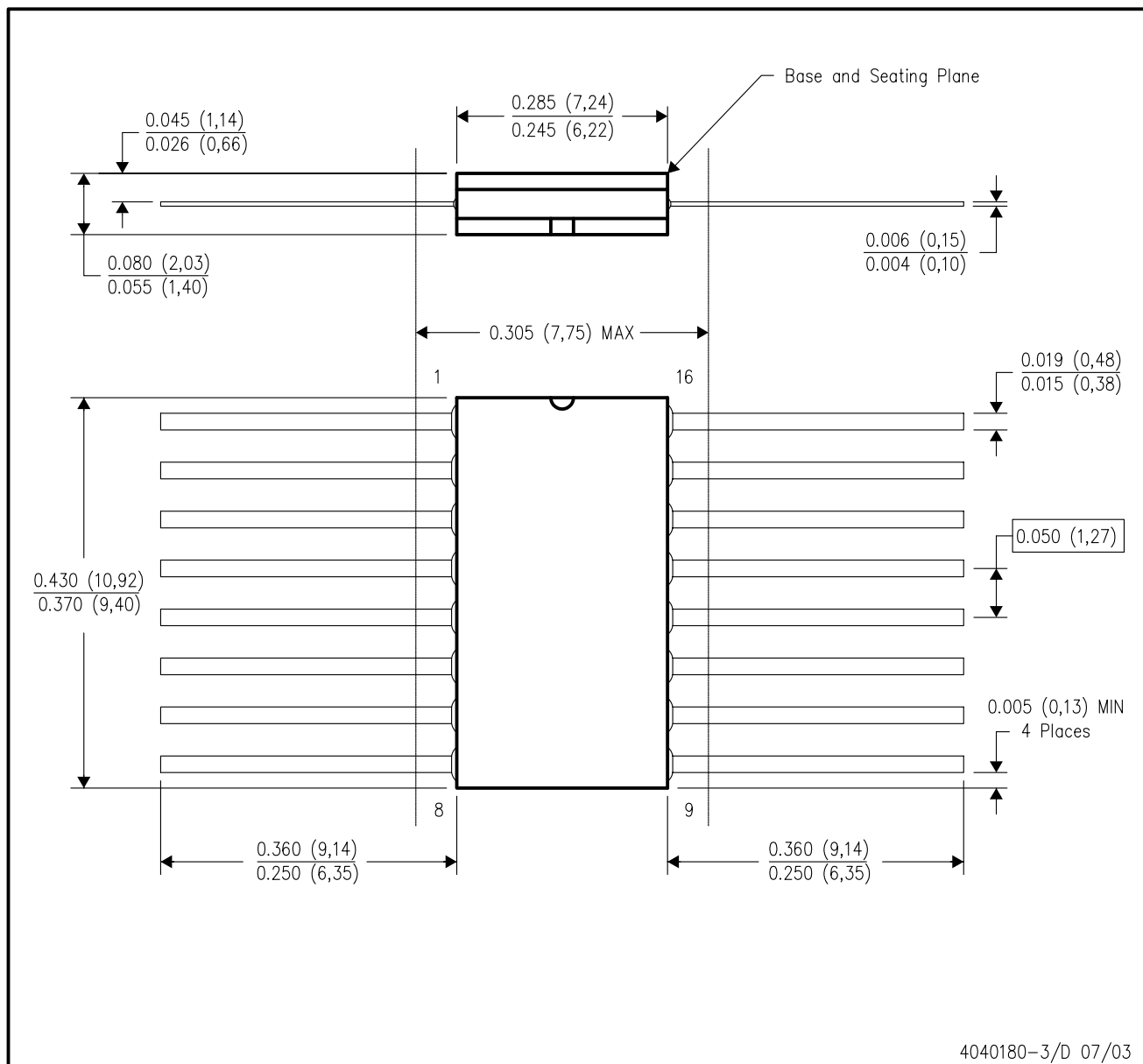


4040083/F 03/03

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package is hermetically sealed with a ceramic lid using glass frit.
  - D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
  - E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK

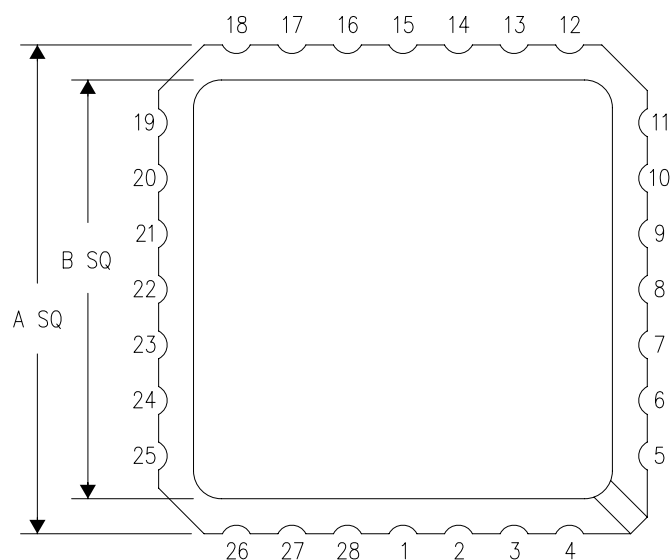


- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a ceramic lid using glass frit.
  - Index point is provided on cap for terminal identification only.
  - Falls within MIL STD 1835 GDFP1-F16 and JEDEC MO-092AC

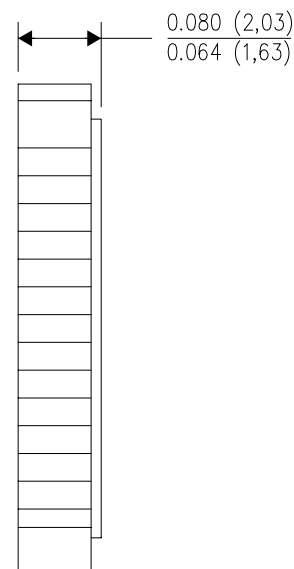
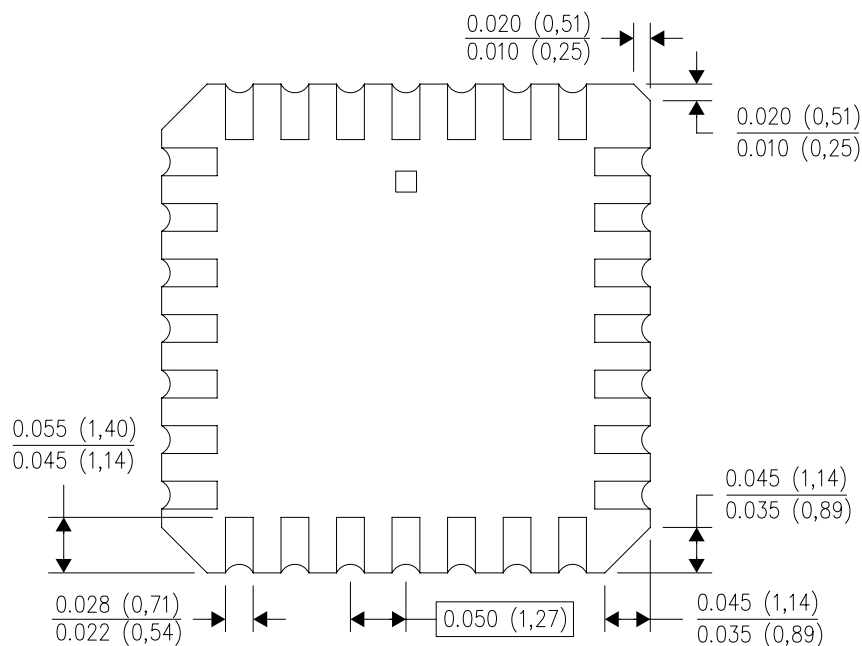
FK (S-CQCC-N\*\*)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



| NO. OF<br>TERMINALS<br>** | A                |                  | B                |                  |
|---------------------------|------------------|------------------|------------------|------------------|
|                           | MIN              | MAX              | MIN              | MAX              |
| 20                        | 0.342<br>(8,69)  | 0.358<br>(9,09)  | 0.307<br>(7,80)  | 0.358<br>(9,09)  |
| 28                        | 0.442<br>(11,23) | 0.458<br>(11,63) | 0.406<br>(10,31) | 0.458<br>(11,63) |
| 44                        | 0.640<br>(16,26) | 0.660<br>(16,76) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 52                        | 0.740<br>(18,78) | 0.761<br>(19,32) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 68                        | 0.938<br>(23,83) | 0.962<br>(24,43) | 0.850<br>(21,6)  | 0.858<br>(21,8)  |
| 84                        | 1.141<br>(28,99) | 1.165<br>(29,59) | 1.047<br>(26,6)  | 1.063<br>(27,0)  |



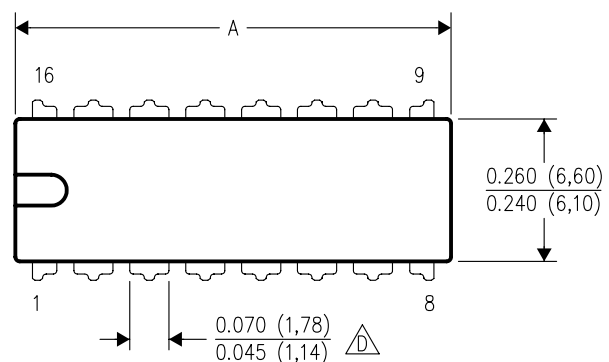
4040140/D 01/11

- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a metal lid.
  - Falls within JEDEC MS-004

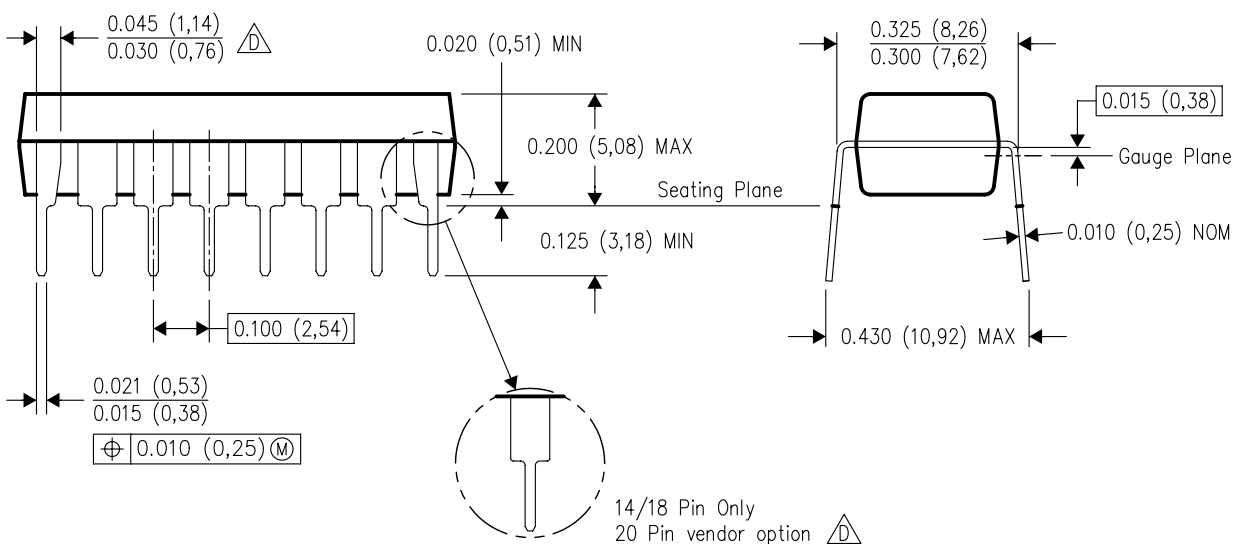
N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



| PINS **<br>DIM      | 14               | 16               | 18               | 20               |
|---------------------|------------------|------------------|------------------|------------------|
| A MAX               | 0.775<br>(19,69) | 0.775<br>(19,69) | 0.920<br>(23,37) | 1.060<br>(26,92) |
| A MIN               | 0.745<br>(18,92) | 0.745<br>(18,92) | 0.850<br>(21,59) | 0.940<br>(23,88) |
| MS-001<br>VARIATION | AA               | BB               | AC               | AD               |



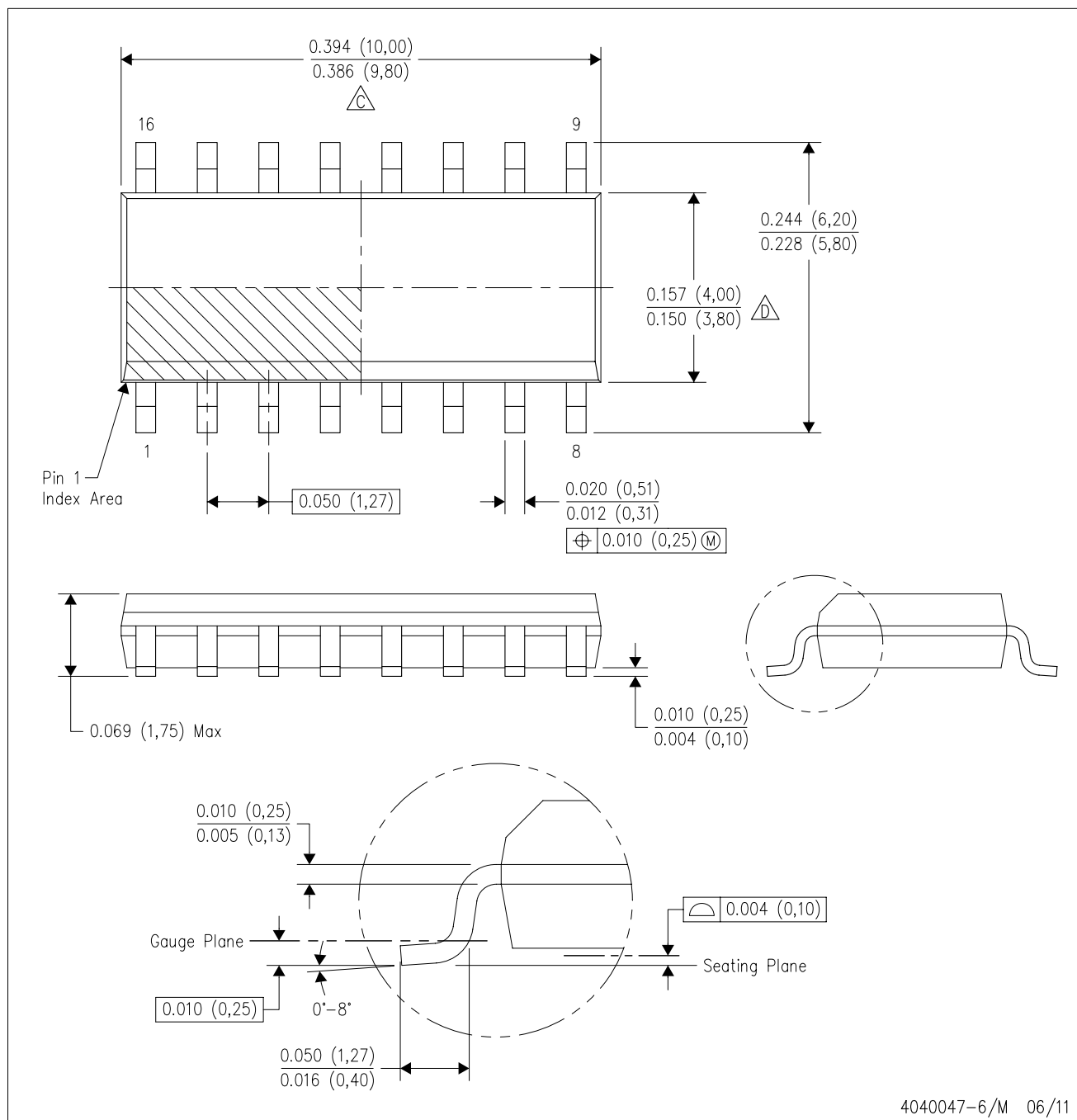
4040049/E 12/2002

NOTES:

- A. All linear dimensions are in inches (millimeters).  
B. This drawing is subject to change without notice.
-  Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).  
 The 20 pin end lead shoulder width is a vendor option, either half or full width.

D (R-PDSO-G16)

PLASTIC SMALL OUTLINE

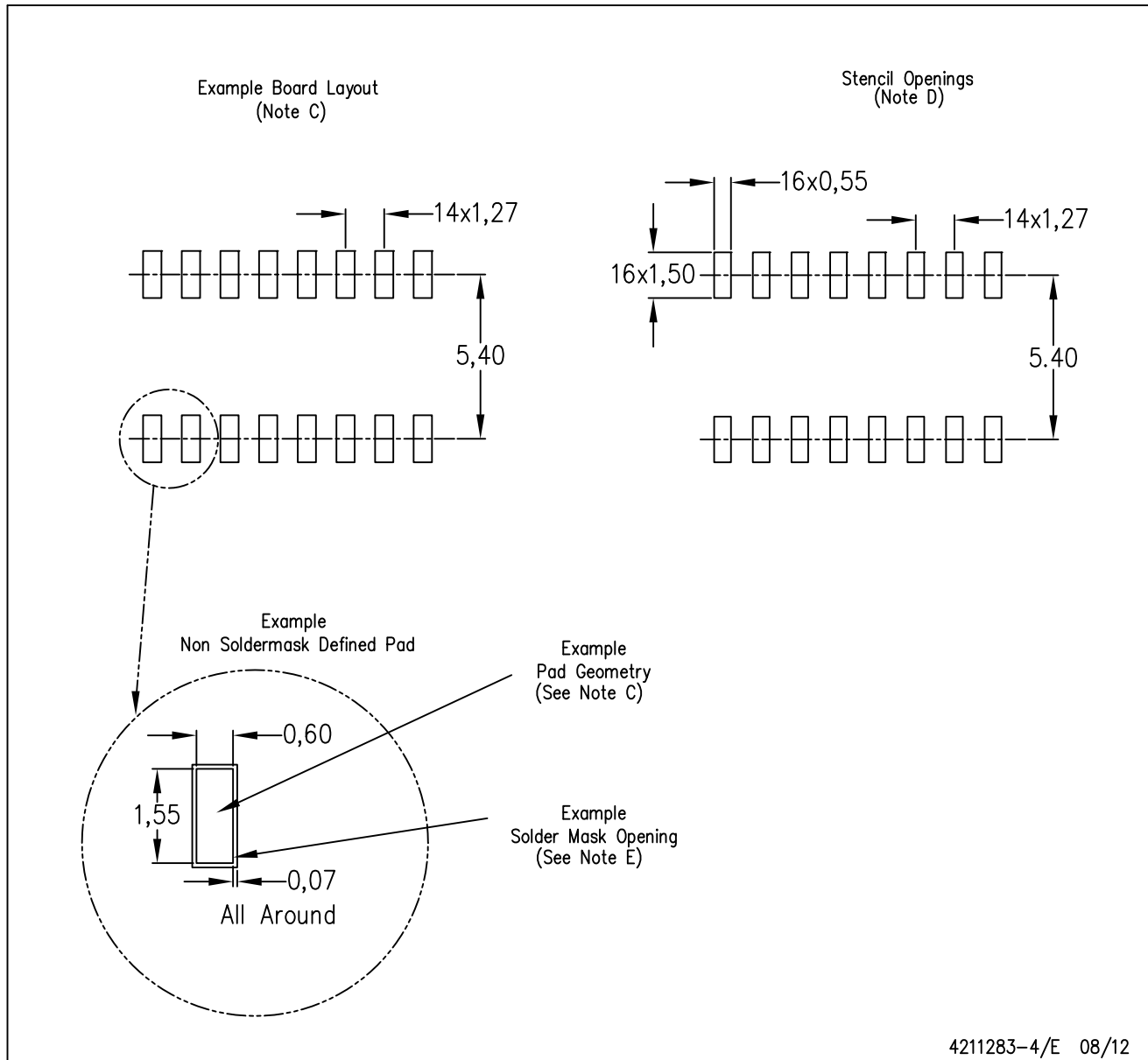


## NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- D. Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AC.

D (R-PDSO-G16)

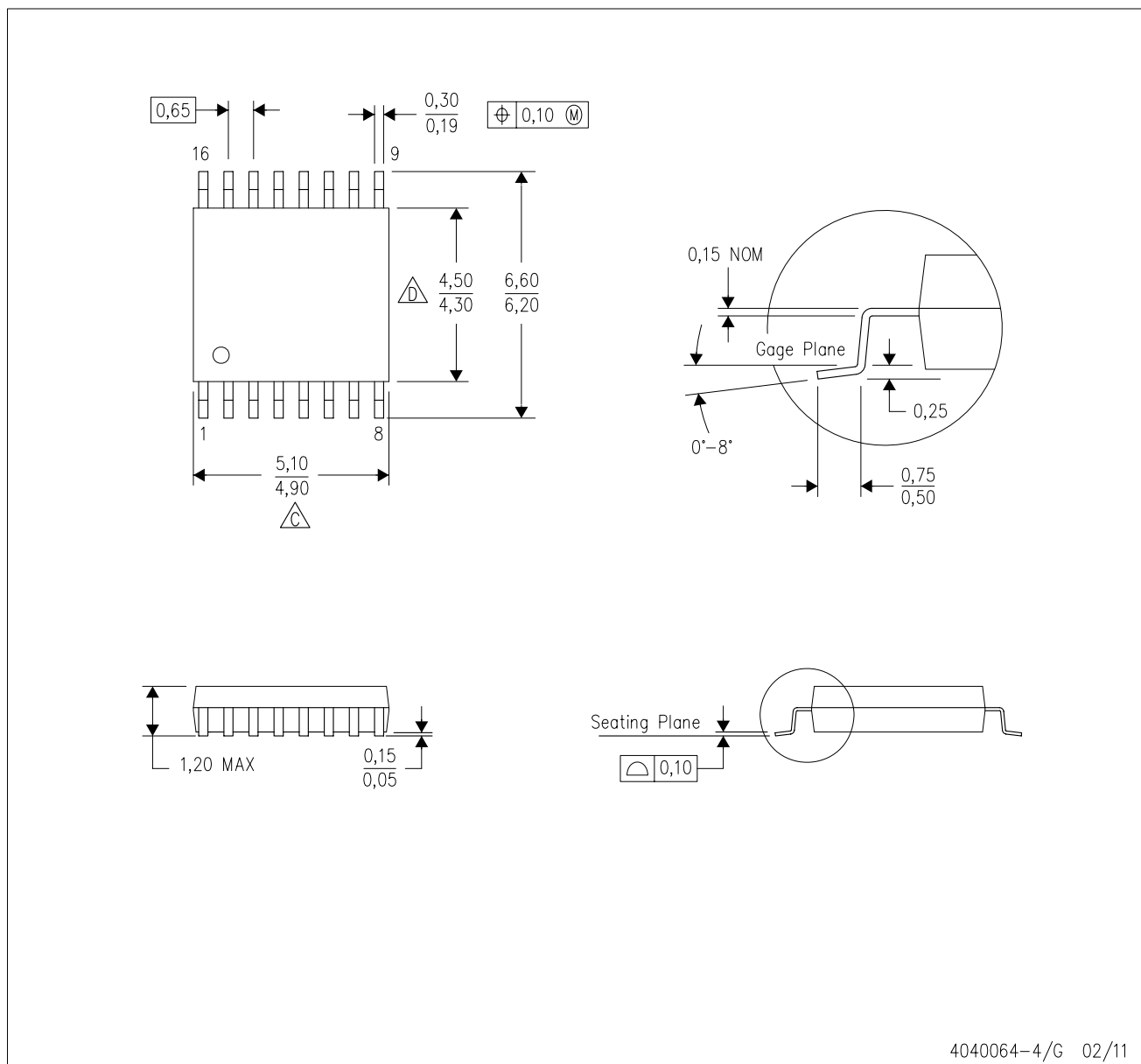
PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Publication IPC-7351 is recommended for alternate designs.
  - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

PW (R-PDSO-G16)

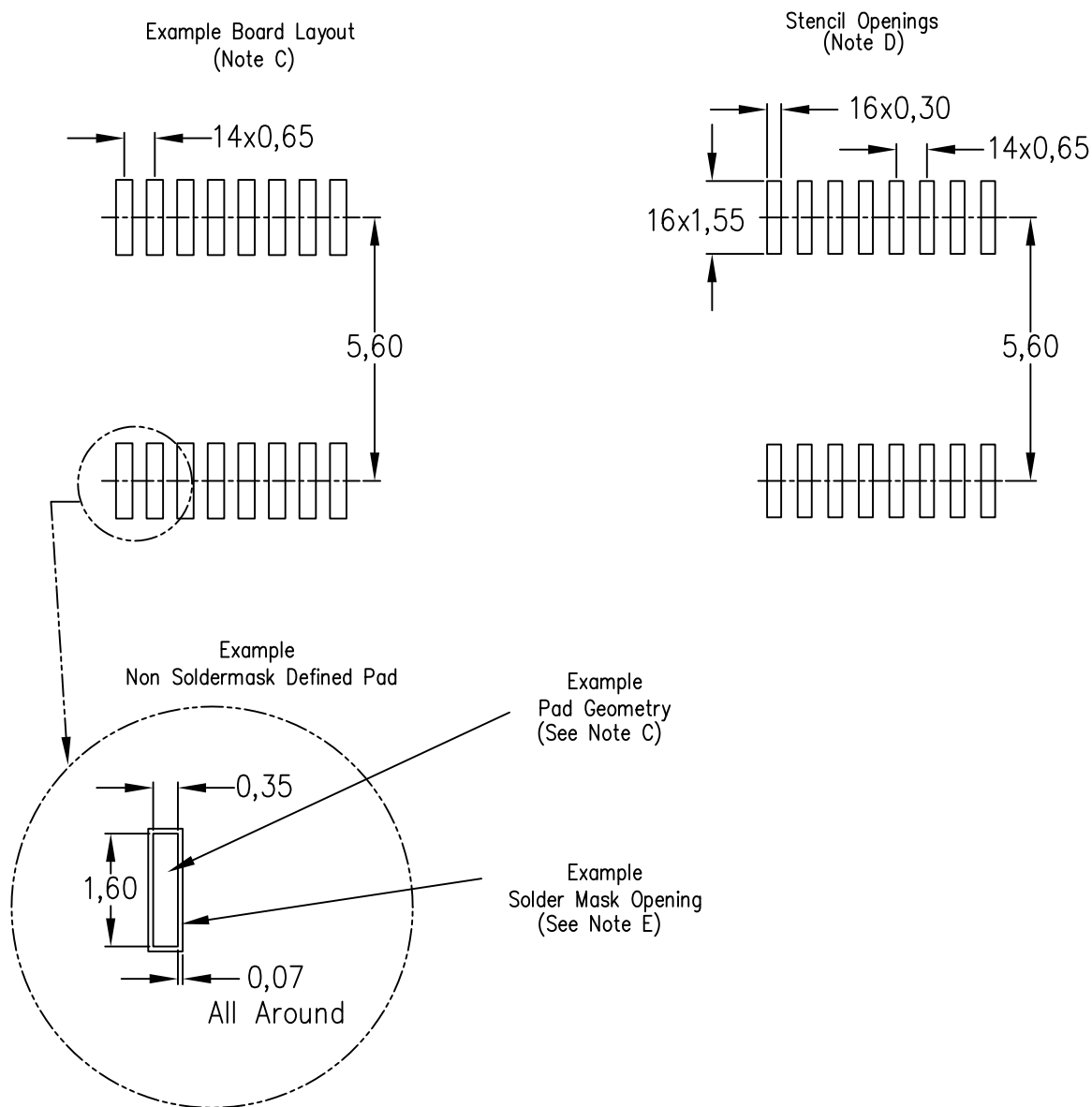
PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
  - B. This drawing is subject to change without notice.
  - $\Delta$  C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
  - $\Delta$  D. Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
  - E. Falls within JEDEC MO-153

PW (R-PDSO-G16)

PLASTIC SMALL OUTLINE



4211284-3/F 12/12

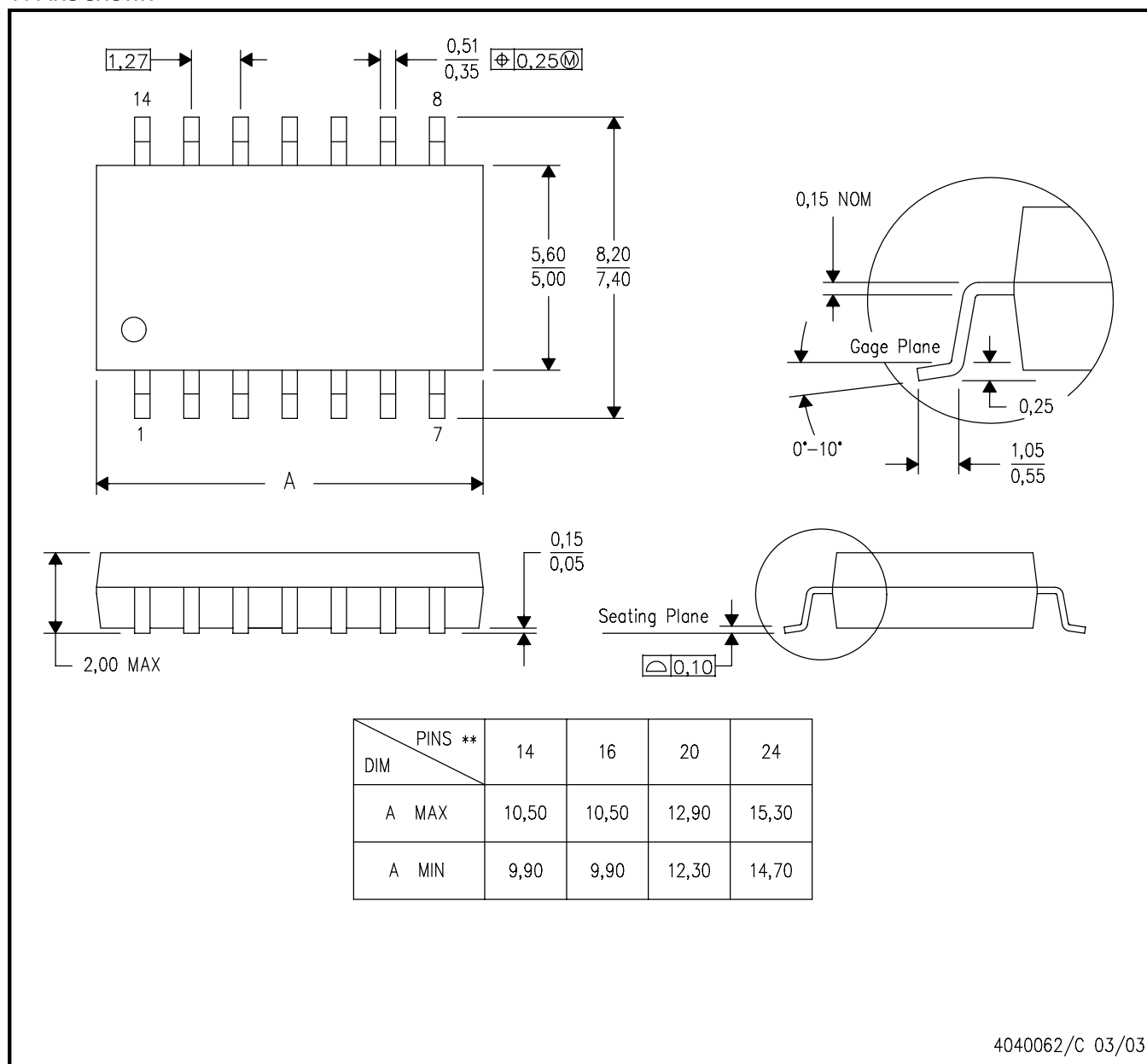
- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Publication IPC-7351 is recommended for alternate designs.
  - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

### Products

|                              |                                                                                      |
|------------------------------|--------------------------------------------------------------------------------------|
| Audio                        | <a href="http://www.ti.com/audio">www.ti.com/audio</a>                               |
| Amplifiers                   | <a href="http://amplifier.ti.com">amplifier.ti.com</a>                               |
| Data Converters              | <a href="http://dataconverter.ti.com">dataconverter.ti.com</a>                       |
| DLP® Products                | <a href="http://www.dlp.com">www.dlp.com</a>                                         |
| DSP                          | <a href="http://dsp.ti.com">dsp.ti.com</a>                                           |
| Clocks and Timers            | <a href="http://www.ti.com/clocks">www.ti.com/clocks</a>                             |
| Interface                    | <a href="http://interface.ti.com">interface.ti.com</a>                               |
| Logic                        | <a href="http://logic.ti.com">logic.ti.com</a>                                       |
| Power Mgmt                   | <a href="http://power.ti.com">power.ti.com</a>                                       |
| Microcontrollers             | <a href="http://microcontroller.ti.com">microcontroller.ti.com</a>                   |
| RFID                         | <a href="http://www.ti-rfid.com">www.ti-rfid.com</a>                                 |
| OMAP Applications Processors | <a href="http://www.ti.com/omap">www.ti.com/omap</a>                                 |
| Wireless Connectivity        | <a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a> |

### Applications

|                               |                                                                                          |
|-------------------------------|------------------------------------------------------------------------------------------|
| Automotive and Transportation | <a href="http://www.ti.com/automotive">www.ti.com/automotive</a>                         |
| Communications and Telecom    | <a href="http://www.ti.com/communications">www.ti.com/communications</a>                 |
| Computers and Peripherals     | <a href="http://www.ti.com/computers">www.ti.com/computers</a>                           |
| Consumer Electronics          | <a href="http://www.ti.com/consumer-apps">www.ti.com/consumer-apps</a>                   |
| Energy and Lighting           | <a href="http://www.ti.com/energy">www.ti.com/energy</a>                                 |
| Industrial                    | <a href="http://www.ti.com/industrial">www.ti.com/industrial</a>                         |
| Medical                       | <a href="http://www.ti.com/medical">www.ti.com/medical</a>                               |
| Security                      | <a href="http://www.ti.com/security">www.ti.com/security</a>                             |
| Space, Avionics and Defense   | <a href="http://www.ti.com/space-avionics-defense">www.ti.com/space-avionics-defense</a> |
| Video and Imaging             | <a href="http://www.ti.com/video">www.ti.com/video</a>                                   |

### TI E2E Community

[e2e.ti.com](http://e2e.ti.com)