



QUAD PRECISION CMOS VOLTAGE COMPARATOR WITH OPEN DRAIN DRIVER

GENERAL DESCRIPTION

The ALD4301A/ALD4301 is a monolithic high performance quad voltage comparator built with advanced silicon gate CMOS technology. It features very high typical input impedance of $10^{12}\Omega$; low input bias current of 10pA; fast response time of 300ns; very low power dissipation of 55 μ A per comparator; high output drive and single +5V or dual \pm 5V power supply operation.

The input voltage range includes ground, making this comparator ideal for low level signal detection with high source impedance. The outputs are open-drain configuration, allowing maximum application flexibility. The outputs can be connected to a higher external voltage than V+ and used in a wired-OR connection with other open drain circuits such as ALD2301/ALD2303. They can also be used with push-pull output types such as ALD2302/ALD4302 voltage comparators simultaneously with open drain comparators using a common V+. The ALD4301A/ALD4301 is ideal for a great variety of voltage comparator applications, especially detection circuits requiring very low input currents and low standby power, yet retaining high output current capability.

FEATURES

- Fanout of 30 LS TTL loads
- Low supply current of 55 μ A/comparator typical
- Pinout/Functional equivalent to LM393 types
- Extremely low input bias currents -- 10pA typical
- Virtually eliminates source impedance effects
- Low operating supply voltage of 3V to 12V
- Single +5V and dual supply \pm 5V operation
- High speed for both large signal and low level signals -- 300ns typical for TTL inputs
- CMOS, NMOS and TTL compatible
- Wired-OR open drain outputs
- High output sinking current -- 60mA typical
- Low supply current spikes
- High gain

ORDERING INFORMATION

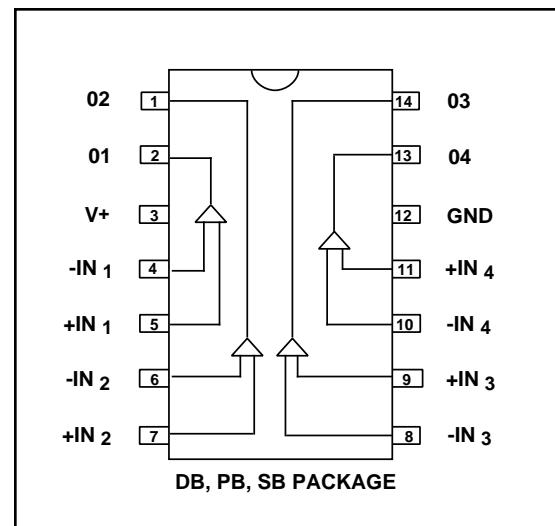
Operating Temperature Range *		
-55°C to +125°C	0°C to +70°C	0°C to +70°C
14-Pin CERDIP Package	14-Pin Small Outline Package (SOIC)	14-Pin Plastic Dip Package
ALD 4301ADB ALD 4301DB	ALD4301ASB ALD4301SB	ALD4301APB ALD4301PB

* Contact factory for industrial temperature range

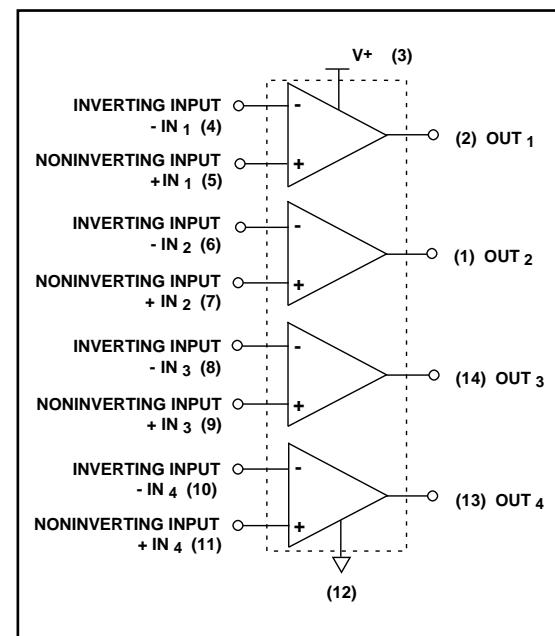
APPLICATIONS

- High source impedance voltage comparison circuits
- Dual/Quad limit window comparator
- Power supply voltage monitor
- Photo-detector sensor circuit
- Relay or LED driver
- Oscillators
- Battery operated instruments
- Remote signal detection

PIN CONFIGURATION



BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Supply voltage, V+	13.2V
Differential input voltage range	-0.3V to V+ +0.3V
Power dissipation	600 mW
Operating temperature range	0°C to +70°C
PB, SB package	0°C to +70°C
DB package	-55°C to +125°C
Storage temperature range	-65°C to +150°C
Lead temperature, 10 seconds	+260°C

OPERATING ELECTRICAL CHARACTERISTICS

TA = 25°C V+= +5V unless otherwise specified

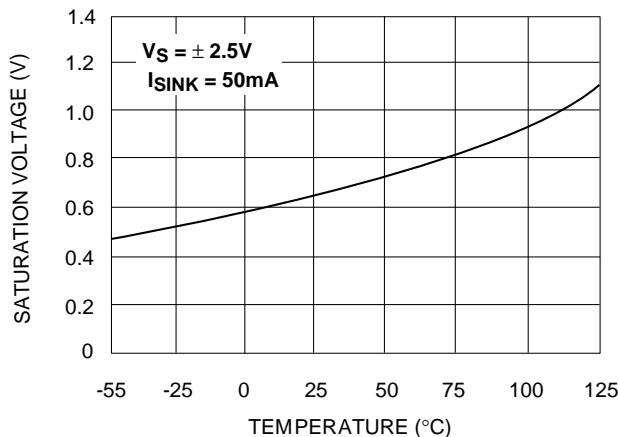
Parameter	Symbol	4301A			4301			Unit	Test Conditions
		Min	Typ	Max	Min	Typ	Max		
Voltage Supply	V+	3 ±1.5		12 ±6	3 ±1.5		12 ±6	V V	Single supply Dual supplies
Supply Current	Is		220	350		220	350	µA	No load
Voltage Gain	AVD	50	150		50	150		V/mV	R _{LOAD} ≥ 15KΩ
Input Offset Voltage	V _{OS}			5			10	mV	R _{LOAD} ≥ 1.5KΩ
Input Offset Current ¹	I _{OS}		0.1	30		0.1	30	pA	
Input Bias Current ¹	I _B		0.1	30		0.1	30	pA	
Common Mode Input Voltage Range ²	V _{ICR}	-0.3		V+ -1.5	-0.3		V+ -1.5	V	
Low Level Output Voltage	V _{OL}		0.15	0.4		0.15	0.4	V	I _{SINK} = 12mA V _{INPUT} = 1V Differential
Low Level Output Current	I _{OL}	24	60		24	60		mA	V _{OL} = 1.0V
High Level Leakage Current	I _{LH}		0.1	20		0.1	20	nA	V _{OH} = 5.0 V
Response Time ²	t _{RP}		650			650		ns	R _L = 5.1KΩ C _L = 15pF 100mV Input Step/5mV Overdrive
			300			300		ns	R _L = 5.1KΩ C _L = 15pF TTL- Level Input Step

Notes: ¹ Consists of junction leakage currents

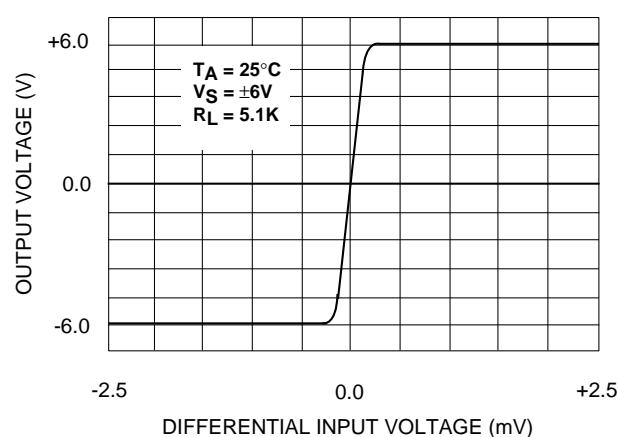
² Sample tested parameters

TYPICAL PERFORMANCE CHARACTERISTICS

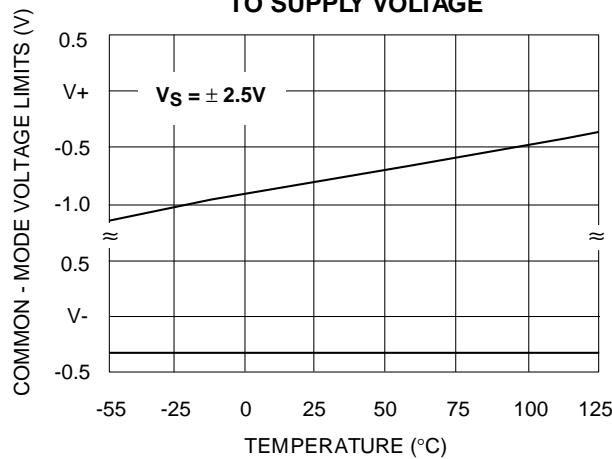
**SATURATION VOLTAGE
vs.TEMPERATURE**



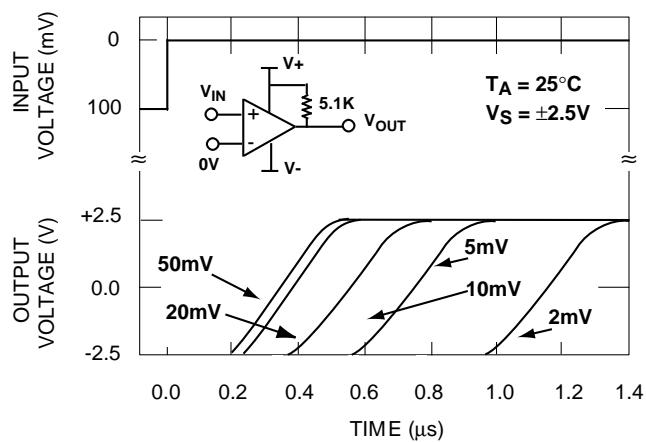
TRANSFER FUNCTION



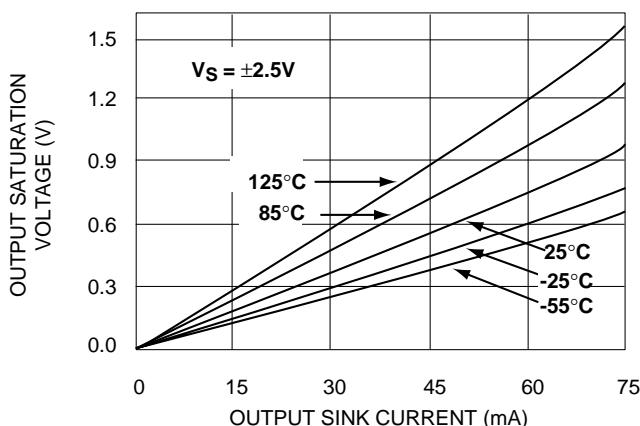
**COMMON - MODE VOLTAGE REFERRED
TO SUPPLY VOLTAGE**



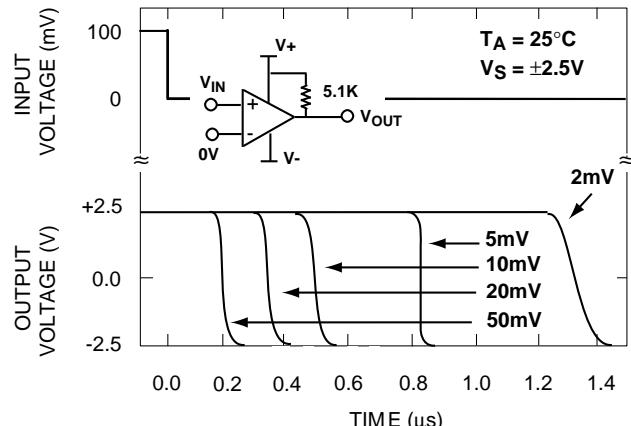
**RESPONSE TIME FOR VARIOUS
INPUT OVERDRIVES**



**SATURATION VOLTAGE vs.
SINK CURRENT**

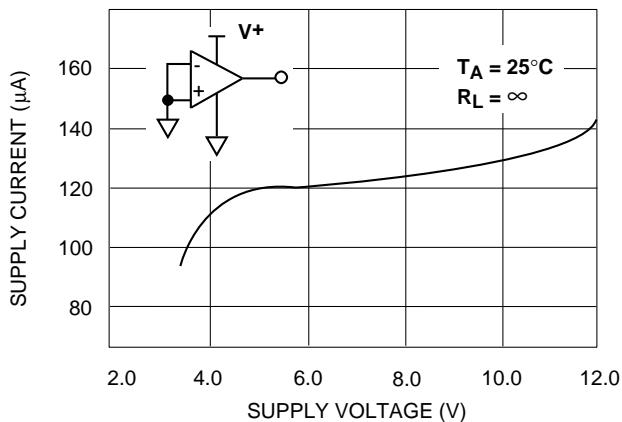


**RESPONSE TIME FOR VARIOUS
INPUT OVERDRIVES**

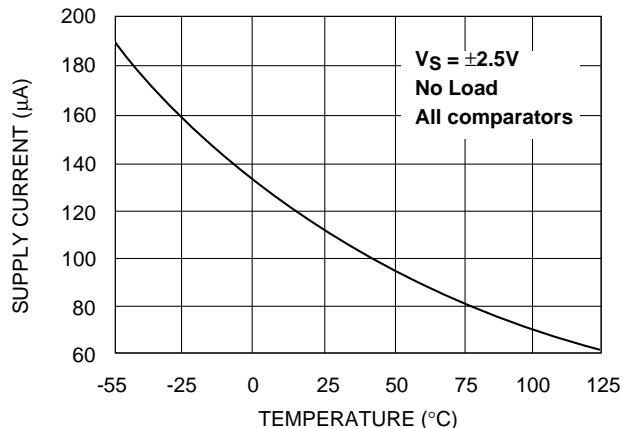


TYPICAL PERFORMANCE CHARACTERISTICS

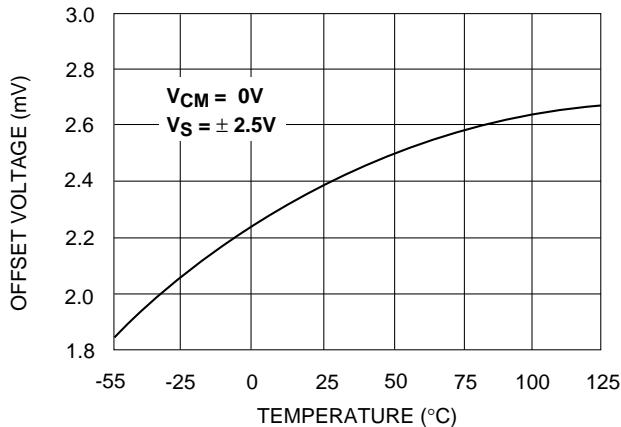
**TOTAL SUPPLY CURRENT vs.
TOTAL SUPPLY VOLTAGE**



SUPPLY CURRENT vs. TEMPERATURE

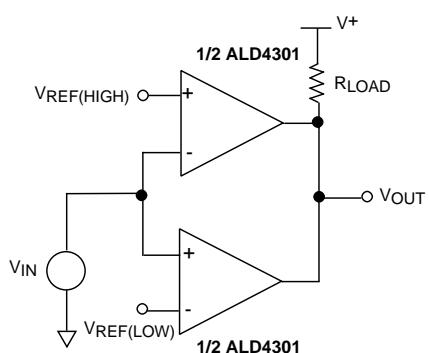


**INPUT OFFSET VOLTAGE vs.
TEMPERATURE**



TYPICAL APPLICATIONS

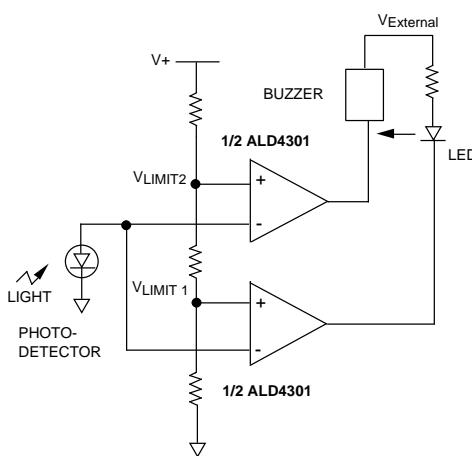
QUAD LIMIT WINDOW COMPARATOR



MINIMUM R_{LOAD} = 1.5kΩ

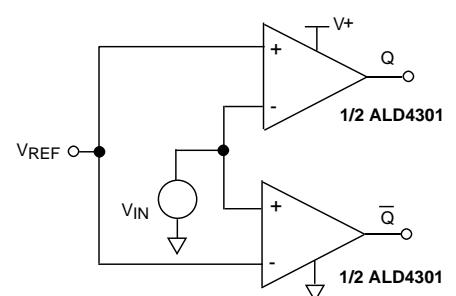
OUTPUT HIGH FOR V_{IN} < V_{REF(HIGH)}
AND V_{IN} > V_{REF(LOW)}

**QUAD LIMIT PHOTO-DETECTOR
MONITOR**



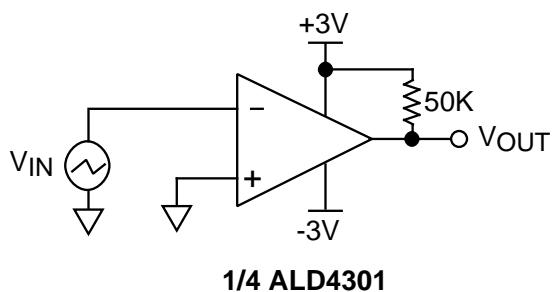
LED turns on as photo-detector voltage reaches V_{LIMIT1}. Both LED and horn turns on as photo-detector voltage reaches V_{LIMIT2}.
V_{EXTERNAL} = +12V V₊ = +5V.

**VOLTAGE COMPARATOR WITH
COMPLEMENTARY OUTPUTS**

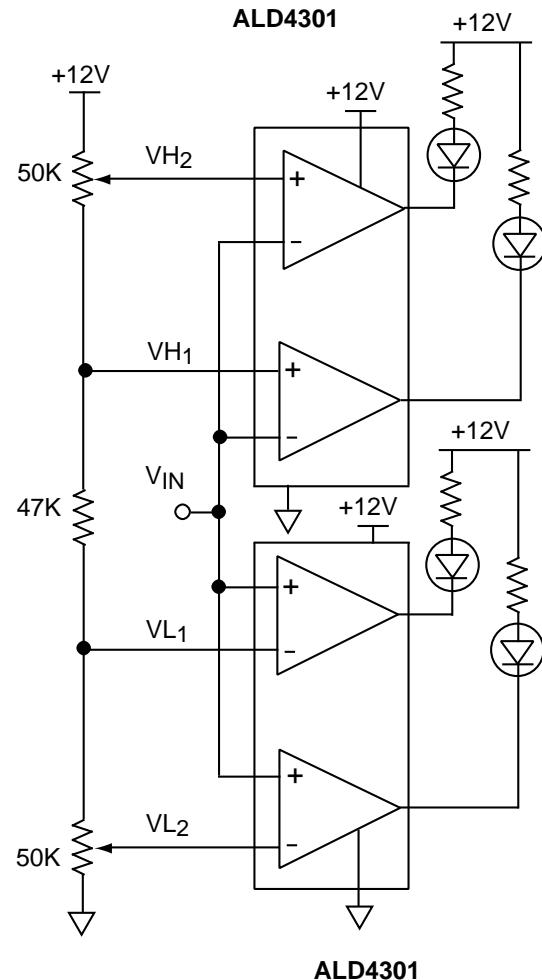


TYPICAL APPLICATIONS

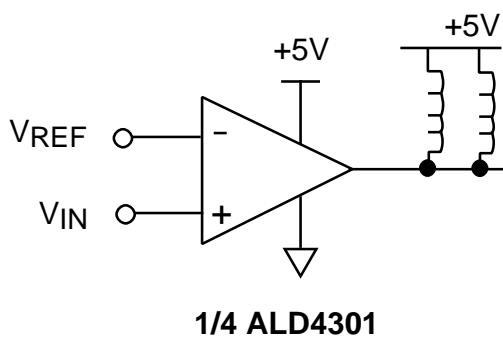
ZERO CROSSING DETECTOR



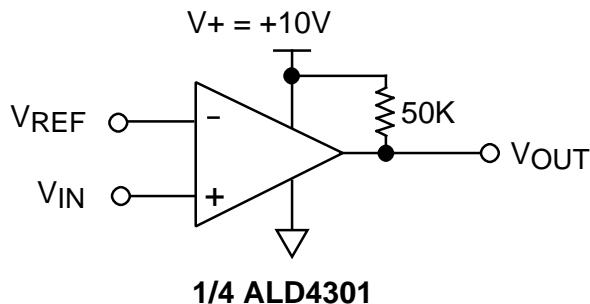
DOUBLE DUAL LIMIT WINDOW COMPARATOR



MULTIPLE RELAY DRIVE



VOLTAGE LEVEL TRANSLATOR



$V_{REF} = 1.4V$ for TTL input

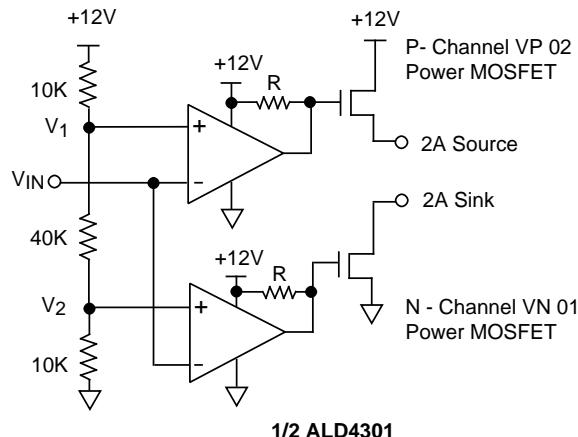
$$V_{REF} = \frac{V^+}{2} \text{ for CMOS input}$$

Output V_{OUT} swings from rail-to-rail

VL₁ and VH₁ first limit window send warning
 VL₂ and VH₂ second limit window execute system cutoff

TYPICAL APPLICATIONS

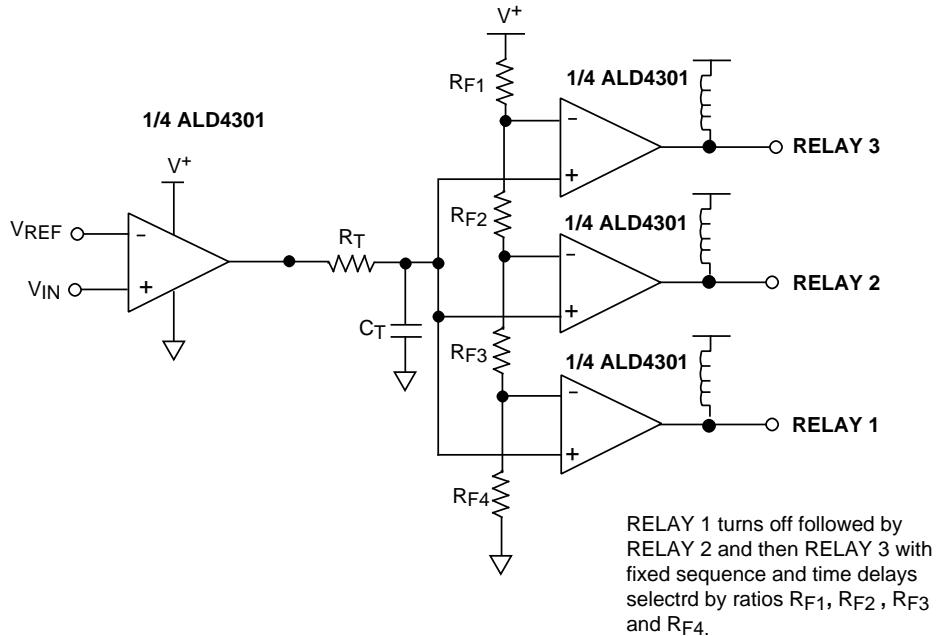
PUSH-PULL COMPLEMENTARY POWER MOSFET DRIVER



$R \geq 50 \text{ k}\Omega$

This circuit eliminates crossover current in the complementary power transistors. The outputs can be used to source and sink different loads or tied together to provide push-pull drive of the same load.

TIME DELAY GENERATOR



Design & Operating Notes:

1. In order to minimize stray oscillation, all unused inputs must be tied to ground.
2. The input bias and offset currents are essentially input protection diode reverse bias leakage currents, and are typically less than 1 pA at room temperature. These currents are a function of ambient temperature, and would have to be considered in applications where very high source impedance or high accuracy are involved.
3. The high output sinking current of 60mA for each output offers flexibility in many applications, as a separate buffer or driver would not be necessary to drive the intended load. However, as the circuit normally operates close to ambient temperature due to its very low power consumption, thermal effects caused by large output current transients must be considered in certain applications.