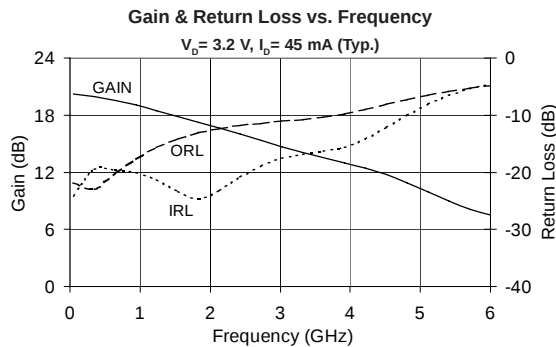




Product Description

The SGA-4463 is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring 1 micron emitters provides high F_T and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only 2 DC-blocking capacitors, a bias resistor and an optional RF choke are required for operation.

The matte tin finish on Sirenza's lead-free package utilizes a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. This package is also manufactured with green molding compounds that contain no anti-mony trioxide nor halogenated fire retardants.



SGA-4463

SGA-4463Z  RoHS Compliant & Green Package

**DC-3500 MHz, Cascadable
SiGe HBT MMIC Amplifier**



Product Features

- Now available in Lead Free, RoHS Compliant, & Green Packaging
- High Gain : 17 dB at 1950 MHz
- Cascadable 50 Ohm
- Operates From Single Supply
- Low Thermal Resistance Package

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

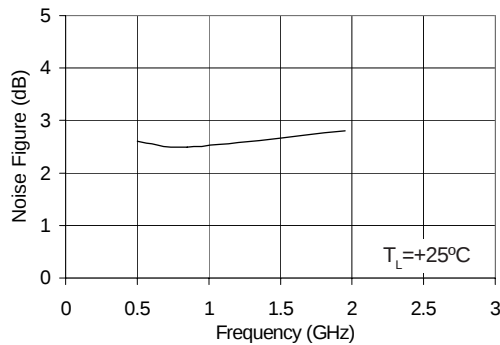
Symbol	Parameter	Units	Frequency	Min.	Typ.	Max.
G	Small Signal Gain	dB	850 MHz 1950 MHz 2400 Mhz	17.5	19.0 17.0 16.0	21.0
P_{1dB}	Output Power at 1dB Compression	dBm	850 MHz 1950 MHz		14.0 12.3	
OIP_3	Output Third Order Intercept Point	dBm	850 MHz 1950 MHz		27.0 24.8	
Bandwidth	Determined by Return Loss (>10dB)	MHz			3500	
IRL	Input Return Loss	dB	1950 MHz		24.4	
ORL	Output Return Loss	dB	1950 MHz		12.8	
NF	Noise Figure	dB	1950 MHz		2.8	
V_D	Device Operating Voltage	V		2.9	3.2	3.5
I_D	Device Operating Current	mA		41	45	49
R_{TH} , j-l	Thermal Resistance (junction to lead)	$^{\circ}\text{C/W}$			255	
Test Conditions:			$V_S = 8 \text{ V}$ $R_{BIAS} = 110 \text{ Ohms}$ $I_D = 45 \text{ mA Typ.}$ $T_L = 25^{\circ}\text{C}$			
			OIP_3 Tone Spacing = 1 MHz, P_{out} per tone = -5 dBm $Z_S = Z_L = 50 \text{ Ohms}$			

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Typical RF Performance at Key Operating Frequencies

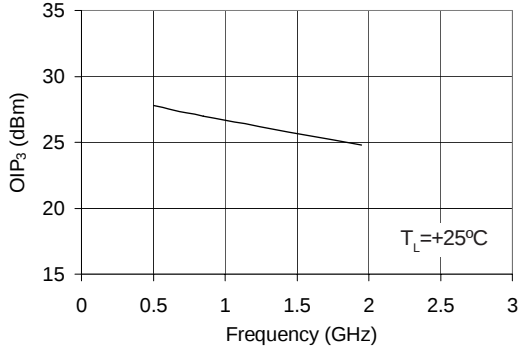
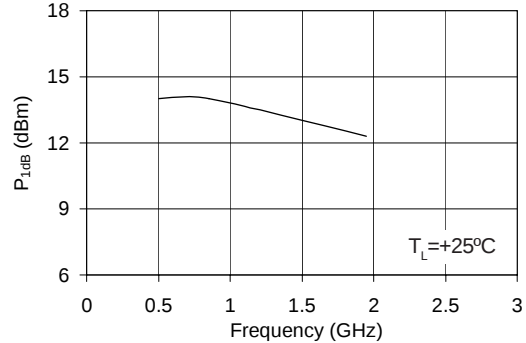
Symbol	Parameter	Unit	Frequency (MHz)					
			100	500	850	1950	2400	3500
G	Small Signal Gain	dB	20.2	19.8	19.0	17.0	16.0	13.7
OIP ₃	Output Third Order Intercept Point	dBm		27.8	27.0	24.8		
P _{1dB}	Output Power at 1dB Compression	dBm		14.0	14.0	12.3		
IRL	Input Return Loss	dB	22.5	19.5	19.9	24.4	21.1	16.5
ORL	Output Return Loss	dB	22.8	21.7	18.4	12.8	11.9	10.5
S ₁₂	Reverse Isolation	dB	23.4	22.8	23.0	22.2	21.7	19.8
NF	Noise Figure	dB		2.6	2.5	2.8		

Test Conditions: $V_S = 8\text{ V}$ $I_D = 45\text{ mA Typ.}$ OIP_3 Tone Spacing = 1 MHz, Pout per tone = -5 dBm
 $R_{BIAS} = 110\text{ Ohms}$ $T_L = 25^\circ\text{C}$ $Z_S = Z_L = 50\text{ Ohms}$

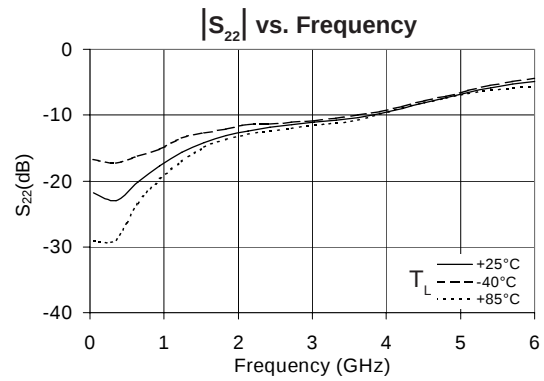
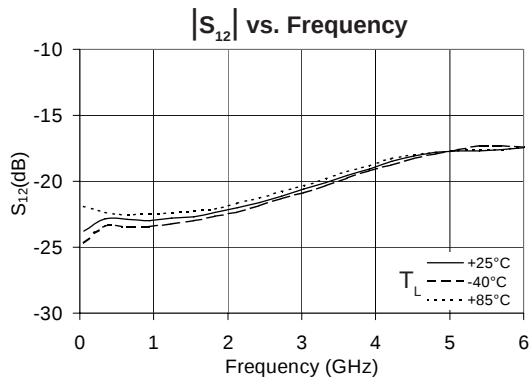
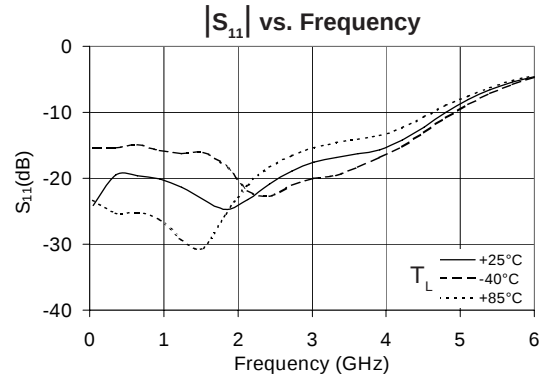
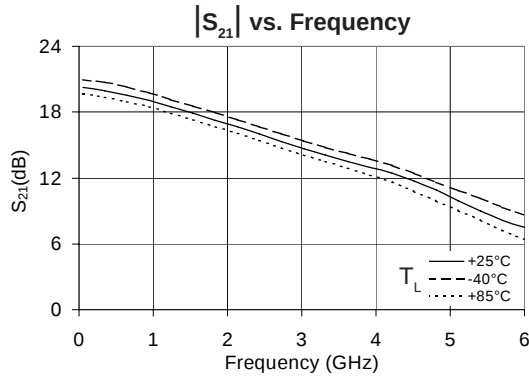
Noise Figure vs. Frequency
 $V_D = 3.2\text{ V}, I_D = 45\text{ mA (Typ.)}$

Absolute Maximum Ratings

Parameter	Absolute Limit
Max. Device Current (I_D)	90 mA
Max. Device Voltage (V_D)	5 V
Max. RF Input Power	+18 dBm
Max. Junction Temp. (T_J)	+150°C
Operating Temp. Range (T_L)	-40°C to +85°C
Max. Storage Temp.	+150°C

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.
 Bias conditions should also satisfy the following expression:
 $I_D V_D < (T_J - T_L) / R_{TH}$

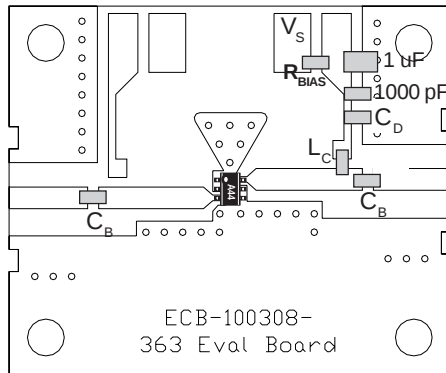
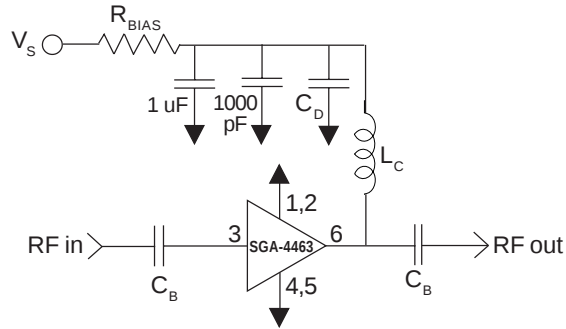
OIP₃ vs. Frequency
 $V_D = 3.2\text{ V}, I_D = 45\text{ mA (Typ.)}$

P_{1dB} vs. Frequency
 $V_D = 3.2\text{ V}, I_D = 45\text{ mA (Typ.)}$


Typical RF Performance Over Temperature (Bias: $V_D = 3.2$ V, $I_D = 45$ mA (Typ.))

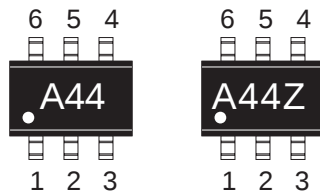


NOTE: Full S-parameter data available at www.sirenza.com

Basic Application Circuit



Part Identification Marking



Caution: ESD sensitive

Appropriate precautions in handling, packaging and testing devices must be observed.

Application Circuit Element Values

Reference Designator	Frequency (Mhz)				
	500	850	1950	2400	3500
C _B	220 pF	100 pF	68 pF	56 pF	39 pF
C _D	100 pF	68 pF	22 pF	22 pF	15 pF
L _C	68 nH	33 nH	22 nH	18 nH	15 nH

Recommended Bias Resistor Values for I_D=45mA

$$R_{BIAS} = (V_S - V_D) / I_D$$

Supply Voltage(V _S)	6 V	8 V	10 V	12 V
R _{BIAS}	62 Ω	110 Ω	150 Ω	200 Ω

Note: R_{BIAS} provides DC bias stability over temperature.

Mounting Instructions

1. Use a large ground pad area near device pins 1, 2, 4, and 5 with many plated through-holes as shown.
2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

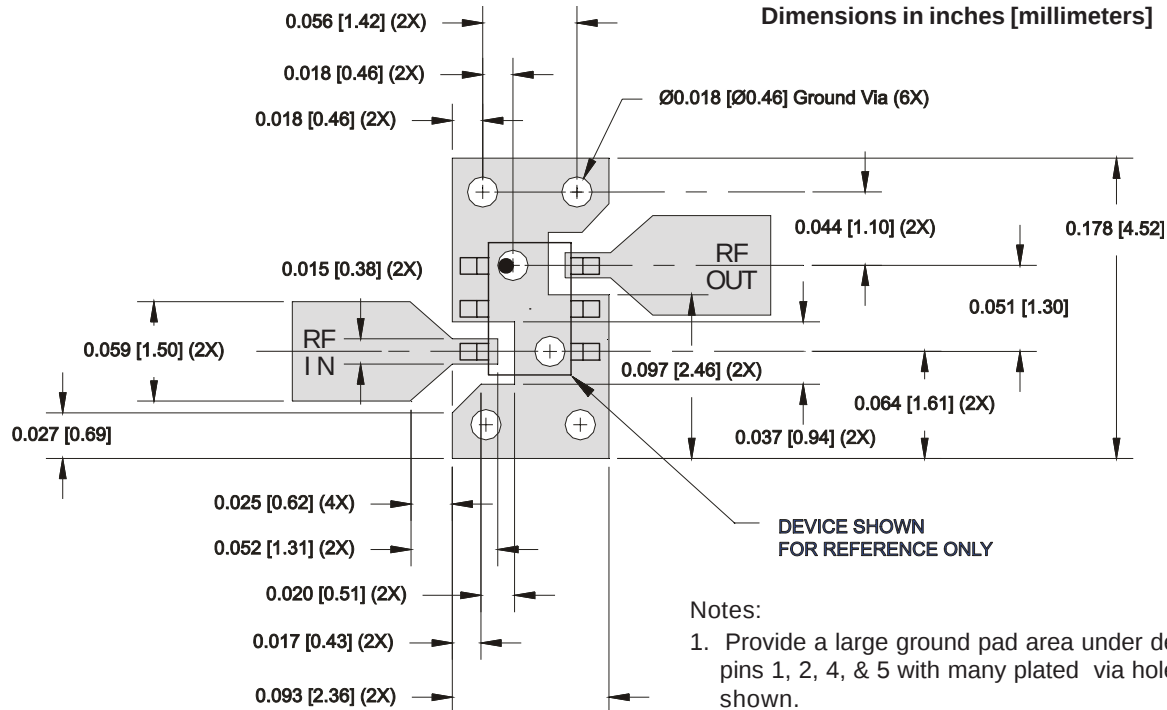
Pin #	Function	Description
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
1, 2, 4, 5	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
6	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

Part Number Ordering Information

Part Number	Reel Size	Devices/Reel
SGA-4463	7"	3000
SGA-4463Z	7"	3000

SOT-363 PCB Pad Layout

Dimensions in inches [millimeters]



Notes:

1. Provide a large ground pad area under device pins 1, 2, 4, & 5 with many plated via holes as shown.
2. Dimensions given for 50 Ohm RF I/O lines are for 31 mil thick Getek. Scale accordingly for different board thicknesses and dielectric constants.
3. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick Getek with 1 ounce copper on both sides.

SOT-363 Nominal Package Dimensions

Dimensions in inches [millimeters]

A link to the SOT-363 package outline drawing with full dimensions and tolerances may be found on the product web page at www.sirenza.com.

