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ADC1215S125F1

ADC1215S125F1 demo board; CMOS version; SPI, Regulators and CMOS buffer on board

IDT's ADC demonstration board is suitable for dynamic performance evaluations from low to high IF configuration. A Data acquisition board can be used to easily analyze the ADC performances in the design and prototyping phase.

Features

- Single power supply (on-board regulators)
- SMA connector for clock and analog inputs signal
- CMOS (buffered outputs)



Product Options

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Technical Documentation

	Title	Type	Format	File Size	Date
User Guides & Manuals					
	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

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ADC1215S125HN	Single 12-bit ADC 125 Msps with Input Buffer CMOS or LVDS DDR digital outputs

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